

Tuong Duy Do

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SUMMARY

Highly motivated and detail-oriented computer engineer with a strong foundation in computer science principles and programming languages. Possesses a solid understanding of hardware and software systems, coupled with a passion for solving complex technical challenges.

EDUCATION

San Diego State University

August 2018 - May 2024

- Bachelor of Science in Computer Engineering

Relevant Courses

- | | | |
|-------------------------------|-----------------------------|------------------------------|
| - Embedded System Programming | - Advance Programming | - Microprocessors |
| - Digital Systems | - Data Structures & OOP C++ | - Digital Circuits/Logic Lab |

PROFESSIONAL EXPERIENCE

SOLAR TURBINES (*Electrical Assembler*)

November 2022 - July 2023

CONTROL MANUFACTURING AND TESTS

- Independently analyzed, constructed and wired schematics to assemble ON/OFF skid and new production control panels and boxes in a timely manner following SWS standards.
- Communicated with Manufacturing Engineer and management to receive and execute orders effectively, managing workloads to meet established milestones/deadlines for assembling ON/OFF skid and new production control panels and boxes.

TECHNICAL EXPERIENCE

Computer Software

- Programming: Python, C/C++, C#, Java, Verilog, SQL, Javascript, HTML, React, Docker.
- Software: Visual Studio, Arduino IDE, Atmel Studios, Xilinx ISE, MATLAB(Certified).
- Additional: Microsoft Powerpoint, Excel, Word.

PROJECTS

Composite Wire Wrap Robotic System (Senior Design)

- Collaborated closely with the California Energy Commission.
- Engineered and constructed a power transmission lines navigation robot that applies carbon fiber straps to mitigate power line sagging, and met strict time and budget constraints.
- Led the implementation of an ATmega328P and LoRa module for robot control and wireless data transfer as the lead embedded system developer on the computer engineering team.
- Completed a one-year course showcasing expertise in long-term project management, teamwork, GANTT charts, risk cubes, mitigation strategies, milestone tracking, iPDRs, and iCDRs.

Field-Programmable Gate Arrays

- Implemented digital logic design utilizing gate drivers, VHDL, seven-segment decoders, & Moore Machines to solve computational problems with Digilent Basys 2 FPGAs & Xilinx ISE.

AVR Microcontroller/Embedded Systems

- Control of ATmega328P microcontrollers in various projects including LED modulation, keypad matrix multiplexing, tone generation, UART initialization, implementation of interrupts, and interfacing of OLED displays with Arduino utilizing SPI communication protocol.

REFERENCES

Name	Email	Position
Kainoa Oliveira	Oliveira_Kainoa@solarturbines.com	Staff Manufacturing Engineer
Barry Dorr	bdorr@sdsu.edu	Engineering Professor
Saeed Manshadi	smanshadi@sdsu.edu	Project Sponsor



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May 21, 2024

Dear Hiring Manager,

I am writing this letter on behalf of Mr. Tuong Do. Tuong was in my Senior Design (Capstone) course at SDSU during Fall, 2023 and Spring, 2024. He worked with a joint EE/COMPE/ME team consisting of 10 ECE and ME students, and they developed an electromechanical robot that traversed high-voltage power lines and applied resin to strengthen them. They successfully demonstrated the system on Senior Design Day at SDSU. Tuong also took my Circuit Analysis (EE310) and Microelectronics (EE330) course during Spring 2019.

Senior Design is a two-semester class which challenges teams to solve a technical problem and design a solution. It is technically and personally challenging for the students. At the end of the program, students are asked to evaluate their teammates. Tuong's evaluation was above average and showed that he was a competent and hard worker who contributed significantly to the project and was well-liked by his teammates.

Based on his performance in Senior Design, I believe that Tuong is the kind of person who will work well with a team, contribute to projects, and connect with his coworkers. I wish him the best and look forward to watching his career take shape. Please feel free to call me at 760 518 2051 or send email to bdorr@sdsu.edu if you have further questions.

Sincerely,

Barry L. Dorr, P.E.
Lecturer

Department of Electrical and Computer Engineering
San Diego State University