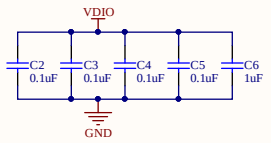
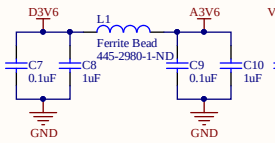
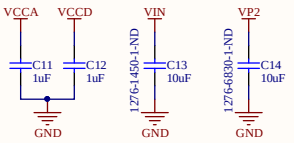
VDDIO



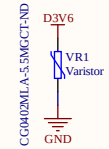
VDDD / A3V6



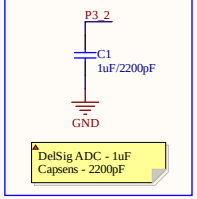
VCCA/D / Power Caps



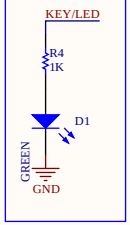
For Sparky Mike



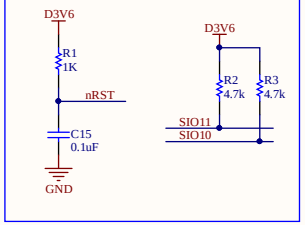
DelSig /CapSens Bypass Caps

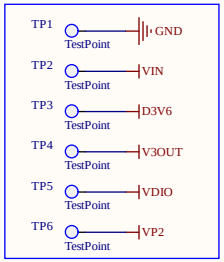


Power LED

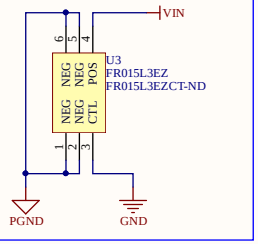


RESET / I2C Pull-ups

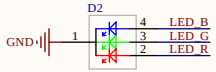




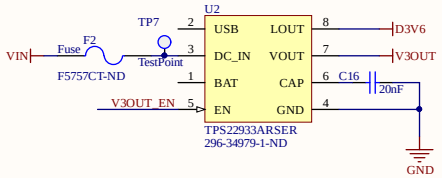
Low-Side Reverse Protector and TVS



User RGB LED



Power Mux and LDO

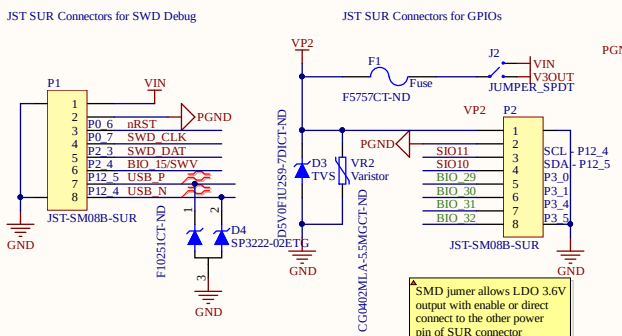


The highest voltage from VBAT/VPP_SUR/VPP_B2B will powering the LDO

D3V6 is the always on output from LDO
V3OUT can be enable/disable by V2OUT_EN pin

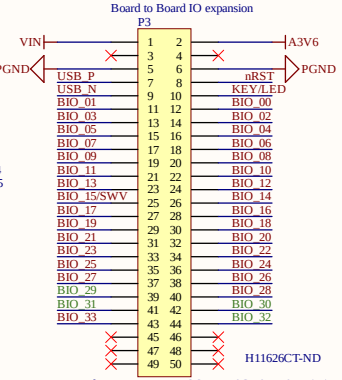
If VIN > 4 V, LDO output 3.6V
If VIN <3.4 V, LDO output VIN - 0.1 V

JST SUR Connectors for SWD Debug / JST SUR Connectors for GPIOs



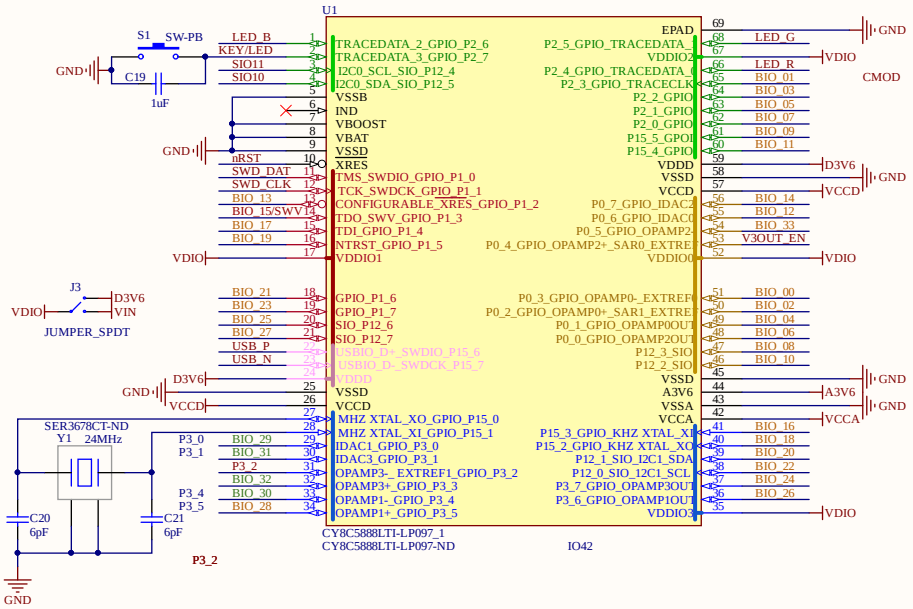
SMD jumer allows LDO 3.6V output with enable or direct connect to the other power pin of SUR connector

Board to Board IO expansion

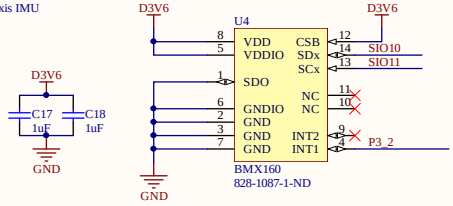


V2 updates
0. Single Power Source
1. TVS Circuit Protection
2. JB2B Conn change to DF40
3. No compoents on back side
4. Castallazed Pads
5. Power LED as button IO
6. VDDIO Should selectable between 3V6 or VIN ---
7. Add IMU

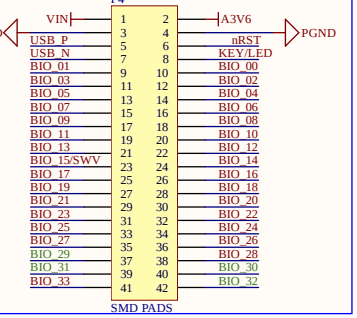
MCU PSOC5LP



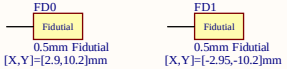
9-Axis IMU



LGA/Castellated Pads



Bottom Side Fidutials



Title		
Size: A3	Number	Revision
Date: 11/14/2019	Sheet of	
File: D:\LLGD\..\ROBOSOM_V2R1.SchDoc	Drawn By:	

SENSOR IO41 IO39 IO38 IO40

LLL Altium Mechanical Layer Standard

M1 – Board Outline
M2 – Cutout / V-Scoring

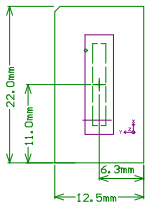
M3 – Mech Drawing – Top
M4 – Mech Drawing – Bottom
M5 – Document / Dimension – Top
M6 – Document / Dimension – Bottom

M7 – No Placement Layer – Top
M8 – No Placement Layer – Bottom

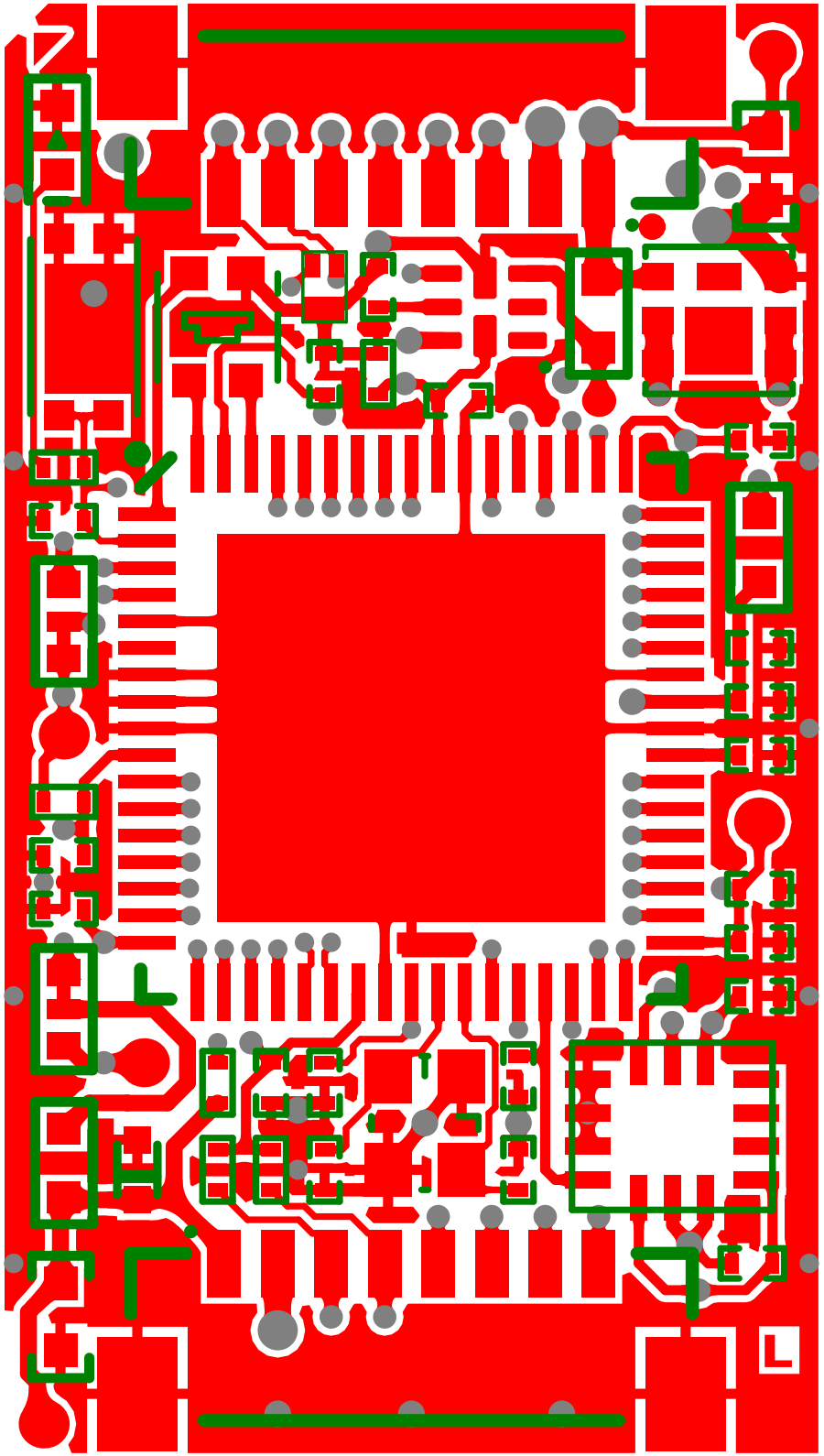
M09 – 2D Assembly Guide –Top
M10 – 2D Assembly Guide –Bottom
M11 – 2D Designator/Part Number –Top
M12 – 2D Designator/Part Number – Bottom

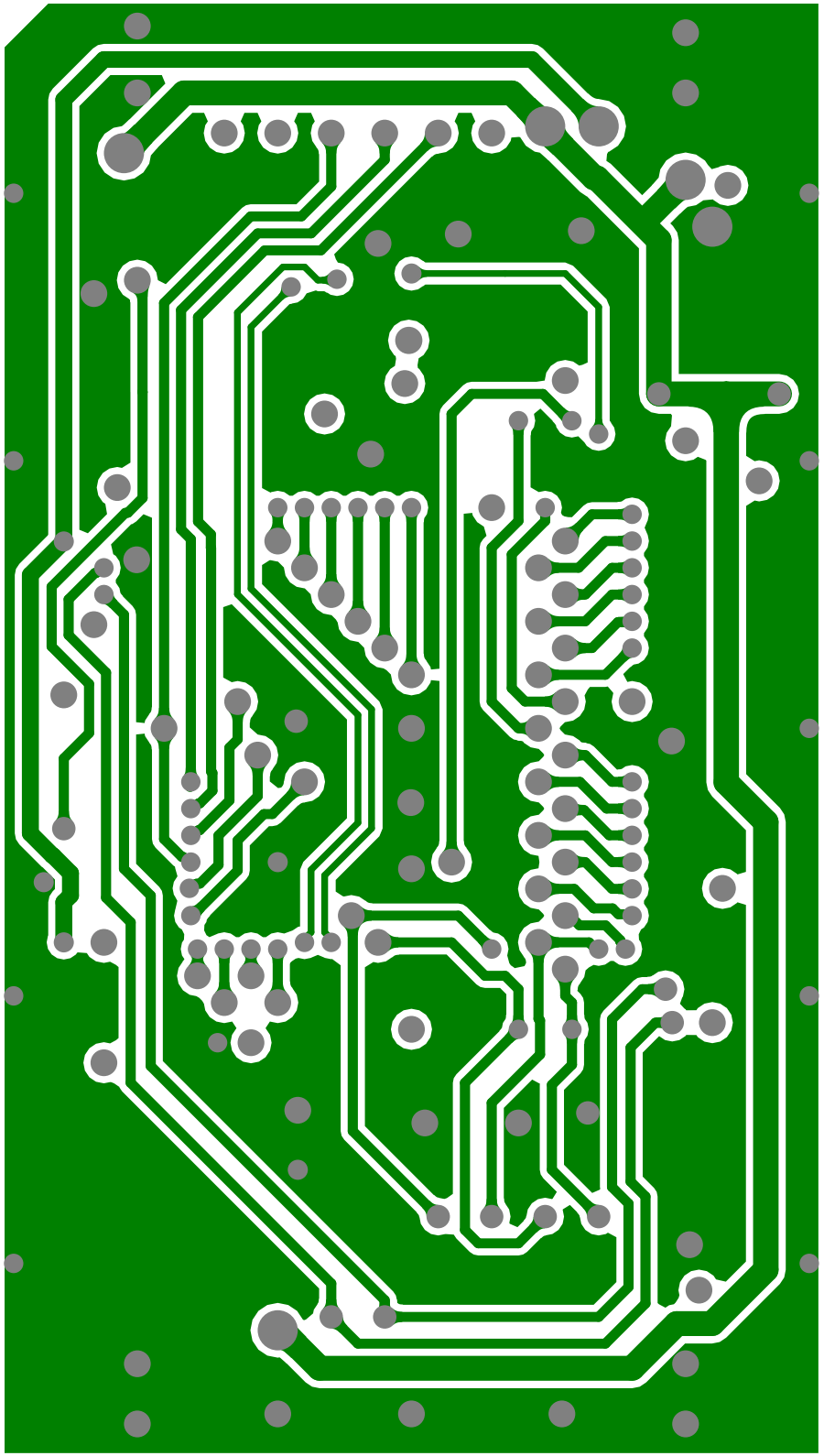
M13 – 3D Parts – Top
M14 – 3D Parts – Bottom
M15 – PnP center – Top
M16 – PnP center – Bottom

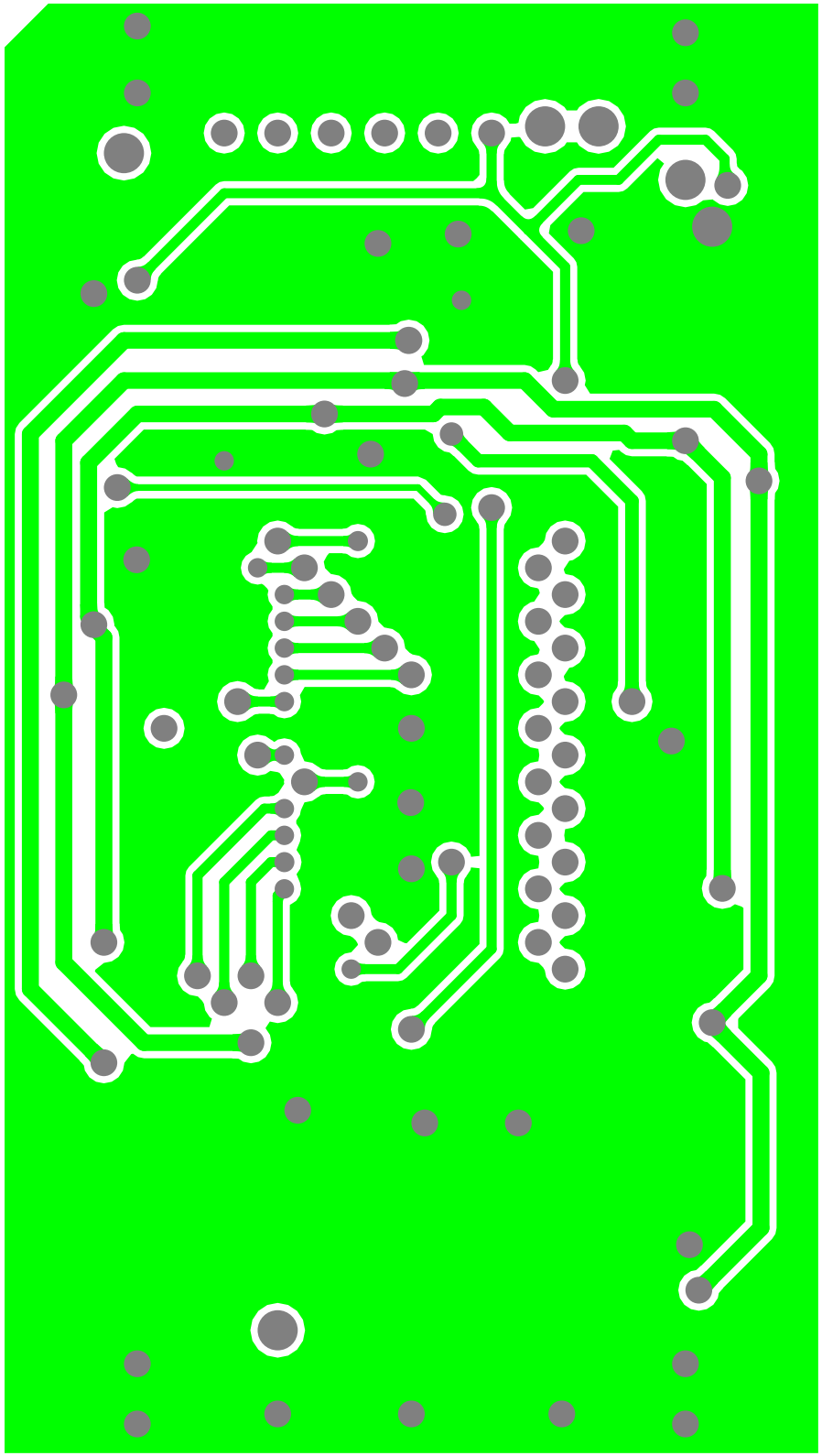
TOP VIEW



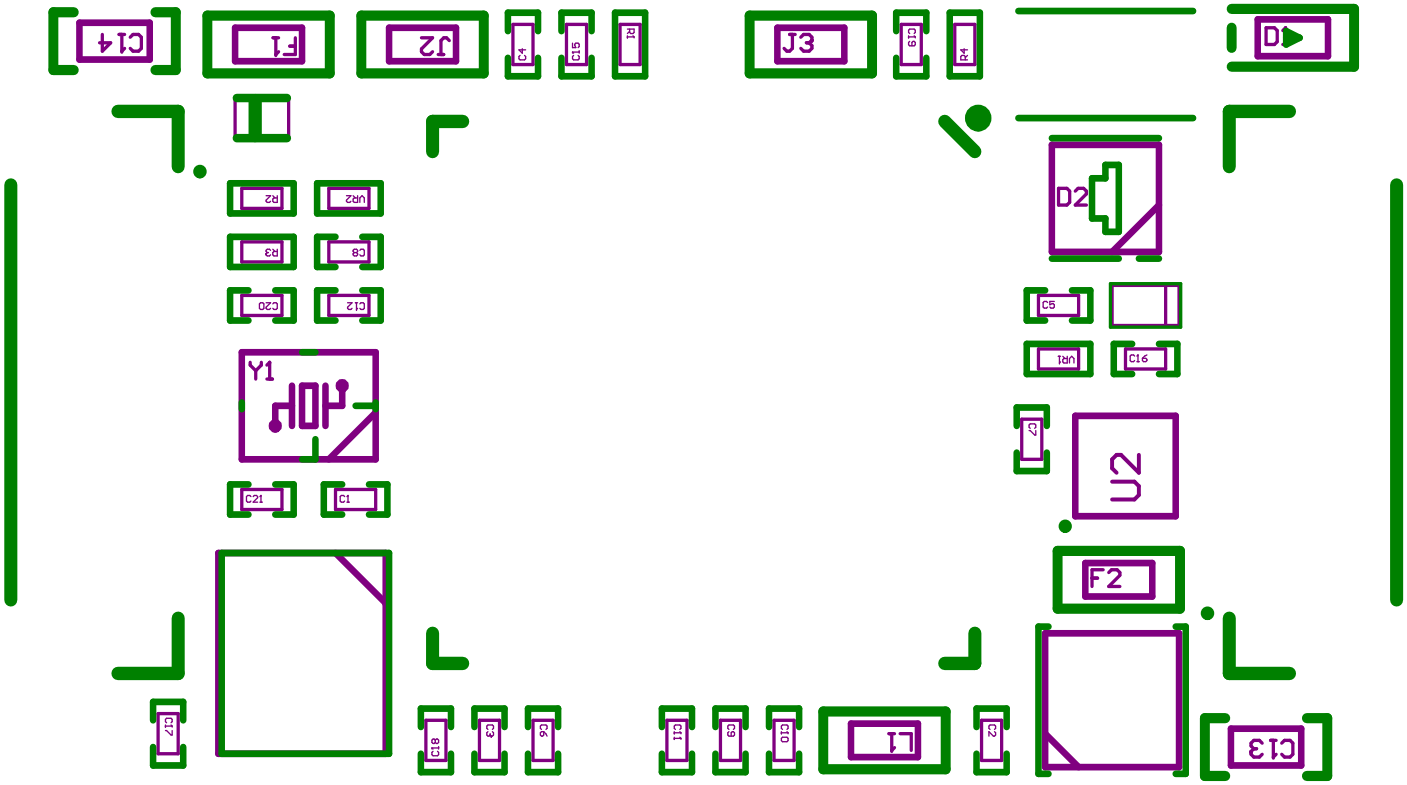
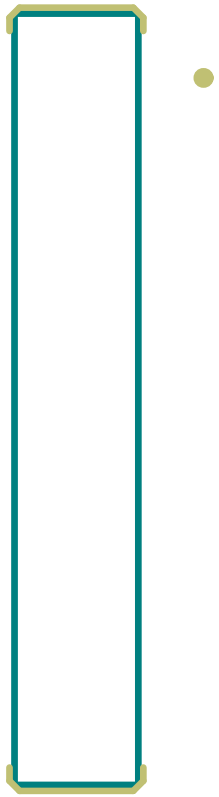
Group Name Group Address City States, Zip Code	PROJECT: PRJ00X_PROJECT_NAME	TITLE: NAME OF THIS DESIGN	
	PCB DESIGNER: NAME OF THE DESIGNER		
	DATE: 11/14/2019	PART NO.: PRJ00X_PRT00X_V00X	REV: V1R1
	FILE NAME: ROBOSOM_V2R1.PcbDoc	DWG NO: DRG000X_YYYY_MM_DD_00X	SCALE: A4 – 1:1







BOTTOM VIEW


C14
F1
J2
C4
C15
R1
J3
C19
R4
D
D2
R2
VR2
R3
C8
C20
C12
Y1
C5
VR1
C16
C7
U2
C21
C1
F2
C17
C18
C3
C6
C11
C9
C10
L1
C2
C13