Asel	
0	PC
1	PC+2
2	EA
3	
4	SP
5	SP+2
6	SP-2
7	

Bsel	
0	DR
1	Rs

START	
0	STOP
1	START

BUSY	
0	DONE
1	BUSY

Dsel	
0	PC
1	PC+2
2	PC+4
3	
4	Rd
5	H8
6	FLAG
7	TMP

Wsel	
0	16
1	S4
2	L8
3	H8

PCmut	
0	
1	+2
2	+4
3	LOAD

SPmut	
0	
1	+2
2	-2
3	LOAD

OP (1)	
00	
01	B
02	A
03	A + B
04	A - B
05	A - B
06	A & B
07	A	B
08	A ^ B
09	A + B (with Scale)
0A	A * B
0B	A / B
0C	A % B
0D	A * B
0E	A / B
0F	

EA (OP下位3ビット)	
0	DR
1	DR + GR[Rx]
2	PC
3	FP
4	
5	PC
6	GR[Rx]
7	GR[Rx]

OP (2)	
10	A << B
11	A << B
12	A >> B
13	A >>> B
14	
15	
16	B
17	
18	
19	
1A	B
1B	
1C	
1D	
1E	
1F	