

SCRUM MEETING WEEK 5 - RISCORD

Sprint planning checklist

Preparation	Meeting	Follow up
Completed tasks for week 4, such as the use case diagram, and setting up the backlog of user stories.	Decided the tech stacks we will use, worked on milestone 2 to present to the TA to check before submitting, completed the data flow diagrams level 0 and 1, UML diagram, came up with a testing strategy, github actions setup, CI, updated our use case diagram to include the requirements stipulated by the TA in week 5.	In week 6, our goal is to work on completing Milestone 2: Project Description and Requirements and submitting it to the GitHub repo as well as Canvas.

Sprint team members

Name
Chinmay Arvind
Anitej Isaac Sharma
Jackson Wilson
Ronak Jain
Soumil Chhabra

Sprint planning meeting items

Previous sprint summary

Sprint theme	N/A
Issues completed	N/A
Issues left	N/A
Team Capacity	N/A
Summary	N/A

Details Current sprint

Start date	2/1/2024
End date	2/15/2024
Sprint theme	Finishing up M2: Project Description and Requirements
Team capacity	100% (40 hours in total i.e. 8 hours/member, for week 4)
Issues capacity	6 issues were handled this week, which were: 1. DFD Level 0, 2. DFD Level 1, 3. Milestone 2, 4. UML class diagram, 5. Deciding upon tech stacks to use, setting up testing strategy, CI, 6. Setting up GitHub actions
Individual capacity	Chinmay - 8 hours Jackson - 8 hours Anitej - 8 hours Soumil - 8 hours Ronak - 8 hours
Potential risks	Assignment due dates happening this week could be a potential risk, since most of the team members have a lot of assignments due during this week.
Mitigations	We have planned ahead to ensure that our assignment due dates do not interfere with each member spending 8 hours on the project for this week to ensure the timely completion of Milestone 2.

Sprint planning resources

- <https://www.lucidchart.com/pages/data-flow-diagram> - Data Flow diagram
- <https://support.discord.com/hc/en-us/categories/200404398> - Discord UI Components
- https://app.diagrams.net/#G1IfHxjmHeuG_FZyOVKbGWWiT16riHLoQJ - UML Diagram (With Multiplicities)