



EMC

AHB SLAVE  
REGISTER  
INTERFACE

DATA  
BUFFERS

AHB SLAVE  
MEMORY  
INTERFACE

MEMORY  
CONTROLLER  
STATE  
MACHINE

PAD INTERFACE

A[23:0]

D[31:0]

WE

shared  
signals

OE

BLS[3:0]

CS[3:0]

static  
memory  
signals

DYCS[3:0]

CAS

RAS

CLKOUT[1:0]

CKEOUT[3:0]

DQMOUT[3:0]

dynamic  
memory  
signals