

# US Patent & Trademark Office

## Patent Public Search | Text View

---

United States Patent	12394538
Kind Code	B2
Date of Patent	August 19, 2025
Inventor(s)	Lee; Chun-Tai

---

### Manufacturing method for a flexible flat cable

---

#### Abstract

A flexible flat cable (FFC) includes a first insulation layer, at least one pair of conductors, a plurality of low-k dielectric layers, two second insulation layers, and at least one shielding layer. The pair of conductors is located within the first insulation layer. Each pair of conductors includes a plurality of first conductors, and the first conductors are axially extending and arranged in parallel. The low-k dielectric layers are embedded in the first insulation layer. Each of the pair of conductors or each of the first conductors is covered and surrounded with one low-k dielectric layer. The two second insulation layers are located on two surfaces of the first insulation layer. The shielding layer is located on the two second insulation layers opposite to the first insulation layer.

---

<b>Inventors:</b>	<b>Lee; Chun-Tai (Taipei, TW)</b>
<b>Applicant:</b>	<b>LUXSHARE-ICT CO., LTD. (Taipei, TW)</b>
<b>Family ID:</b>	<b>1000008766338</b>
<b>Assignee:</b>	<b>LUXSHARE-ICT CO., LTD. (Taipei, TW)</b>
<b>Appl. No.:</b>	<b>18/110519</b>
<b>Filed:</b>	<b>February 16, 2023</b>

#### Prior Publication Data

<b>Document Identifier</b>	<b>Publication Date</b>
US 20230197314 A1	Jun. 22, 2023

#### Foreign Application Priority Data

TW	108140535	Nov. 07, 2019
----	-----------	---------------

#### Related U.S. Application Data

Publication Classification

**Int. Cl.:** **H01B7/08** (20060101); **B32B3/02** (20060101); **B32B7/12** (20060101); **B32B15/08** (20060101); **B32B15/20** (20060101); **B32B37/02** (20060101); **H01B7/04** (20060101); **H01B13/00** (20060101); **H01B13/22** (20060101); **H01R12/77** (20110101); **H05K9/00** (20060101)

**U.S. Cl.:**

**CPC** **H01B7/0838** (20130101); **B32B3/02** (20130101); **B32B7/12** (20130101); **B32B15/08** (20130101); **B32B15/20** (20130101); **B32B37/02** (20130101); **H01B7/04** (20130101); **H01B13/0036** (20130101); **H01B13/22** (20130101); **H01R12/77** (20130101); **H05K9/0088** (20130101); **H05K9/0098** (20130101); B32B2250/40 (20130101); B32B2260/02 (20130101); B32B2307/202 (20130101); B32B2307/204 (20130101); B32B2307/206 (20130101); B32B2311/04 (20130101); B32B2311/12 (20130101); B32B2311/16 (20130101); B32B2311/22 (20130101); B32B2311/24 (20130101); B32B2457/08 (20130101)

Field of Classification Search

**USPC:** None

---

References Cited

U.S. PATENT DOCUMENTS

Patent No.	Issued Date	Patentee Name	U.S. Cl.	CPC
3612744	12/1970	Thomas	174/117FF	H01B 7/0838
4185162	12/1979	Bogese, II	174/32	H01B 7/0838
4287385	12/1980	Dombrowsky	174/105R	H01B 7/0838
4468089	12/1983	Brorein	385/101	G02B 6/4404
4475006	12/1983	Olyphant, Jr.	174/105R	H01B 7/0861
5003126	12/1990	Fujii	174/117FF	G02B 6/4403
5084594	12/1991	Cady	174/117M	H01B 7/0861
5900588	12/1998	Springer	174/117F	H01B 7/0861
6300846	12/2000	Brunker	N/A	N/A
7612290	12/2008	Le	N/A	N/A

8338709	12/2011	Kodama	174/117FF	H01B 7/0838
10164311	12/2017	Lee et al.	N/A	N/A
2009/0126972	12/2008	Matsushita	174/113R	H01B 7/0861
2012/0199376	12/2011	Konya	174/117F	H01B 7/0869
2012/0199378	12/2011	Konya	N/A	N/A
2013/0043058	12/2012	Shanai	428/354	H01B 7/295
2013/0161057	12/2012	Konya	174/102R	H01B 7/0838
2013/0277088	12/2012	Gundel	174/107	H01B 11/203
2014/0090883	12/2013	Gundel	174/102R	H01B 11/1895
2018/0053981	12/2017	Bae	N/A	H05K 1/0221
2019/0096542	12/2018	Huang	N/A	H01B 7/0838

## FOREIGN PATENT DOCUMENTS

<b>Patent No.</b>	<b>Application Date</b>	<b>Country</b>	<b>CPC</b>
202917205	12/2012	CN	N/A
109585068	12/2018	CN	N/A
2487894	12/2011	EP	N/A
2487694	12/2013	EP	C08L 23/0869
4066725	12/2007	JP	N/A
2008198592	12/2007	JP	H01B 11/00
5247956	12/2012	JP	N/A
2013140716	12/2012	JP	H01B 7/08
201209854	12/2011	TW	N/A
M492509	12/2013	TW	N/A
M530461	12/2015	TW	H01B 7/08
M536407	12/2016	TW	N/A
I583277	12/2016	TW	N/A
M570520	12/2017	TW	N/A

## OTHER PUBLICATIONS

Gerpheide—Reliability & econ various shielding flat cables—polyester adhesive w-163C+motive—IEEE 1971 EIC Electr. Insul. Conf.—1971 (Year: 1971). cited by examiner

Hedrick—high-temp polyimide nanofoams for microelectronic applications—React. Func. Polymers—1996 (Year: 1996). cited by examiner

Nakamura—JP 2008-198592 A—MT—flat cable w-multiple layers—2008 (Year: 2008). cited by examiner

Fukuda—JP 2013-140716 A—sister of CN D1—Bib+MT-Google—shielded flat cable—2013 (Year: 2013). cited by examiner

Chin—TW M530461 U—CN D2—Bib-MT-Google—flexible flat cable structure—2016 (Year: 2016). cited by examiner

*Primary Examiner:* Lawler; John Vincent

*Attorney, Agent or Firm:* Birch, Stewart, Kolasch & Birch, LLP

---

## **Background/Summary**

CROSS-REFERENCES TO RELATED APPLICATIONS (1) This application is a Division of application Ser. No. 15/930,747, filed on May 13, 2020 with claiming the priority benefit of U.S. provisional application Ser. No. 62/880,678, filed on Jul. 31, 2019 and Patent Application No. 108140535 filed in Taiwan, R.O.C. on Nov. 7, 2019. The entirety of the above-mentioned patent applications are hereby incorporated by references herein and made a part of the specification.

### **BACKGROUND**

#### **Technical Field**

(1) The present invention relates to a high-speed signal transmission line and a manufacturing method thereof, and especially, to a flexible flat cable (FFC), a manufacturing method thereof and a signal transmission device.

#### **Related Art**

(2) A flexible flat cable (FFC) is a high-precision cable product used for signal transmission, and has advantages such as being bendable at will and high-speed signal transmission. The FFC can be simply and flexibly applied to signal transmission of various electronic products, and therefore widely applied to many fields. The FFC herein may be used in combination with an electronic connector or may be directly soldered on a printed circuit board (PCB), to transmit signals from one end of the FFC to the other end, thereby achieving an objective of signal transmission.

(3) A conventional FFC is manufactured by adopting a lamination process. In other words, by heating and pressurizing upper and lower insulation films, hot melt adhesives, adjacent to respective sides of a conductor, of the insulation films are fused with each other and clamp the conductor between the films, to form a flattened FFC.

### **SUMMARY**

(4) However, in the lamination process, the hot melt adhesives on the upper and lower sides of the conductor may flow, leading to non-uniformity of the hot melt adhesive around the conductor. Consequently, a dielectric layer is not stable, and high-frequency signal transmission is further greatly affected, and for example, quality of signals is reduced. Moreover, a pitch between conductors (the shortest distance between two center points of two adjacent conductors) is maintained in dependence on a guide bar in the process. As a result, a size of the pitch may fluctuate with the stability of the process, to affect the quality of high-frequency signal transmission. In addition, the insulation films are made, through a coating process, of a material having a relatively high dielectric constant, and as a result, the quality of the high-frequency signal transmission of the clamped conductor is affected.

(5) In an embodiment, a manufacturing method for an FFC includes: arranging at least one pair of conductors in parallel between two insulation films, where each pair of conductors is covered by a low-k dielectric layer; laminating the two insulation films into one insulation layer, where each pair of conductors covered by the low-k dielectric layer is embedded in the insulation layer; and laminating at least one shielding layer on two surfaces of the insulation layer.

(6) In another embodiment, a manufacturing method for an FFC includes: arranging at least one pair of conductors in parallel between two insulation films, where each pair of conductors includes

a plurality of conductors, and each conductor is covered by a low-k dielectric layer; laminating the two insulation films into one insulation layer, where each pair of conductors covered by the low-k dielectric layer is embedded in the insulation layer; and laminating at least one shielding layer on two surfaces of the insulation layer.

(7) In still another embodiment, a manufacturing method for an FFC includes: arranging at least one pair of conductors between two insulating-shielding films in parallel, where each pair of conductors is covered by a low-k dielectric layer, and each insulating-shielding film includes a metal layer, an external dielectric layer located on the metal layer and adjacent to the pair of conductors, and a hot melt insulation layer located on the external dielectric layer and adjacent to the pair of conductors; and laminating the two insulating-shielding films into one insulating-shielding layer, where each pair of conductors covered by the low-k dielectric layer is embedded in the insulating-shielding layer.

(8) In still another embodiment, a manufacturing method for an FFC includes: arranging at least one pair of conductors between two insulating-shielding films in parallel, where each pair of conductors includes a plurality of conductors, each conductor is covered by a low-k dielectric layer, and each insulating-shielding film includes a metal layer, an external dielectric layer located on the metal layer and adjacent to the pair of conductors, and a hot melt insulation layer located on the external dielectric layer and adjacent to the pair of conductors; and laminating the two insulating-shielding films into one insulating-shielding layer, where each pair of conductors covered by the low-k dielectric layer is embedded in the insulating-shielding layer.

(9) Based on above, according to the FFC, the manufacturing method thereof, and the signal transmission device of the present invention, a conductor clamped in an insulation layer (that is, the first insulation layer) is covered by a layer of low-k material (that is, the low-k dielectric layer), to prevent a high-k material from affecting the signal transmission, to further improve quality of high-speed signal transmission relatively. In addition, according to the embodiments of the present invention, the process is convenient and low in cost.

---

## Description

### BRIEF DESCRIPTION OF THE DRAWINGS

- (1) FIG. 1 is a schematic diagram of a first embodiment of a flexible flat cable;
- (2) FIG. 2 is a schematic diagram of a cross section along a cutting line I-I in FIG. 1;
- (3) FIG. 3 is a schematic diagram of a second embodiment of a flexible flat cable;
- (4) FIG. 4 is a schematic diagram of a cross section along a cutting line II-II in FIG. 3;
- (5) FIG. 5 is a schematic diagram of a third embodiment of a flexible flat cable;
- (6) FIG. 6 is a schematic diagram of a cross section along a cutting line III-III in FIG. 5;
- (7) FIG. 7 is a schematic diagram of a fourth embodiment of a flexible flat cable;
- (8) FIG. 8 is a schematic diagram of a cross section along a cutting line IV-IV in FIG. 7;
- (9) FIG. 9 is a flowchart of a first embodiment of a manufacturing method for a flexible flat cable;
- (10) FIG. 10 is a flowchart of a second embodiment of a manufacturing method for a flexible flat cable;
- (11) FIG. 11 is a flowchart of a third embodiment of a manufacturing method for a flexible flat cable;
- (12) FIG. 12 is a flowchart of a fourth embodiment of a manufacturing method for a flexible flat cable;
- (13) FIG. 13 is a schematic diagram of a fifth embodiment of a flexible flat cable;
- (14) FIG. 14 is a schematic diagram of a cross section of an example along a cutting line V-V in FIG. 13;
- (15) FIG. 15 is a schematic diagram of a cross section of another example along a cutting line V-V

in FIG. 13;

(16) FIG. 16 is a schematic diagram of a sixth embodiment of a flexible flat cable;

(17) FIG. 17 is a schematic diagram of a cross section of an example along a cutting line VI-VI in FIG. 16;

(18) FIG. 18 is a schematic diagram of a cross section of another example along a cutting line VI-VI in FIG. 16;

(19) FIG. 19 is a schematic diagram of a seventh embodiment of a flexible flat cable;

(20) FIG. 20 is a schematic diagram of a cross section of an example along a cutting line VII-VII in FIG. 19;

(21) FIG. 21 is a schematic diagram of a cross section of another example along a cutting line VII-VII in FIG. 19;

(22) FIG. 22 is a schematic diagram of an eighth embodiment of a flexible flat cable;

(23) FIG. 23 is a schematic diagram of a cross section of an example along a cutting line VIII-VIII in FIG. 22;

(24) FIG. 24 is a schematic diagram of a cross section of another example along a cutting line VIII-VIII in FIG. 22;

(25) FIG. 25 and FIG. 26 are schematic diagrams of a cross section of a flexible flat cable with a first exemplary shielding layer;

(26) FIG. 27 is a flowchart of a fifth embodiment of a manufacturing method for a flexible flat cable;

(27) FIG. 28 is a flowchart of a sixth embodiment of a manufacturing method for a flexible flat cable;

(28) FIG. 29 is a flowchart of a seventh embodiment of a manufacturing method for a flexible flat cable;

(29) FIG. 30 and FIG. 31 are schematic diagrams of a cross section of a flexible flat cable with a second exemplary shielding layer;

(30) FIG. 32 is a flowchart of an eighth embodiment of a manufacturing method for a flexible flat cable;

(31) FIG. 33 and FIG. 34 are schematic diagrams of a cross section of a flexible flat cable with a third exemplary shielding layer;

(32) FIG. 35 and FIG. 36 are schematic diagrams of a cross section of a flexible flat cable with a fourth exemplary shielding layer;

(33) FIG. 37 is a flowchart of a ninth embodiment of a manufacturing method for a flexible flat cable;

(34) FIG. 38 is a schematic diagram of a first application example of a flexible flat cable; and

(35) FIG. 39 is a schematic diagram of a second application example of a flexible flat cable.

#### DETAILED DESCRIPTION

(36) In some embodiments, referring to FIG. 1 to FIG. 4, a flexible flat cable (FFC) 10 includes a first insulation layer 110, at least one pair of conductors 122, a plurality of low-k dielectric layers 124, two metal layers 152, and two external dielectric layers 158.

(37) Herein, each pair of conductors 122 includes two adjacent conductors (referred to as first conductors 122a below). The first conductors 122a longitudinally extend and are disposed in parallel. In other words, each of the first conductors 122a extends along a first direction, and the first conductors 122a are arranged in parallel along a second direction. The first direction is approximately perpendicular to the second direction.

(38) In an embodiment, as shown in FIG. 1 and FIG. 2, the first conductors 122a are respectively covered by the low-k dielectric layers 124. In other words, a surface, surrounding the axis, of each first conductor 122a is covered by a low-k dielectric layer 124, to form conductors 130 with a jacket. In addition, the conductors 130 with a jacket are embedded in the first insulation layer 110. In an example, the conductors 130 with a jacket are arranged at intervals between each other. In

other words, there is a gap between two adjacent conductors **130** with a jacket.

(39) In another embodiment, as shown in FIG. 3 and FIG. 4, the pairs of conductors **122** are respectively covered by the low-k dielectric layers **124**. In other words, each pair of conductors **122** is covered by a low-k dielectric layer **124**, to form a pair of conductors **132** with a jacket. In addition, conductors **130** of the pairs of conductors **132** with a jacket are embedded in the first insulation layer **110**. In an example, the low-k dielectric layers **124** extend longitudinally, are parallel to each other and are arranged at intervals. Each pair of conductors **122** is embedded in a corresponding low-k dielectric layer **124**. In other words, there is a gap between two adjacent pairs of conductors **132** with a jacket.

(40) Referring to FIG. 1 to FIG. 4 again, the two external dielectric layers **158** are respectively located on two surfaces of the first insulation layer **110**. The two metal layers **152** are respectively located on the two external dielectric layers **158** opposite to the first insulation layer **110**.

(41) In some embodiments, the first insulation layer **110**, the external dielectric layers **158**, and the metal layers **152** may be implemented by using two insulating-shielding films. For example, the insulating-shielding films may be insulating-shielding tapes. For example, one part of the first insulation layer **110**, one external dielectric layer **158**, and one metal layer **152** form one tape, and the other part of the first insulation layer **110**, the other external dielectric layer **158**, and the other metal layer **152** form the other tape. In other words, outermost layers of the FFC **10** are the metal layers **152**.

(42) In some embodiments, the FFC **10** may further include two second insulation layers **140**. The two second insulation layers **140** are respectively located on the two metal layers **152** opposite to the two external dielectric layers **158**.

(43) In some embodiments, the first insulation layer **110**, the external dielectric layers **158**, the metal layers **152**, and the second insulation layers **140** may be implemented by using two insulating-shielding films. For example, the insulating-shielding films may be insulating-shielding tapes. For example, one part of the first insulation layer **110**, one external dielectric layer **158**, one metal layer **152**, and one second insulation layer **140** form one tape, and the other part of the first insulation layer **110**, the other external dielectric layer **158**, the other metal layer **152**, and the other second insulation layer **140** form the other tape. In other words, outermost layers of the FFC **10** are the second insulation layers **140**, to protect the internal structure.

(44) In some embodiments, as shown in FIG. 5 to FIG. 8, the FFC **10** may further include at least one conductor (a second conductor **122b**) that is not covered or surrounded by a low-k dielectric layer **124**, that is, a conductor with no jacket. The second conductor **122b** axially extends and is arranged in parallel with the pairs of conductors **122**. The second conductor **122b** is directly embedded in the first insulation layer **110**. The pairs of conductors **122** and the second conductors **122b** are arranged at intervals and in a way that their lateral positions are switched one for the other. In some embodiments, viewed from a cross section of the FFC **10**, two conductors that are closest to two side edges of the FFC **10** are the second conductors **122b**, that is, conductors with no jacket.

(45) In some embodiments, the metal layer **152** may be a metal foil or a metal coating. In some embodiments, the metal layer **152** may be formed on a surface of the second insulation layer **140** by means of dry lamination, wet lamination, or evaporation.

(46) In some embodiments, the external dielectric layer **158** may be formed on a surface, adjacent to the first insulation layer **110**, of the metal layer **152** by means of dry lamination, wet lamination, or coating, that is, on a surface of the other side, opposite to the second insulation layer **140**, of the metal layer **152**.

(47) In some embodiments, the first insulation layer **110** is hot melt. In some embodiments, the first insulation layer **110** may be formed on a surface of the external dielectric layer **158** by means of wet lamination or coating.

(48) In some embodiments, referring to FIG. 9 and FIG. 10, a manufacturing method for an FFC **10**

is as follows: first, at least one pair of conductors **122** (that is, a plurality of conductors **130** with a jacket or at least one pair of conductors **132**, not shown) with a jacket) is arranged in parallel between two insulating-shielding films **160** (step **S11** or **S21**). In an embodiment, as shown in FIG. **9**, each insulating-shielding film **160** includes a metal layer **152**, an external dielectric layer **158** located on the metal layer **152** and adjacent to at least one pair of conductors **122**, and a hot melt insulation layer (that is, a part of a first insulation layer **110**) located on the external dielectric layer **158** and adjacent to the at least one pair of conductors **122**. In an embodiment, as shown in FIG. **10**, each insulating-shielding film **160** includes a second insulation layer **140**, a metal layer **152**, an external dielectric layer **158** located on the metal layer **152** and adjacent to at least one pair of conductors **122**, and a hot melt insulation layer (that is, a part of a first insulation layer **110**) located on the external dielectric layer **158** and adjacent to the at least one pair of conductors **122**.

(49) After step **S11** or **S21**, the two insulating-shielding films **160** are laminated into one insulating-shielding layer (step **S12** or **S22**), and the conductor **130** with a jacket or the pair of conductors **132** with a jacket is embedded in the insulating-shielding layer, to form an FFC (step **S13** or **S23**). In an example, hot melt first insulation layers **110** are located on sides, adjacent to the conductors **122a**, of the two insulating-shielding films **160**. Therefore, in the lamination step (step **S11** or **S21**), the first insulation layers **110** of the two insulating-shielding films **160** may be fused into one layer due to lamination, and the conductor **130** with a jacket or the pair of conductors **132** with a jacket (and the conductor **122b** with no jacket, not shown) is embedded in the layer.

(50) In some embodiments, as shown in FIG. **11** and FIG. **12**, a large block of FFC including a large quantity of pairs of conductors **122**, that is, a plurality of FFCs **10** coupled to each other at edges (that is, side edges extending along the conductors **122a**), may be formed at a time based on steps **S11** to **S13** or **S21** to **S23**, and then cutting (that is, cutting off two adjacent conductors **122a**) is performed according to the quantity of conductors **122a** (and conductors **122b** with no jacket, not shown) required by each FFC **10** to split the large block of FFC into a plurality of FFCs **10** (step **S13** or **S23**).

(51) In some embodiments, before step **S11** or **S21**, the insulating-shielding films **160** may be formed in advance.

(52) In an example, a step of forming the insulating-shielding film **160** includes: forming the external dielectric layer **158** on the surface of the metal layer **152** by means of dry lamination, wet lamination, or coating, and forming the first insulation layer **110** on the surface of the other side, opposite to the metal layer **152**, of the external dielectric layer **158** by means of wet lamination or coating. The first insulation layer **110** is made of a hot melt material.

(53) In another example, a step of forming the insulating-shielding film **160** includes: forming the metal layer **152** on the surface of the second insulation layer **140** by means of dry lamination, wet lamination, or evaporation, forming the external dielectric layer **158** on the surface of the other side, opposite to the second insulation layer **140**, of the metal layer **152** by means of dry lamination, wet lamination, or coating, and forming the first insulation layer **110** on the surface of the other side, opposite to the metal layer **152**, of the external dielectric layer **158** by means of wet lamination or coating. The first insulation layer **110** is made of a hot melt material.

(54) If the metal layer **152** is formed by means of dry lamination or wet lamination, the metal layer **152** may be a metal foil. If the metal layer **152** is formed by means of evaporation, the metal layer **152** may be a metal coating.

(55) In some embodiments, referring to FIG. **13** to FIG. **18**, an FFC **10** includes a first insulation layer **110**, at least one pair of conductors **122**, a plurality of low-k dielectric layers **124**, two second insulation layers **140**, and at least one shielding layer **150**. Each shielding layer **150** is multi-layered.

(56) Herein, each pair of conductors **122** includes two first conductors **122a**. The first conductors **122a** longitudinally extend and are disposed in parallel. In other words, each of the first conductors **122a** extends along a first direction, and the first conductors **122a** are arranged in parallel along a



second direction. The first direction is approximately perpendicular to the second direction.

(57) In some embodiments, as shown in FIG. 13 to FIG. 15, the first conductors **122a** are respectively covered by the low-k dielectric layers **124**. In other words, a surface, surrounding the axis, of each first conductor **122a** is covered by a low-k dielectric layer **124**, to form conductors **130** with a jacket. In addition, the conductors **130** with a jacket are embedded in the first insulation layer **110**. In an example, the conductors **130** with a jacket are arranged at intervals between each other. In other words, there is a gap between two adjacent conductors **130** with a jacket.

(58) In some other embodiment, as shown in FIG. 16 to FIG. 18, the pairs of conductors **122** are respectively covered by the low-k dielectric layers **124**. In other words, each pair of conductors **122** is covered by a low-k dielectric layer **124**, to form a pair of conductors **132** with a jacket. In addition, conductors **130** of the pairs of conductors **132** with a jacket are embedded in the first insulation layer **110**. In an example, the low-k dielectric layers **124** extend longitudinally, are parallel to each other and are arranged at intervals. Each pair of conductors **122** is embedded in a corresponding low-k dielectric layer **124**. In other words, there is a gap between two adjacent pairs of conductors **132** with a jacket.

(59) The two second insulation layers **140** are respectively located on two surfaces of the first insulation layer **110**. The shielding layer **150** is located on the second insulation layer **140** opposite to the first insulation layer **110**.

(60) In some embodiments, referring to FIG. 14 and FIG. 17, using two shielding layers **150** as an example, the two shielding layers **150** are respectively located on second insulation layers **140** opposite to a first insulation layer **110**. Each shielding layer **150** includes a metal layer **152**, an adhesive layer **154**, and a third insulation layer **156**. The metal layer **152** is located on a corresponding second insulation layer **140** opposite to the first insulation layer **110**. The adhesive layer **154** is configured to bond the metal layer **152** and the second insulation layer **140** that are adjacent. The third insulation layer **156** is located on the corresponding metal layer **152** opposite to the second insulation layer **140**, to protect the internal structure. For example, for one FFC **10**, the second insulation layer **140**, the adhesive layer **154**, the metal layer **152**, and the third insulation layer **156** are stacked sequentially outward from the middle first insulation layer **110**.

(61) In some embodiments, referring to FIG. 15 and FIG. 18, using two shielding layers **150** as an example, the two shielding layers **150** are respectively located on second insulation layers **140** opposite to a first insulation layer **110**. Each shielding layer **150** includes an external dielectric layer **158**, a metal layer **152**, and an adhesive layer **154**. The external dielectric layer **158** is located on a corresponding second insulation layer **140** opposite to the first insulation layer **110**. The adhesive layer **154** is configured to bond the external dielectric layer **158** and the second insulation layer **140** that are adjacent. The metal layer **152** is located on the corresponding external dielectric layer **158** opposite to the second insulation layer **140**. For example, for one FFC **10**, the second insulation layer **140**, the adhesive layer **154**, the external dielectric layer **158**, and the metal layer **152** are stacked sequentially outward from the middle first insulation layer **110**. Here, the outermost side of the FFC **10** is not protected by a third insulation layer **156**.

(62) In some embodiments, each shielding layer **150** may further include a third insulation layer **156**. In an example, the third insulation layer **156** is located on the corresponding metal layer **152** opposite to the adhesive layer **154**, to provide protection. For example, for one FFC **10**, the second insulation layer **140**, the adhesive layer **154**, the external dielectric layer **158**, the metal layer **152**, and the third insulation layer **156** are stacked sequentially outward from the middle first insulation layer **110**. Here, the third insulation layer **156** is disposed on the outermost side of the FFC **10**, to protect the internal structure. In another example, the third insulation layer **156** may be disposed between the metal layer **152** and the external dielectric layer **158** (not shown). In other words, the third insulation layer **156** is located on the corresponding external dielectric layer **158** opposite to the second insulation layer **140**. The metal layer **152** is located on the corresponding third insulation layer **156** opposite to the adhesive layer **154**. For example, for one FFC **10**, the second

insulation layer **140**, the adhesive layer **154**, the external dielectric layer **158**, the third insulation layer **156**, and the metal layer **152** are stacked sequentially outward from the middle first insulation layer **110**. Here, the third insulation layer **156** is configured to adjust an absolute distance between the metal layer **152** and a conductor **122a**, to provide a better signal shielding effect.

(63) In some embodiments, as shown in FIG. **19** to FIG. **24**, the FFC **10** may further include at least one conductor (a second conductor **122b**) that is not covered or surrounded by a low-k dielectric layer **124**, that is, a conductor with no jacket. The second conductor **122b** axially extends and is arranged in parallel with the pairs of conductors **122**. The second conductor **122b** is directly embedded in the first insulation layer **110**. The pairs of conductors **122** and the second conductors **122b** are arranged at intervals and in a way that their lateral positions are switched one for the other. In some embodiments, viewed from a cross section of the FFC **10**, two conductors that are closest to two side edges of the FFC **10** are the second conductors **122b**, that is, conductors with no jacket.

(64) In some embodiments, the metal layer **152** may be a metal foil or a metal coating. In some embodiments, the metal layer **152** may be formed on a surface of the third insulation layer **156** by means of dry lamination, wet lamination, or evaporation.

(65) In some embodiments, the external dielectric layer **158** may be formed on a surface, adjacent to the first insulation layer **110**, of the metal layer **152** or the third insulation layer **156** by means of dry lamination, wet lamination, or coating.

(66) In some embodiments, the adhesive layer **154** is formed on a surface of the external dielectric layer **158** or the metal layer **152** by means of trans-lamination, wet lamination or coating. In some embodiments, the adhesive layer **154** may be a dielectric adhesive layer.

(67) In some embodiments, the first insulation layer **110** is hot melt. In some embodiments, the first insulation layer **110** may be formed on a surface of the second insulation layer **140** by means of wet lamination or coating.

(68) In some embodiments, as shown in FIG. **13** to FIG. **24**, if there are two shielding layers **150**, a width of the shielding layer **150** may be approximately equal to a width of the second insulation layer **140**. That is, edges of the shielding layer **150**, the second insulation layer **140**, and the first insulation layer **110** are flush with each other. In some embodiments, as shown in FIG. **25** and FIG. **26**, if there are two shielding layers **150**, a width of the second insulation layer **140** may exceed a width of the shielding layer **150**. That is, viewed from a cross section of the FFC **10**, two side edges of the second insulation layer **140** (and the first insulation layer **110**) respectively exceed two side edges of each shielding layer **150**, to make each shielding layer **150** shrink inward opposite to the second insulation layer **140**.

(69) In some embodiments, as shown in FIG. **27** and FIG. **28**, if edges of the FFC **10** are flush with each other or the shielding layer **150** of the FFC **10** shrinks inward, the FFC **10** may be manufactured by using the following method. Referring to FIG. **27** and FIG. **28**, first, at least one pair of conductors **122** (that is, a plurality of conductors **130** with a jacket or at least one pair of conductors (**132**, not shown) with a jacket) is arranged in parallel between two insulation films **170** (step S51 or S61). Each insulation film **170** includes a first insulation layer **110** and a second insulation layer **140**. The first insulation layer **110** is adjacent to the pair of conductors **122** and is stacked on the second insulation layer **140**, that is, the first insulation layer **110** covers an internal surface of the second insulation layer **140**. The first insulation layer **110** herein is hot melt.

(70) After step S51 or S61, the two insulation films **170** are laminated into one insulation layer (step S52 or S62), and the conductors **130** with a jacket or the pairs of conductors **132** with a jacket are embedded in the insulation layer (step S53 or S63). Therefore, in the lamination step, the first insulation layers **110** on internal sides of the two insulation films **170** may be fused into one layer due to lamination, and the conductor **130** with a jacket or the pair of conductors **132** with a jacket (and the conductor **122b** with no jacket, not shown) is embedded in the layer.

(71) After step S53 or S63, two shielding films (that is, the shielding layers **150**) are then

respectively laminated on two external surfaces of the two insulation films **170** opposite to the pairs of conductors **122** (step **S54** or **S64**), to form the FFC **10** (step **S55** or **S65**). In other words, the two shielding films (that is, the shielding layers **150**) are respectively laminated on the two second insulation layers **140** opposite to the first insulation layer **110**. In an example, as shown in FIG. **27**, each shielding film (that is, the shielding layer **150**) is flush with the insulation film **170** (that is, the first insulation layer **110** and the second insulation layer **140**). In another example, viewed from a cross section of the FFC **10**, two side edges of the insulation film **170** (that is, the first insulation layer **110** and the second insulation layer **140**) and two side edges of each shielding film (that is, the shielding layer **150**) shrink inward. That is, as shown in FIG. **28**, a width of each shielding film (that is, the shielding layer **150**) is less than a width of the insulation film **170** (that is, the first insulation layer **110** and the second insulation layer **140**). The stacked structure of each shielding film (that is, the shielding layer **150**) is the same as that described in the foregoing embodiments, and therefore is not repeated.

(72) In some embodiments, as shown in FIG. **29**, a large block of FFC **10** including a large quantity of pairs of conductors **122**, that is, a plurality of FFCs **10** coupled to each other at edges (that is, side edges extending along the conductors **122a**) may be formed at a time based on steps **S51** to **S55**, and then cutting (that is, cutting off two adjacent conductors **122a**) is performed according to the quantity of conductors **122a** required by each FFC **10** to split the large block of FFC into a plurality of FFCs **10** (step **S56**).

(73) In some embodiments, as shown in FIG. **30** and FIG. **31**, if there are two shielding layers **150**, a width of the shielding layer **150** may exceed a width of the second insulation layer **140**. That is, viewed from a cross section of the FFC **10**, two side edges of each shielding layer **150** respectively exceed two side edges of the second insulation layer **140** (and the first insulation layer **110**), to make each shielding layer **150** protrude outward opposite to the second insulation layer **140**.

(74) In some embodiments, as shown in FIG. **32**, if the shielding layer **150** of the FFC **10** protrudes outward, the FFC **10** may be manufactured by using the following method. Referring to FIG. **32**, first, at least one pair of conductors **122** (that is, a plurality of conductors **130** with a jacket or at least one pair of conductors (**132**, not shown) with a jacket) is arranged in parallel between two insulation films **170** (step **S71**). Each insulation film **170** includes a first insulation layer **110** and a second insulation layer **140**. The first insulation layer **110** is adjacent to the pair of conductors **122** and is stacked on the second insulation layer **140**, that is, the first insulation layer **110** covers an internal surface of the second insulation layer **140**. The first insulation layer **110** herein is hot melt.

(75) After step **S71**, the two insulation films **170** are laminated into one insulation layer (step **S72**), and the conductors **130** with a jacket or the pairs of conductors **132** with a jacket are embedded in the insulation layer (step **S73**). Therefore, in the lamination step, the first insulation layers **110** on internal sides of the two insulation films **170** may be fused into one layer due to lamination, and the conductor **130** with a jacket or the pair of conductors (**132**) with a jacket (and the conductor **122b** with no jacket, not shown) is embedded in the layer.

(76) After step **S73**, two shielding films (that is, the shielding layers **150**) are then respectively laminated on two external surfaces of the two insulation films **170** opposite to the pairs of conductors **122**, and segments, exceeding the insulation film **170** (that is, the insulation layer), of the two shielding films (that is, the shielding layers **150**) are laminated with each other (step **S74**), to form the FFC **10** (step **S75**). In other words, the two shielding films (that is, the shielding layers **150**) are respectively laminated on the two second insulation layers **140** opposite to the first insulation layer **110**. Segments, exceeding the insulation film **170** (that is, the insulation layer), at one end of the two shielding films (that is, the shielding layers **150**) are laminated with each other, and segments, exceeding the insulation film **170** (that is, the insulation layer), at the other end of the two shielding films (that is, the shielding layers **150**) are laminated with each other. Therefore, the stacked structure of each shielding film (that is, the shielding layer **150**) is the same as that described in the foregoing embodiments, and therefore is not repeated.

(77) In some embodiments, if there is only one shielding layer **150**, the two second insulation layers **140** may be surrounded and covered by the shielding layer **150**. That is, as shown in FIG. **33** and FIG. **34**, the shielding layer **150** is envelope-shaped. In other words, the shielding layer **150** surrounds and covers a multi-layered structure in which the second insulation layer **140**, the first insulation layer **110**, and the second insulation layer **140** are sequentially stacked. In an example, as shown in FIG. **33** and FIG. **34**, after the shielding layer **150** surrounds the foregoing multi-layered structure, two ends of the shielding layer **150** are bonded with each other. In another example, as shown in FIG. **35** and FIG. **36**, after the shielding layer **150** surrounds the foregoing multi-layered structure, two ends of the shielding layer **150** may be stacked and laminated with each other.

(78) In some embodiments, as shown in FIG. **37**, if the shielding layer **150** of the FFC **10** is envelope-shaped, the FFC **10** may be manufactured by using the following method. Referring to FIG. **37**, first, at least one pair of conductors **122** (that is, a plurality of conductors **130** with a jacket or at least one pair of conductors (**132**, not shown) with a jacket) is arranged in parallel between two insulation films **170** (step **S81**). Each insulation film **170** includes a first insulation layer **110** and a second insulation layer **140**. The first insulation layer **110** is adjacent to the pair of conductors **122** and is stacked on the second insulation layer **140**, that is, the first insulation layer **110** covers an internal surface of the second insulation layer **140**. The first insulation layer **110** herein is hot melt.

(79) After step **S81**, the two insulation films **170** are laminated into one insulation layer (step **S82**), and the conductors **130** with a jacket or the pairs of conductors **132** with a jacket are embedded in the insulation layer (step **S83**). Therefore, in the lamination step, the first insulation layers **110** on internal sides of the two insulation films **170** may be fused into one layer due to lamination, and the conductor **130** with a jacket or the pair of conductors **132** with a jacket (and the conductor **122b** with no jacket, not shown) is embedded in the layer.

(80) After step **S83**, one shielding film (that is, the shielding layer **150**) surrounds the insulation layer (that is, the multi-layered structure in which the second insulation layer **140**, the first insulation layer **110**, and the second insulation layer **140** are sequentially stacked) in a circle and is laminated on the insulation layer (step **S84**), to form the FFC (step **S85**). Therefore, the stacked structure of the shielding film (that is, the shielding layer **150**) is the same as that described in the foregoing embodiments, and therefore is not repeated.

(81) In some embodiments, before step **S51**, **S61**, **S71**, or **S81**, the insulation films **170** may be formed in advance. In some embodiments, a step of forming the insulation films **170** may include: forming the first insulation layer **110** on a surface of the second insulation layer **140** by means of wet lamination or coating. The first insulation layer **110** is made of a hot melt material.

(82) In some embodiments, for example, the foregoing wet lamination may be extrusion.

(83) In some embodiments, the material of the foregoing conductors **122a** and **122b** may be aluminum (Al), copper (Cu), gold (Au), argent (Ag), or multi-layered metals. In some embodiments, the multi-layered metal conductors **122a** and **122b** may be, for example, conductors **122a** and **122b** made of copper but having an outer layer plated with at least one of the following metals: tin, nickel, alloy, argent, gold, aluminum, and the like.

(84) In some embodiments, the conductors **112a** and **112b** may be solid conductors (for example, signal-core cables) or stranded conductors (for example, multi-core cables). In some embodiments, a shape of a cross section of the conductors **112a** and **112b** may be circular, oblate, oval, rectangular, or beveled rectangular. The cross section of the conductors **112a** and **112b** is approximately perpendicular to the axis of the conductors **112a** and **112b**. In some embodiments, an aspect ratio of the cross section of the rectangular or beveled rectangular conductors **112a** and **112b** may be 6:1.

(85) For example, the conductors **112a** and **112b** may be but is not limited to circular solid conductors, circular stranded conductors, oblate solid conductors, oblate stranded conductors, oval solid conductors, oval stranded conductors, rectangular solid conductors, rectangular stranded

conductors, beveled rectangular solid conductors, or beveled rectangular stranded conductors.

(86) In some embodiments, the material of the low-k dielectric layer **124**, the material of the first insulation layer **110**, the material of the second insulation layer **140**, the material of the adhesive layer **154**, the material of the external dielectric layer **158**, and the material of the third insulation layer **156** may be high polymers.

(87) In some embodiments, the material of the low-k dielectric layer **124** may be a temperature-resistant material. Therefore, the melting point of the low-k dielectric layer **124** is higher than the melting point of the first insulation layer **110**.

(88) In some embodiments, the dielectric constant of the low-k dielectric layer **124** is lower than that of the outermost insulation layer (that is, the second insulation layer **140** shown in FIG. **1** to FIG. **8** or the third insulation layer **156** shown in FIG. **13** to FIG. **24**). In some embodiments, the dielectric constant of the low-k dielectric layer **124** is lower than 2.5. The material of the low-k dielectric layer **124** may be fluorinated ethylene propylene (FEP), polypropylene (PP), polyolefin (PO), polyethylene (PE), liquid crystal polymer (LCP), or the like.

(89) In some embodiments, microbubbles may be formed in the low-k dielectric layer **124** through a foaming technology. In some embodiments, a medium in the microbubbles in the low-k dielectric layer **124** may be air.

(90) In some embodiments, a shortest distance between the edge of the conductor **122a** and the external edge of the low-k dielectric layer **124** is less than or equal to a radius of the conductor **122a**.

(91) In some embodiments, the material of the foregoing first insulation layer **110** may be PO, thermoplastic elastomer (TPE), thermoplastic polyurethane (TPU), or the like. Preferably, the material of the first insulation layer **110** may be a low-k material. The dielectric constant of the first insulation layer **110** may be less than 2.5.

(92) In some embodiments, the material of the second insulation layer **140** shown in FIG. **13** to FIG. **24** may be polyethylene terephthalate (PET), FEP, polyphenylene sulfide (PPS), LCP, or the like. Preferably, the material of the second insulation layer **140** shown in FIG. **13** to FIG. **24** may be a low-k material. The dielectric constant of the second insulation layer **140** shown in FIG. **13** to FIG. **24** may be less than 2.5.

(93) In some embodiments, a main component of the material of the adhesive layer **154** may be PET, acrylic, silicone, PP, PO, TPE, TPU, or the like. Preferably, the main component of the material of the adhesive layer **154** may be a low-k material, such as PP, PO, TPE, or TPU. The dielectric constant of the adhesive layer **154** may be less than 2.5.

(94) In some embodiments, the material of the external dielectric layer **158** may be PET, acrylic, silicone, PP, PO, TPE, TPU or the like. Preferably, the material of the external dielectric layer **158** may be a low-k material, such as PP, PO, TPE, or TPU. The dielectric constant of the external dielectric layer **158** may be less than 2.5.

(95) In some embodiments, the conductors **130** with a jacket or the pairs of conductors **132** with a jacket may be applied to high-speed signal transmission. In other words, the conductors **130** with a jacket or the pairs of conductors **132** with a jacket may be configured to transmit signals at a high speed. In some embodiments, a rate used for transmitting signals by the conductors **130** with a jacket or the pairs of conductors **132** with a jacket may be equal to or greater than 10 Gbps.

(96) In addition, the conductors **122b** with no jacket are applied to non-high-speed signal transmission. For example, the conductors **122b** with no jacket may be configured to transmit power or connect to ground.

(97) In some embodiments, the FFC **10** may include only the conductors **130** with a jacket or only the pairs of conductors **132** with a jacket, and does not include the conductors **122b** with no jacket. Therefore, the conductors **112a** in the FFC **10** may be arranged at equal intervals.

(98) In some embodiments, the FFC **10** may include the conductors **130** with a jacket or the pairs of conductors **132** with a jacket, and include the conductors **122b** with no jacket. Therefore, the

conductors **112a** and **112b** in the FFC **10** may be arranged at equal intervals.

(99) In some embodiments, referring to FIG. **38**, the conductors **112a** and **112b** of the FFC **10** according to any one of the foregoing embodiments may be directly soldered on a circuit board **20**, to electrically connect the conductors **112a** and **112b** to a circuit **22** on the circuit board **20**.

(100) In some embodiments, referring to FIG. **39**, one end or two ends of the FFC **10** according to any one of the foregoing embodiments may be coupled to an electrical connector component **30**, to form a signal transmission wire (that is, a signal transmission device). Therefore, the conductors **112a** and **112b** of the FFC **10** are electrically connected to a conductive terminal of the electrical connector component **30**.

(101) Based on above, according to the FFC, the manufacturing method thereof, and the signal transmission device of the present invention, the conductor **112a** clamped in an insulation layer (that is, the first insulation layer **110**) is covered by a layer of low-k material (that is, the low-k dielectric layer **124**), to prevent a high-k material from affecting the signal transmission, to further improve quality of high-speed signal transmission relatively. In addition, according to the embodiments of the present invention, the process is convenient and low in cost.

## Claims

1. A manufacturing method for an FFC, comprising: arranging at least one pair of conductors in parallel between two hot melt insulation films, wherein each of the at least one pair of conductors is covered by a low-k dielectric layer; laminating the two hot melt insulation films into a hot melt insulation layer, wherein a melting point of the low-k dielectric layer is higher than a melting point of the two hot melt insulation films, the two hot melt insulation films are fused into the hot melt insulation layer, and each of the at least one pair of conductors covered by the low-k dielectric layer is embedded in the hot melt insulation layer; and laminating at least one shielding layer on two surfaces of the hot melt insulation layer.

2. The manufacturing method for the FFC according to claim 1, wherein the at least one shielding layer is two shielding layers, and the step of laminating the at least one shielding layer on the two surfaces of the insulation layer comprises: laminating the two shielding layers on the two surfaces of the insulation layer respectively, wherein a width of each of the two shielding layers is equal to a width of the insulation layer.

3. The manufacturing method for the FFC according to claim 1, wherein the at least one shielding layer is two shielding layers, and the step of laminating the at least one shielding layer on the two surfaces of the insulation layer comprises: laminating the two shielding layers on the two surfaces of the insulation layer respectively, wherein two side edges of the insulation layer respectively exceed two side edges of each of the two shielding layers.

4. The manufacturing method for the FFC according to claim 1, wherein the at least one shielding layer is two shielding layers, and the step of laminating the at least one shielding layer on the two surfaces of the insulation layer comprises: laminating the two shielding layers on the two surfaces of the insulation layer respectively, wherein two edges of each of the two shielding layers respectively exceed two side edges of the insulation layer; and laminating segments of the two shielding layers exceeding the insulation layer with each other.

5. The manufacturing method for the FFC according to claim 1, wherein the at least one shielding layer is one shielding layer, and the step of laminating the at least one shielding layer on the two surfaces of the insulation layer comprises: laminating the shielding layer on the two surfaces of the insulation layer, wherein the insulation layer is surrounded with the shielding layer.

6. A manufacturing method for an FFC, comprising: arranging at least one pair of conductors in parallel between two hot melt insulation films, wherein each of the at least one pair of conductors comprises a plurality of conductors, and the plurality of conductors in a same pair of the at least one pair of conductors are collectively and directly covered by and surrounded with corresponding

one of at least one low-k dielectric layer; laminating and fusing the two hot melt insulation films, which the at least one pair of conductors and the at least one low-k dielectric layer are sandwiched between, into a hot melt insulation layer, wherein a melting point of the at least one low-k dielectric layer is higher than a melting point of the two hot melt insulation films, and each of the plurality of conductors covered by the corresponding low-k dielectric layer is embedded in the hot melt insulation layer; and laminating at least one shielding layer on two surfaces of the hot melt insulation layer.

7. The manufacturing method for the FFC according to claim 6, wherein the at least one shielding layer is two shielding layers, and the step of laminating the at least one shielding layer on the two surfaces of the insulation layer comprises: laminating the two shielding layers on the two surfaces of the insulation layer respectively, wherein a width of each of the two shielding layers is equal to a width of the insulation layer.

8. The manufacturing method for the FFC according to claim 6, wherein the at least one shielding layer is two shielding layers, and the step of laminating the at least one shielding layer on the two surfaces of the insulation layer comprises: laminating the two shielding layers on the two surfaces of the insulation layer respectively, wherein two side edges of the insulation layer respectively exceed two side edges of each of the two shielding layers.

9. The manufacturing method for the FFC according to claim 6, wherein the at least one shielding layer is two shielding layers, and the step of laminating the at least one shielding layer on the two surfaces of the insulation layer comprises: laminating the two shielding layers on the two surfaces of the insulation layer respectively, wherein two edges of each of the two shielding layers respectively exceed two side edges of the insulation layer; and laminating segments of the two shielding layers exceeding the insulation layer with each other.

10. The manufacturing method for the FFC according to claim 6, wherein the at least one shielding layer is one shielding layer, and the step of laminating the at least one shielding layer on the two surfaces of the insulation layer comprises: laminating the shielding layer on the two surfaces of the insulation layer, wherein the insulation layer is surrounded with the shielding layer.

11. A manufacturing method for an FFC, comprising: arranging at least one pair of conductors in parallel between two insulating-shielding films, wherein each of the at least one pair of conductors comprises a plurality of conductors, the at least one pair of conductors is covered by at least one low-k dielectric layer, and each of the two insulating-shielding films comprises a metal layer, an external dielectric layer on the metal layer and adjacent to the at least one pair of conductors, and a hot melt insulation layer on the external dielectric layer and adjacent to the at least one pair of conductors; and laminating the two insulating-shielding films into an insulating-shielding layer, wherein a melting point of the low-k dielectric layer is higher than a melting point of the hot melt insulation layers of the two insulating-shielding films; wherein the hot melt insulation layers of the two insulating-shielding films are fused into one insulation layer, and the at least one pair of conductors and the at least one low-k dielectric layer are embedded in the insulating-shielding layer.

12. The manufacturing method for an FFC according to claim 11, wherein a pair number of the at least one pair of conductors is the same as a number of the at least one low-k dielectric layer, and the at least one pair of conductors is respectively covered by the at least one low-k dielectric layer.

13. The manufacturing method for an FFC according to claim 11, wherein a number of the at least one low-k dielectric layer is a plurality, and the plurality of conductors are one by one covered by the plurality of low-k dielectric layer.

---