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(54) **COMMUNICATION METHOD, APPARATUS,
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MEDIUM**

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(71) Applicant: **BEIJING XIAOMI MOBILE
SOFTWARE CO., LTD.**, Beijing (CN)

(72) Inventor: **Xiandong DONG**, Beijing (CN)

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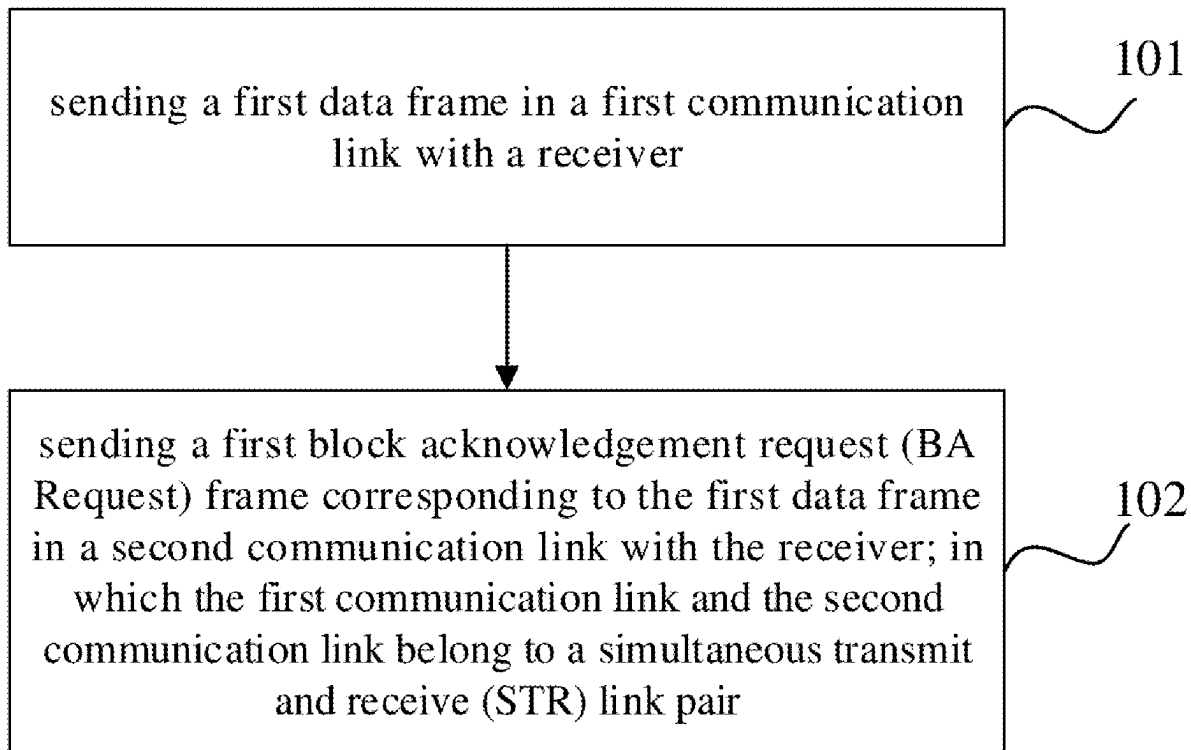
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ABSTRACT

A communication method, performed by a transmitter, includes: sending a first data frame in a first communication link with a receiver; and sending a first block acknowledgement request (BA Request) frame corresponding to the first data frame in a second communication link with the receiver. The first communication link and the second communication link belong to a simultaneous transmit and receive (STR) link pair.



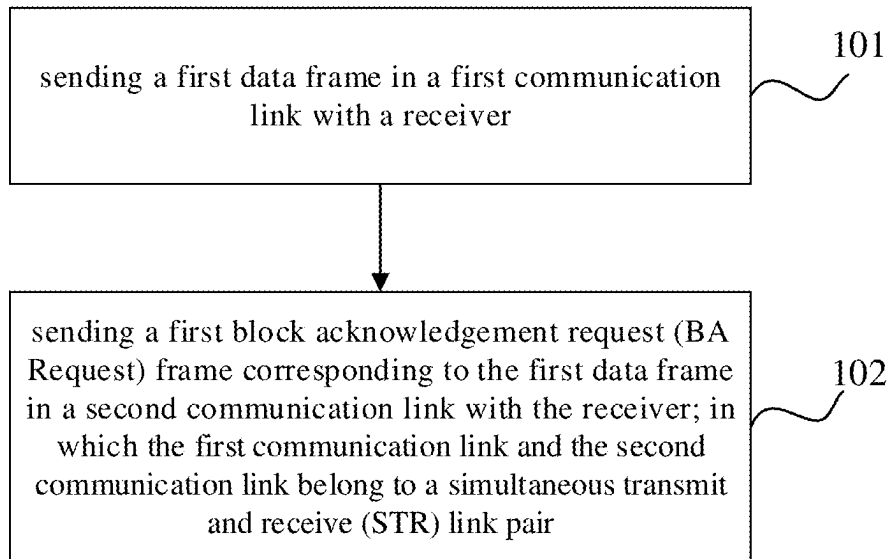


FIG. 1

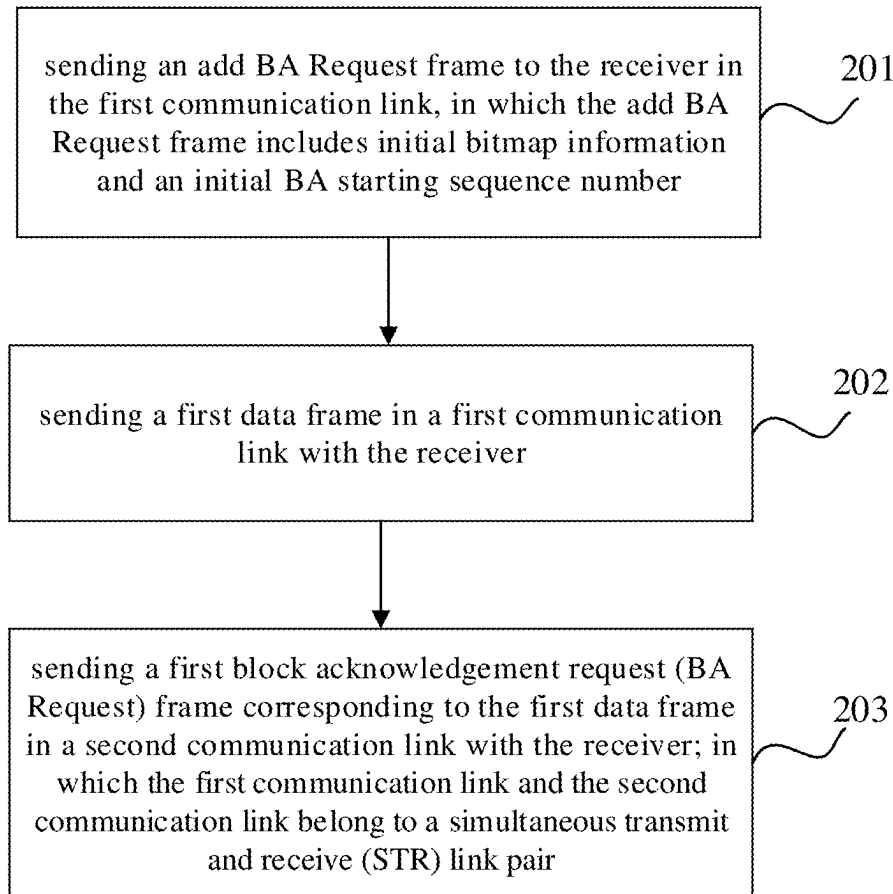


FIG. 2

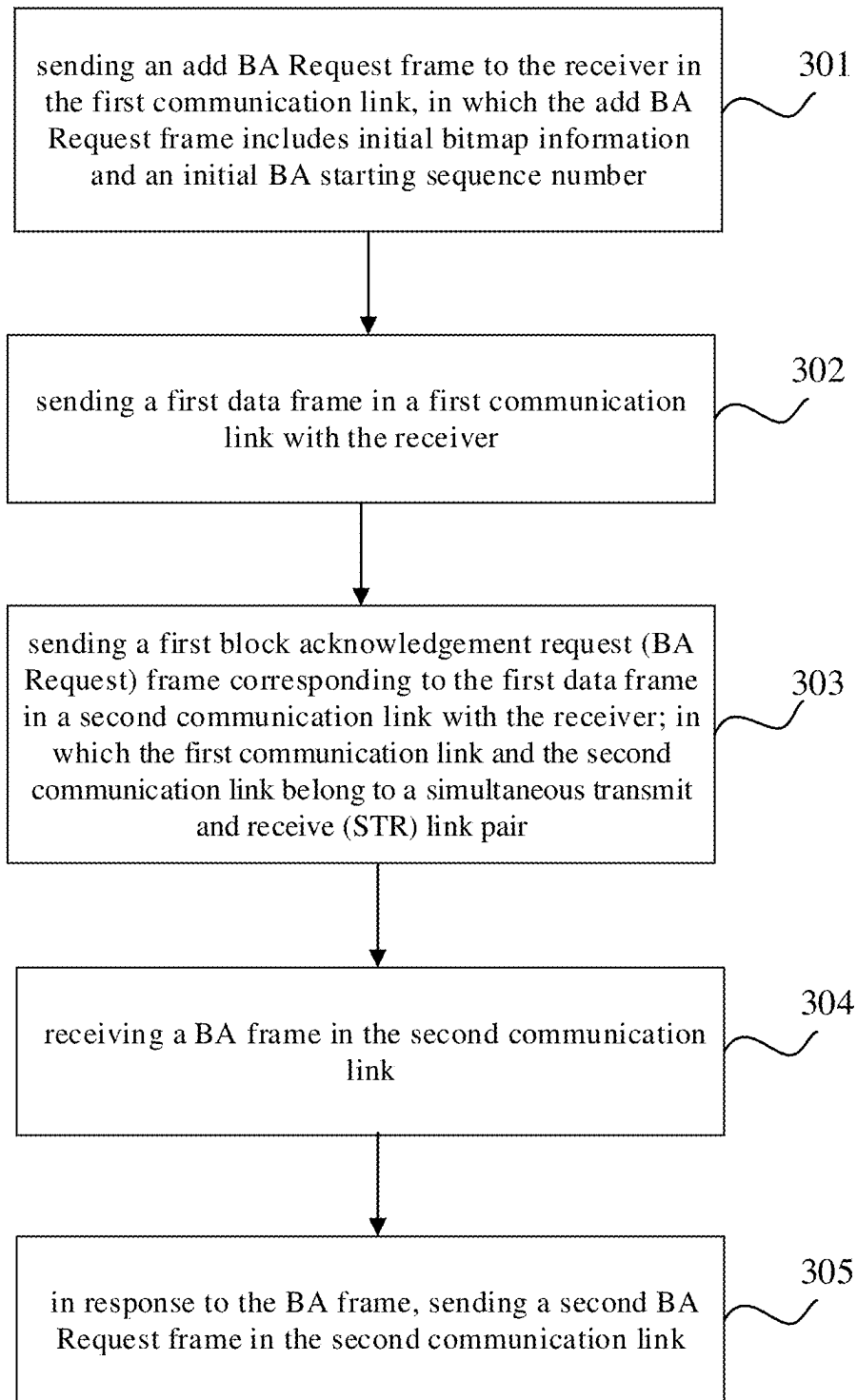


FIG. 3

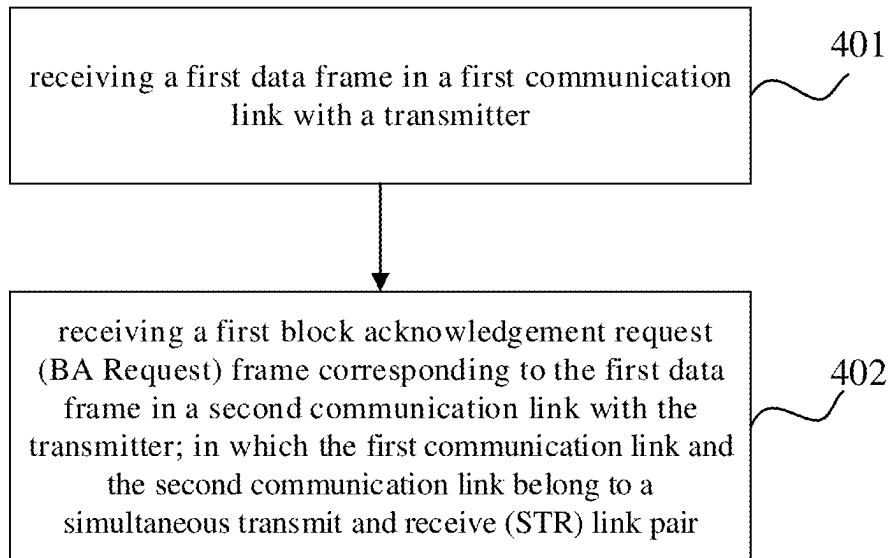


FIG. 4

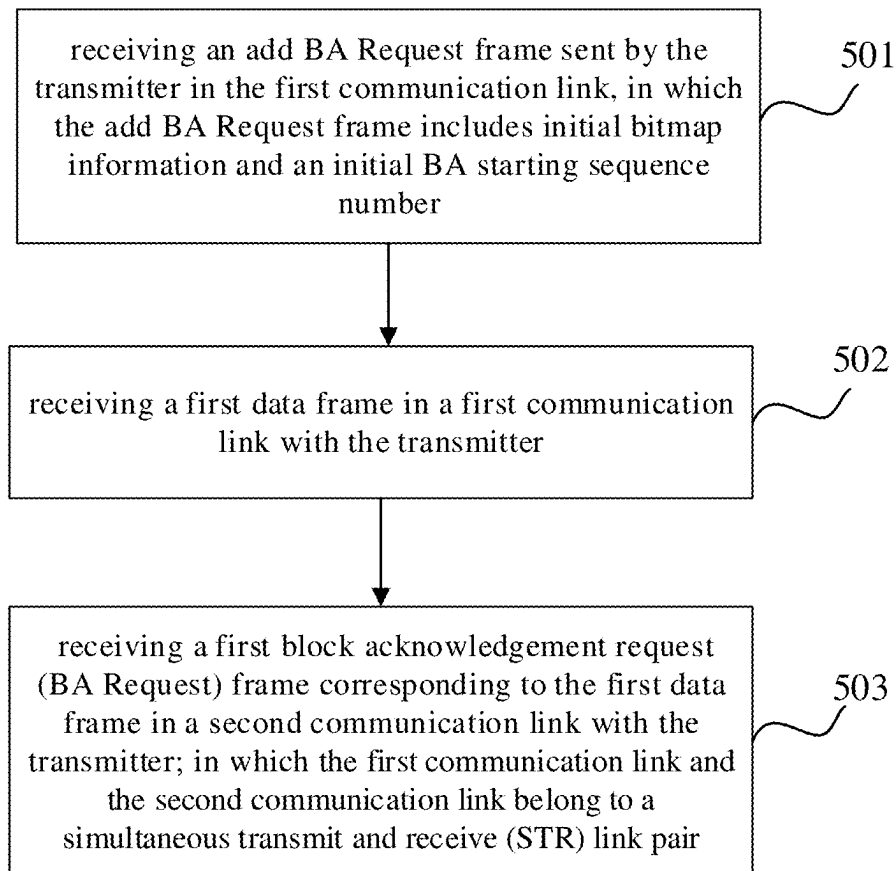


FIG. 5

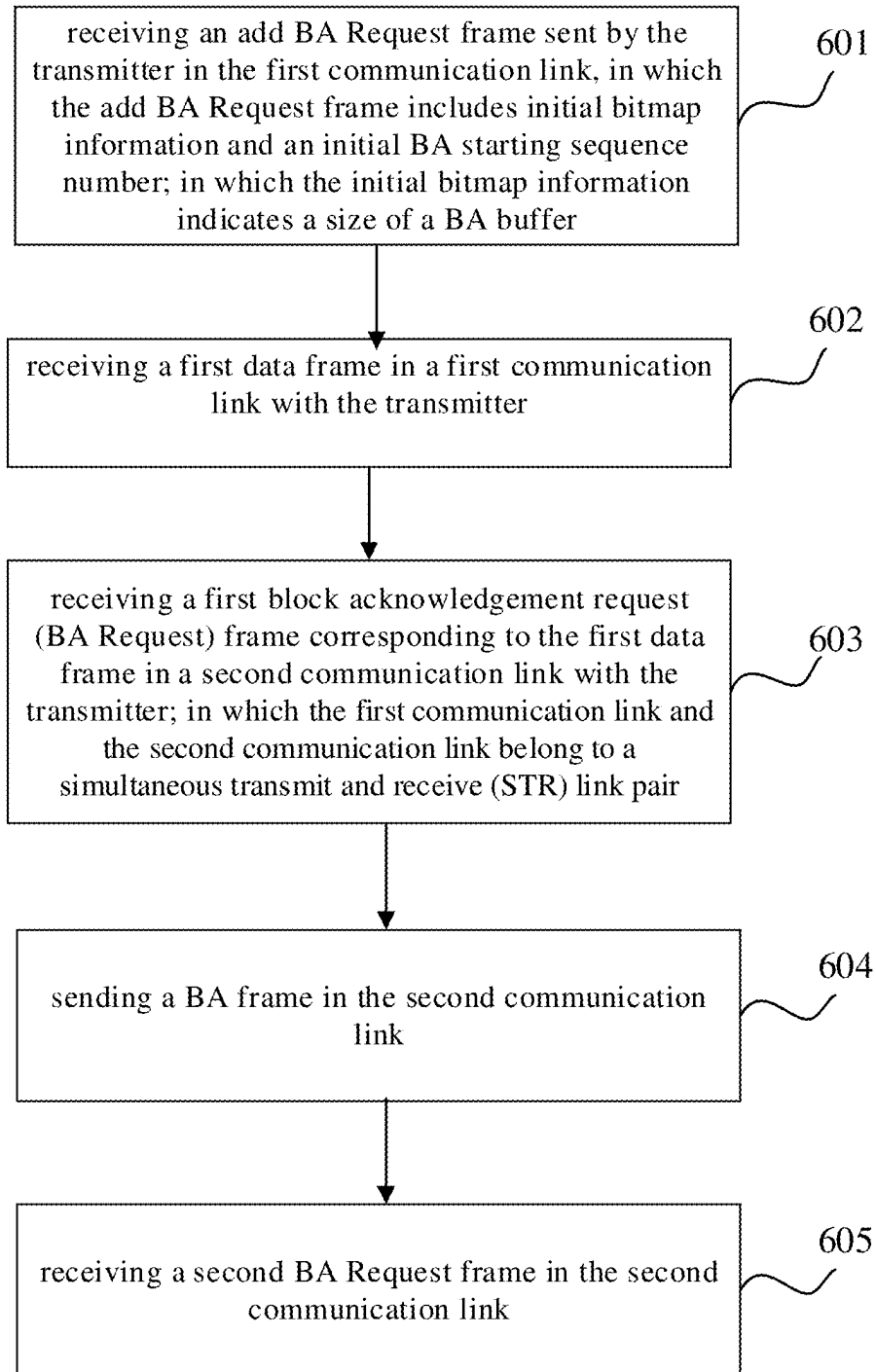


FIG. 6

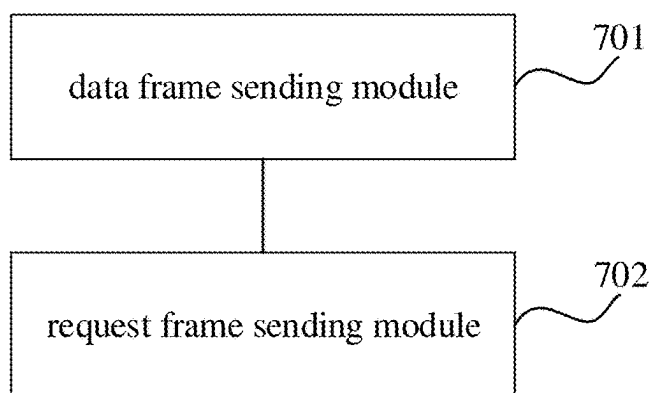


FIG. 7

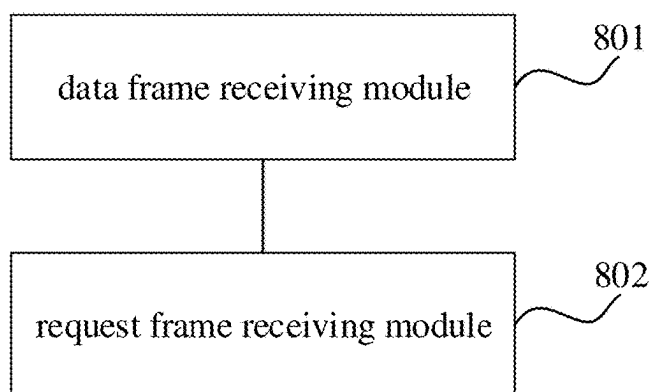


FIG. 8

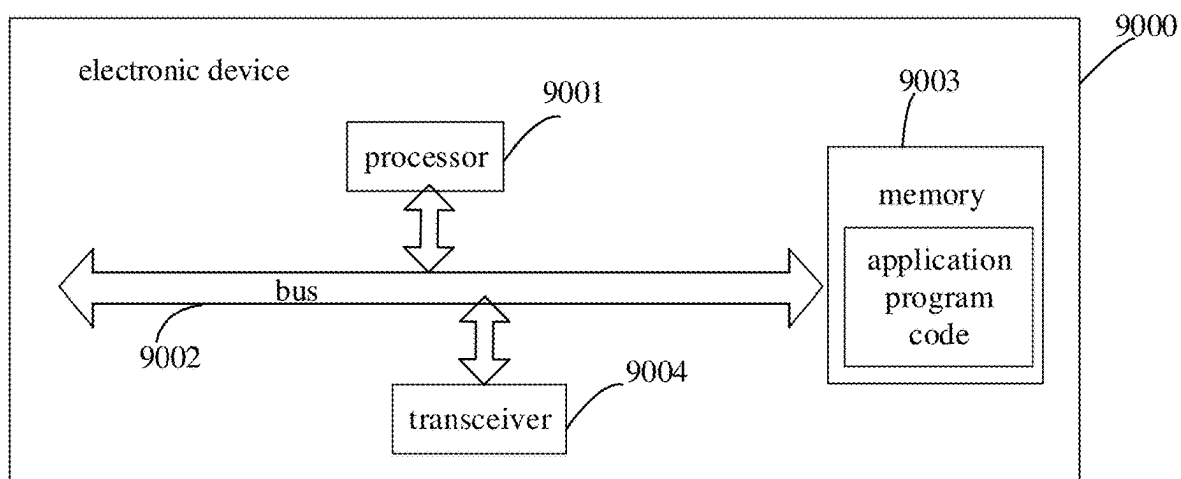


FIG. 9

**COMMUNICATION METHOD, APPARATUS,
ELECTRONIC DEVICE, AND STORAGE
MEDIUM**

**CROSS-REFERENCE TO RELATED
APPLICATION**

[0001] This application is a U.S. National Phase application of the International Patent Application No. PCT/CN2022/085959, filed Apr. 8, 2022, the entire contents of which are incorporated herein by reference.

TECHNICAL FIELD

[0002] Embodiments of the present disclosure relate to the field of mobile communication technologies, and more particularly, they relate to a communication method, a communication apparatus, an electronic device, and a storage medium.

BACKGROUND

[0003] With the rapid development of the mobile communication technology, a wireless fidelity (Wi-Fi) technology has made a great progress in terms of transmission rate and throughput. At present, research contents of the Wi-Fi technology include, for example, a 320 MHz bandwidth transmission, an aggregation and coordination of multiple frequency bands, etc. Main application scenarios of the Wi-Fi technology include, for example, a video transmission, an augmented reality (AR), a virtual reality (VR), etc.

[0004] Specifically, the aggregation and coordination of the multiple frequency bands refer to communication between devices at 2.4 GHz, 5.8 GHz, 6 GHz and other frequency bands at the same time. For the scenario where the devices communicate at the multiple frequency bands at the same time, a new media access control (MAC) mechanism needs to be defined for management. In addition, the aggregation and coordination of the multiple frequency bands are expected to support a low-latency transmission.

[0005] At present, a maximum bandwidth supported in a technology of the aggregation and coordination of the multiple frequency bands is 320 MHz (i.e. 160 MHz+160 MHz). In addition, 240 MHz (i.e. 160 MHz+80 MHz) and other bandwidths supported by existing standards may also be supported.

[0006] In the Wi-Fi technology currently studied, 1024 MAC service data units (MSDUs) may be fed back in one link under an existing block acknowledgement (BA) mechanism; however, for a low-latency service, the existing BA mechanism takes a long time and may not be able to meet a low-latency transmission requirement.

SUMMARY

[0007] In an aspect, an embodiment of the present disclosure provides a communication method, which is performed by a transmitter, and includes:

[0008] sending a first data frame in a first communication link with a receiver; and

[0009] sending a first block acknowledgement request (BA Request) frame corresponding to the first data frame in a second communication link with the receiver; in which the first communication link and the second communication link belong to a simultaneous transmit and receive (STR) link pair.

[0010] In another aspect, an embodiment of the present disclosure further provides a communication method, which is performed by a receiver, and includes:

[0011] receiving a first data frame in a first communication link with a transmitter; and

[0012] receiving a first block acknowledgement request (BA Request) frame corresponding to the first data frame in a second communication link with the transmitter; in which the first communication link and the second communication link belong to a simultaneous transmit and receive (STR) link pair.

[0013] An embodiment of the present disclosure further provides an electronic device, which includes a memory, a processor, and a computer program stored in the memory and executable on the processor. The processor is configured to perform the method as described above.

[0014] An embodiment of the present disclosure further provides a computer-readable storage medium, which has stored therein a computer program that, when executed by a processor, causes the method according to embodiments of the present disclosure to be implemented.

[0015] Additional aspects and advantages of embodiments of the present disclosure will be given in part in the following descriptions, which will become apparent from the following descriptions, or will be learned from the practice of the present disclosure.

BRIEF DESCRIPTION OF THE DRAWINGS

[0016] In order to clearly illustrate the technical solutions in embodiments of the present disclosure, the accompanying drawings to be used in the description of embodiments of the present disclosure will be briefly introduced below. Apparently, the drawings in the following description only show some embodiments of the present disclosure. For those of ordinary skill in the art, other drawings may also be obtained according to these drawings without any creative effort.

[0017] FIG. 1 is a first flowchart of a communication method according to an embodiment of the present disclosure.

[0018] FIG. 2 is a second flowchart of a communication method according to an embodiment of the present disclosure.

[0019] FIG. 3 is a third flowchart of a communication method according to an embodiment of the present disclosure.

[0020] FIG. 4 is a fourth flowchart of a communication method according to an embodiment of the present disclosure.

[0021] FIG. 5 is a fifth flowchart of a communication method according to an embodiment of the present disclosure.

[0022] FIG. 6 is a sixth flowchart of a communication method according to an embodiment of the present disclosure.

[0023] FIG. 7 is a first schematic block diagram of a communication apparatus according to an embodiment of the present disclosure.

[0024] FIG. 8 is a second schematic block diagram of a communication apparatus according to an embodiment of the present disclosure.

[0025] FIG. 9 is a schematic block diagram of an electronic device according to an embodiment of the present disclosure.

DETAILED DESCRIPTION

[0026] The term “and/or” in embodiments of the present disclosure describes an association relationship of associated objects, indicating that three relationships may exist. For example, A and/or B may represent three situations where: A exists alone, A and B exist simultaneously, and B exists alone. The character “/” generally represents that objects associated before and after the character are in an “or” relationship.

[0027] In embodiments of the present disclosure, the term “a/the plurality of” refers to two or more than two, and other quantifiers are similar thereto.

[0028] Reference will now be made in detail to illustrative embodiments, examples of which are illustrated in the accompanying drawings. The following description refers to the accompanying drawings in which the same numbers in different drawings represent the same or similar elements unless otherwise represented. The implementations set forth in the following description of illustrative embodiments do not represent all implementations consistent with the present disclosure. Instead, they are merely examples of devices and methods consistent with some aspects of the present disclosure as recited in the appended claims.

[0029] Terms used in the present disclosure are only for the purpose of describing specific embodiments, but should not be construed to limit the present disclosure. As used in the present disclosure and the appended claims, “a/an”, “said” and “the” in singular forms are intended to include plural forms, unless clearly indicated in the context otherwise. It should also be understood that, the term “and/or” used herein represents and contains any or all possible combinations of one or more associated items listed.

[0030] It should be understood that, although terms such as “first,” “second” and “third” may be used in the present disclosure for describing various information, these information should not be limited by these terms. These terms are only used for distinguishing information of the same type from each other. For example, first information may also be referred to as second information, and similarly, the second information may also be referred to as the first information, without departing from the scope of the present disclosure. For example, as used herein, the term “if” may be construed to mean “when”, “upon” or “in response to determining” depending on the context.

[0031] The technical solutions in embodiments of the present disclosure will be described clearly and completely below with reference to the accompanying drawings in embodiments of the present disclosure. Apparently, the described embodiments are only some embodiments of the present disclosure, rather than all embodiments of the present disclosure. All other embodiments obtained by those of ordinary skill in the art based on embodiments of the present disclosure without making creative works belong to the protection scope of the present disclosure.

[0032] Embodiments of the present disclosure provide a communication method, a communication apparatus, an electronic device, and a storage medium to solve a problem that an existing BA mechanism is difficult to meet a low-latency transmission requirement.

[0033] The method and the apparatus are based on a same application concept. Since the method and the apparatus solve the problem in a similar principle, regarding an

implementation of the apparatus and the method, reference may be made to each other, and repeated parts thereof will not be elaborated.

[0034] As shown in FIG. 1, an embodiment of the present disclosure provides a communication method. Optionally, the method may be performed by an electronic device.

[0035] The electronic device is a transmitter, such as an access point (AP) device or a station (STA) device. The method may include steps as follows.

[0036] In step 101, a first data frame is sent in a first communication link with a receiver.

[0037] In a multi-link scenario, in order to improve a spectrum utilization and adapt to a high-density communication environment, two communication manners, i.e. a non-simultaneous transmit and receive (NSTR) and a simultaneous transmit and receive (STR), are introduced. In an STR scenario, a transmission and a reception may be performed respectively and simultaneously in different communication links, and there is no interference between transmissions of individual communication links. However, under normal circumstances, if a BA feedback is performed after a transmission of 1024 consecutive MSDUs is completed, the transmitter needs to perform retransmission if the 1024 consecutive MSDUs are not received correctly, which will result in a longer transmission time. Therefore, in an embodiment of the present disclosure, the transmitter sends the first data frame in the first communication link (communication link, i.e., Link) with the receiver, and the first data frame may be, for example, an aggregation of the MSDUs (A-MSDU) or an aggregation of physical protocol data units (PPDU). The PPDU includes a physical layer header preamble (PLCP header preamble) and a MAC protocol data unit (MPDU) and/or an aggregation of the MPDUs (A-MPDU).

[0038] Optionally, the first communication link is a communication link for establishing a BA mechanism (a negotiation of an initial BA length) between the transmitter and the receiver.

[0039] In step 102, a first block acknowledgement request (BA Request) frame corresponding to the first data frame is sent in a second communication link with the receiver; in which the first communication link and the second communication link belong to a simultaneous transmit and receive (STR) link pair.

[0040] The first communication link and the second communication link belong to a simultaneous transmit and receive (STR) link pair. When the transmitter sends the first data frame in the first communication link, and the first BA Request frame is sent in the second communication link at the same time, the first BA Request frame is configured to request the receiver to perform the BA feedback on a received first data frame. In this way, the receiver may perform the BA feedback while the transmitter is transmitting the data frame, without waiting for a completion of transmission of the 1024 MSDUs, thereby shortening a transmission time.

[0041] In embodiments of the present disclosure, the transmitter first sends the first data frame in the first communication link with the receiver, and then sends the first BA Request frame corresponding to the first data frame in the second communication link with the receiver. A BA feedback is performed while transmitting the first data frame, thereby shortening the transmission time, meeting a transmission requirement of a low-latency service, improv-

ing a spectrum utilization, and adapting to a high-density communication environment. Embodiments of the present disclosure solve a problem that an existing BA mechanism is difficult to meet a low-latency transmission requirement.

[0042] Referring to FIG. 2, an embodiment of the present disclosure further provides a communication method. Optionally, the method may be performed by an electronic device. The electronic device is a transmitter, such as an AP or an STA. The method may include steps as follows.

[0043] In step 201, an add BA Request frame is sent to the receiver in the first communication link, and the add BA Request frame includes initial bitmap information and an initial BA starting sequence number; in which the initial bitmap information indicates a size of a BA buffer.

[0044] When establishing a BA mechanism (a negotiation of an initial BA length) with the receiver, the transmitter first sends the add BA Request frame to the receiver in the first communication link, and the add BA Request frame carries the initial bitmap information. The initial bitmap information is the size of the BA buffer, that is, an amount of data accommodated in the BA buffer, such as 32 bytes, 64 bytes or 128 bytes. In addition, the add BA Request frame also includes the initial BA starting sequence number, and the starting sequence number is configured to identify a starting position where the transmitter sends the data frame.

[0045] In step 202, a first data frame is sent in a first communication link with the receiver.

[0046] The transmitter sends the first data frame in the first communication link with the receiver. The first data frame may be, for example, an AMSDU or an aggregation of physical protocol data unit (APPDU).

[0047] In step 203, a first block acknowledgement request (BA Request) frame corresponding to the first data frame is sent in a second communication link with the receiver; in which the first communication link and the second communication link belong to a simultaneous transmit and receive (STR) link pair.

[0048] The first communication link and the second communication link belong to a simultaneous transmit and receive (STR) link pair. When the transmitter sends the first data frame in the first communication link, the first BA Request frame is sent in the second communication link at the same time. In this way, the receiver may perform the BA feedback while the transmitter is transmitting the data frame, without waiting for a completion of transmission of 1024 MSDUs, thereby shortening a transmission time.

[0049] In an optional embodiment, after sending the add BA Request frame to the receiver in the first communication link, the method includes:

[0050] receiving an add BA response frame sent by the receiver.

[0051] After receiving the add BA Request frame, the receiver will feed back the add BA response frame to the transmitter, identifying that it supports the add BA Request frame.

[0052] In an optional embodiment, the first BA Request frame includes a first BA starting sequence number, and/or first bitmap information.

[0053] The first BA starting sequence number is the same as the initial BA starting sequence number; in a first transmission after the BA mechanism is established, the BA starting sequence number in the BA Request frame is the same as the initial BA starting sequence number.

[0054] The number of bytes indicated by the first bitmap information is less than that indicated by the initial bitmap information, that is, the first bitmap information is less than a length of the buffer size negotiated in establishing the BA mechanism. For example, in establishing the BA mechanism, a length of the BA bitmap is 128 bytes, and the first bitmap information indicates that a required feedback length is 32 bytes. The receiver may feed back a receiving status of the A-MSDU with a length of 32 bytes that have been transmitted, without having to wait for a completion of transmission of 128 bytes before performing the BA feedback. In this way, the bytes indicated by the initial bitmap information are transmitted multiple times, and the BA feedback is performed for each transmitted data frame to shorten an overall transmission time.

[0055] Referring to FIG. 3, an embodiment of the present disclosure further provides a communication method. Optionally, the method may be performed by an electronic device. The electronic device is a transmitter, such as an AP or an STA. The method may include steps as follows.

[0056] In step 301, an add BA Request frame is sent to the receiver in the first communication link, and the add BA Request frame includes initial bitmap information and an initial BA starting sequence number; in which the initial bitmap information indicates a size of a BA buffer.

[0057] In step 302, a first data frame is sent in a first communication link with the receiver.

[0058] In step 303, a first block acknowledgement request (BA Request) frame corresponding to the first data frame is sent in a second communication link with the receiver; in which the first communication link and the second communication link belong to a simultaneous transmit and receive (STR) link pair.

[0059] In step 304, a BA frame is received in the second communication link.

[0060] The transmitter receives the BA frame fed back by the receiver, determines whether the receiver has successfully received all contents of the first data frame according to a feedback of the BA frame, and resends the BA Request frame of the data frame that was not successfully received, i.e., a second BA Request frame, in the second communication link, as shown in step 305. It is understandable that if all contents of the first data frame are successfully received, there is no need to resend it.

[0061] In step 305, in response to the BA frame, a second BA Request frame is sent in the second communication link.

[0062] In an optional embodiment, the second BA Request frame includes a second BA starting sequence number; and/or second bitmap information.

[0063] The second BA starting sequence number is a sum of the initial BA starting sequence number and the initial bitmap information, that is, a value of a starting sequence number in the second BA Request frame is a sum of the BA bitmap length in the BA Request frame sent for a first time and a value of the starting sequence number in the BA Request frame sent for the first time.

[0064] The number of bytes indicated by the second bitmap information is less than that indicated by the initial bitmap information. For example, in establishing the BA mechanism, the length of the BA bitmap is 128 bytes, and the second bitmap information indicates that a required feedback length may be 32 bytes or 64 bytes; on the other hand, if the first bitmap information indicates that the required feedback length is 32 bytes, then the starting

sequence number in the second BA Request frame is 33th, i.e. $(32+1)^{th}$. In this way, the bytes indicated by the initial bitmap information are transmitted multiple times, and the BA feedback is performed for each transmitted data frame to shorten an overall transmission time.

[0065] In embodiments of the present disclosure, the transmitter first sends the first data frame in the first communication link with the receiver, and then sends the first BA Request frame corresponding to the first data frame in the second communication link with the receiver. A BA feedback is performed while transmitting the first data frame, thereby shortening the transmission time, meeting a transmission requirement of a low-latency service, improving a spectrum utilization, and adapting to a high-density communication environment.

[0066] Referring to FIG. 4, an embodiment of the present disclosure further provides a communication method. Optionally, the method may be performed by an electronic device. The electronic device is a receiver, such as an AP or an STA. The method may include steps as follows.

[0067] In step 401, a first data frame is received in a first communication link with a transmitter.

[0068] In a multi-link scenario, in order to improve a spectrum utilization and adapt to a high-density communication environment, two communication manners, i.e. an NSTR and an STR, are introduced. In an STR scenario, a transmission and a reception may be performed respectively and simultaneously in different communication links, and there is no interference between transmissions of individual communication links. However, under normal circumstances, if a BA feedback is performed after a transmission of 1024 consecutive MSDUs is completed, the transmitter needs to perform retransmission if the 1024 consecutive MSDUs are not received correctly, which will result in a longer transmission time. Therefore, in an embodiment of the present disclosure, the transmitter sends the first data frame in the first communication link with the receiver, and the receiver receives the first data frame in the first communication link with the transmitter. The first data frame may be, for example, an AMSDU or an APPDU.

[0069] Optionally, the first communication link is a communication link for establishing a BA mechanism (a negotiation of an initial BA length) between the transmitter and the receiver.

[0070] In step 402, a first block acknowledgement request (BA Request) frame corresponding to the first data frame is received in a second communication link with the transmitter; in which the first communication link and the second communication link belong to a simultaneous transmit and receive (STR) link pair.

[0071] The first communication link and the second communication link belong to a simultaneous transmit and receive (STR) link pair. The receiver receives the first data frame in the first communication link with the transmitter, and receives the first BA Request frame in the second communication link at the same time, and subsequently performs the BA feedback in the second communication link. In this way, the receiver may perform the BA feedback while the transmitter is transmitting the data frame, without waiting for a completion of transmission of the 1024 MSDUs, thereby shortening a transmission time.

[0072] In embodiments of the present disclosure, the receiver receives the first data frame in the first communication link with the transmitter; receives the first block

acknowledgment request (BA Request) frame corresponding to the first data frame in the second communication link with the transmitter; and performs a BA feedback while transmitting the first data frame, thereby shortening the transmission time, meeting a transmission requirement of a low-latency service, improving a spectrum utilization, and adapting to a high-density communication environment. Embodiments of the present disclosure solve a problem that an existing BA mechanism is difficult to meet a low-latency transmission requirement.

[0073] Referring to FIG. 5, an embodiment of the present disclosure further provides a communication method. Optionally, the method may be performed by an electronic device. The electronic device is a receiver, such as an AP or an STA. The method may include steps as follows.

[0074] In step 501, an add BA Request frame sent by the transmitter is received in the first communication link, and the add BA Request frame includes initial bitmap information and an initial BA starting sequence number; in which the initial bitmap information indicates a size of a BA buffer.

[0075] When establishing a BA mechanism (a negotiation of an initial BA length) with the transmitter, the receiver first receives the add BA Request frame sent by the transmitter in the first communication link, and obtains the initial bitmap information carried in the add BA Request frame. The initial bitmap information is the size of the BA buffer, that is, an amount of data accommodated in the BA buffer, such as 32 bytes, 64 bytes or 128 bytes. In addition, the add BA Request frame also includes the initial BA starting sequence number, and the starting sequence number identifies a starting position where the transmitter sends the data frame.

[0076] In step 502, a first data frame is received in a first communication link with the transmitter.

[0077] The receiver receives the first data frame in the first communication link with the transmitter. The first data frame may be, for example, an AMSDU or an APPDU.

[0078] In step 503, a first block acknowledgement request (BA Request) frame corresponding to the first data frame is received in a second communication link with the transmitter; in which the first communication link and the second communication link belong to a simultaneous transmit and receive (STR) link pair.

[0079] The receiver receives the first data frame in the first communication link with the transmitter, and receives the first BA Request frame in the second communication link at the same time, and subsequently performs the BA feedback in the second communication link.

[0080] In this way, the receiver may perform the BA feedback while the transmitter is transmitting the data frame, without waiting for a completion of transmission of 1024 MSDUs, thereby shortening a transmission time.

[0081] In an optional embodiment, after receiving the add BA Request frame sent by the transmitter in the first communication link, the method includes:

[0082] sending an add BA response frame to the receiver.

[0083] After receiving the add BA Request frame, the receiver will feed back the add BA response frame to the transmitter, identifying that it supports the add BA Request frame.

[0084] In an optional embodiment, the first BA Request frame includes a first BA starting sequence number, and/or first bitmap information.

[0085] The first BA starting sequence number is the same as the initial BA starting sequence number; in a first trans-

mission after the BA mechanism is established, the BA starting sequence number in the BA Request frame is the same as the initial BA starting sequence number.

[0086] The number of bytes indicated by the first bitmap information is less than that indicated by the initial bitmap information, that is, the first bitmap information is less than a length of the buffer size negotiated in establishing the BA mechanism. For example, in establishing the BA mechanism, a length of the BA bitmap is 128 bytes, and the first bitmap information indicates that a required feedback length is 32 bytes. The receiver may feed back a receiving status of the A-MSDU with a length of 32 bytes that have been transmitted, without having to wait for a completion of transmission of 128 bytes before performing the BA feedback. In this way, the bytes indicated by the initial bitmap information are transmitted multiple times, and the BA feedback is performed for each transmitted data frame to shorten an overall transmission time.

[0087] Referring to FIG. 6, an embodiment of the present disclosure further provides a communication method. Optionally, the method may be performed by an electronic device. The electronic device is a receiver, such as an AP or an STA. The method may include steps as follows.

[0088] In step 601, an add BA Request frame sent by the transmitter is received in the first communication link, and the add BA Request frame includes initial bitmap information and an initial BA starting sequence number; in which the initial bitmap information indicates a size of a BA buffer.

[0089] In step 602, a first data frame is received in a first communication link with the transmitter.

[0090] In step 603, a first block acknowledgement request (BA Request) frame corresponding to the first data frame is received in a second communication link with the transmitter; in which the first communication link and the second communication link belong to a simultaneous transmit and receive (STR) link pair.

[0091] In step 604, a BA frame is sent in the second communication link.

[0092] The receiver feeds back the BA frame in the second communication link, and indicates to the transmitter via the BA frame contents of the first data frame successfully received by the receiver. If all contents of the first data frame are not completely received, the transmitter will resend the BA Request frame of the data frame that was not successfully received, i.e., a second BA Request frame, and the receiver will receive the BA Request frame again, as shown in step 605. It is understandable that if all contents of the first data frame are successfully received, there is no need to resend it.

[0093] In step 605, a second BA Request frame is received in the second communication link.

[0094] In an optional embodiment, the second BA Request frame includes a second BA starting sequence number; and/or second bitmap information.

[0095] The second BA starting sequence number is a sum of the initial BA starting sequence number and the initial bitmap information, that is, a value of a starting sequence number in the second BA Request frame is a sum of the BA bitmap length in the BA Request frame sent for a first time and a value of the starting sequence number in the BA Request frame sent for the first time.

[0096] The number of bytes indicated by the second bitmap information is less than that indicated by the initial bitmap information. For example, in establishing the BA

mechanism, the length of the BA bitmap is 128 bytes, and the second bitmap information indicates that a required feedback length may be 32 bytes or 64 bytes; on the other hand, if the first bitmap information indicates that the required feedback length is 32 bytes, then the starting sequence number in the second BA Request frame is 33th, i.e. $(32+1)^{th}$. In this way, the bytes indicated by the initial bitmap information are transmitted multiple times, and the BA feedback is performed for each transmitted data frame to shorten an overall transmission time.

[0097] In embodiments of the present disclosure, the receiver receives the first data frame in the first communication link with the transmitter; receives the first block acknowledgement request (BA Request) frame corresponding to the first data frame in the second communication link with the transmitter; and performs a BA feedback while transmitting the first data frame, thereby shortening the transmission time, meeting a transmission requirement of a low-latency service, improving a spectrum utilization, and adapting to a high-density communication environment.

[0098] Referring to FIG. 7, based on a same principle as the method provided in embodiments of the present disclosure, an embodiment of the present disclosure further provides an electronic device, which is a transmitter, and includes:

[0099] a data frame sending module 701 configured to send a first data frame in a first communication link with a receiver; and

[0100] a request frame sending module 702 configured to send a first block acknowledgement request (BA Request) frame corresponding to the first data frame in a second communication link with the receiver.

[0101] The first communication link and the second communication link belong to a simultaneous transmit and receive (STR) link pair.

[0102] Optionally, in an embodiment of the present disclosure, the electronic device includes:

[0103] a request sending module configured to send an add BA Request frame to the receiver in the first communication link.

[0104] The add BA Request frame includes initial bitmap information and an initial BA starting sequence number; in which the initial bitmap information indicates a size of a BA buffer.

[0105] Optionally, in an embodiment of the present disclosure, the electronic device includes:

[0106] a response receiving module configured to receive an add BA response frame sent by the receiver.

[0107] Optionally, in an embodiment of the present disclosure, the first BA Request frame includes:

[0108] a first BA starting sequence number; and/or

[0109] first bitmap information.

[0110] The first BA starting sequence number is the same as the initial BA starting sequence number. The number of bytes indicated by the first bitmap information is less than that indicated by the initial bitmap information.

[0111] Optionally, in an embodiment of the present disclosure, the electronic device includes:

[0112] a first BA receiving module configured to receive a BA frame in the second communication link; and

[0113] a first BA sending module configured to, in response to the BA frame, send a second BA Request frame in the second communication link.

[0114] Optionally, in an embodiment of the present disclosure,

[0115] the second BA Request frame includes:

[0116] a second BA starting sequence number; and/or

[0117] second bitmap information.

[0118] The second BA starting sequence number is a sum of the initial BA starting sequence number and the initial bitmap information. The number of bytes indicated by the second bitmap information is less than that indicated by the initial bitmap information.

[0119] In embodiments of the present disclosure, the data frame sending module 701 first sends the first data frame in the first communication link with the receiver, and then the request frame sending module 702 sends the first BA Request frame corresponding to the first data frame in the second communication link with the receiver. A BA feedback is performed while transmitting the first data frame, thereby shortening the transmission time, meeting a transmission requirement of a low-latency service, improving a spectrum utilization, and adapting to a high-density communication environment.

[0120] An embodiment of the present disclosure further provides a communication apparatus, which is applied to a transmitter, and includes:

[0121] a first sending module configured to send a first data frame in a first communication link with a receiver; and

[0122] a second sending module configured to send a first block acknowledgement request (BA Request) frame corresponding to the first data frame in a second communication link with the receiver; in which the first communication link and the second communication link belong to a simultaneous transmit and receive (STR) link pair.

[0123] The apparatus further includes other modules of the electronic device in the aforementioned embodiments, which will not be described in detail here.

[0124] Referring to FIG. 8, based on a same principle as the method provided in embodiments of the present disclosure, an embodiment of the present disclosure further provides an electronic device, which is a receiver, such as an AP or an STA, and includes:

[0125] a data frame receiving module 801 configured to receive a first data frame in a first communication link with a transmitter; and

[0126] a request frame receiving module 802 configured to receive a first block acknowledgement request (BA Request) frame corresponding to the first data frame in a second communication link with the transmitter.

[0127] The first communication link and the second communication link belong to a simultaneous transmit and receive (STR) link pair.

[0128] Optionally, in an embodiment of the present disclosure, the electronic device includes:

[0129] a request receiving module configured to receive an add BA Request frame sent by the transmitter in the first communication link.

[0130] The add BA Request frame includes initial bitmap information and an initial BA starting sequence number; in which the initial bitmap information indicates a size of a BA buffer.

[0131] Optionally, in an embodiment of the present disclosure, the electronic device includes:

[0132] a response sending module configured to send an add BA response frame to the receiver.

[0133] Optionally, in an embodiment of the present disclosure, the first BA Request frame includes:

[0134] a first BA starting sequence number; and/or

[0135] first bitmap information.

[0136] The first BA starting sequence number is the same as the initial BA starting sequence number. The number of bytes indicated by the first bitmap information is less than that indicated by the initial bitmap information.

[0137] Optionally, in an embodiment of the present disclosure, the electronic device includes:

[0138] a second BA receiving module configured to send a BA frame in the second communication link; and

[0139] a second BA sending module configured to receive a second BA Request frame in the second communication link.

[0140] Optionally, in an embodiment of the present disclosure, the second BA Request frame includes:

[0141] a second BA starting sequence number; and/or

[0142] second bitmap information.

[0143] The second BA starting sequence number is a sum of the initial BA starting sequence number and the initial bitmap information. The number of bytes indicated by the second bitmap information is less than that indicated by the initial bitmap information.

[0144] In embodiments of the present disclosure, the data frame receiving module 801 receives the first data frame in the first communication link with the transmitter; the request frame receiving module 802 receives the first block acknowledgement request (BA Request) frame corresponding to the first data frame in the second communication link with the transmitter; and a BA feedback is performed while transmitting the first data frame, thereby shortening the transmission time, meeting a transmission requirement of a low-latency service, improving a spectrum utilization, and adapting to a high-density communication environment.

[0145] An embodiment of the present disclosure further provides a communication apparatus, which is applied to a receiver, and includes:

[0146] a first receiving module configured to receive a first data frame in a first communication link with a transmitter; and

[0147] a second receiving module configured to receive a first block acknowledgement request (BA Request) frame corresponding to the first data frame in a second communication link with the transmitter; in which the first communication link and the second communication link belong to a simultaneous transmit and receive (STR) link pair.

[0148] The apparatus further includes other modules of the electronic device in the aforementioned embodiments, which will not be described in detail here.

[0149] In an optional embodiment, an embodiment of the present disclosure further provides an electronic device, as shown in FIG. 9. The electronic device 9000 shown in FIG. 9 may be a server, including: a processor 9001 and a memory 9003. The processor 9001 and the memory 9003 are connected, for example, via a bus 9002. Optionally, the electronic device 9000 may further include a transceiver 9004. It should be noted that in actual applications, the number of the transceivers 9004 is not limited to one, and the

structure of the electronic device **9000** does not constitute a limitation on the embodiments of the disclosure.

[0150] The processor **9001** may be a central processing unit (CPU), a general-purpose processor, a digital signal processor (DSP), an application specific integrated circuit (ASIC), a field programmable gate array (FPGA), or other programmable logic devices, transistor logic devices, hardware components, or any combination thereof. The processor **9001** may implement or execute various exemplary logical blocks, modules, and circuits described in conjunction with the contents of the present disclosure. The processor **9001** may also be a combination that implements computing functions, such as a combination of one or more microprocessors, a combination of a DSP and a microprocessor, etc.

[0151] The bus **9002** may include a path to transmit information between the above components. The bus **9002** may be a peripheral component interconnect (PCI) bus, an extended industry standard architecture (EISA) bus, or the like. The bus **9002** can be divided into an address bus, a data bus, a control bus, etc. For ease of representation, FIG. 9 illustrates only one thick line, but this does not mean that there is only one bus or one type of bus.

[0152] The memory **9003** may be a read only memory (ROM) or other types of static storage devices that may store static information and instructions, or a random access memory (RAM) or other types of dynamic storage devices that may store information and instructions; and the memory **9003** may also be an electrically erasable programmable read only memory (EEPROM), a compact disc read only memory (CD-ROM), or other optical disk storage media or optical disc storage media (including a compressed optical disk, a laser disk, an optical disk, a digital versatile disk, a Blu-ray disk, etc.), a magnetic disk storage medium or other magnetic storage devices, or any other medium that may be used to carry or store the desired program codes in the form of instructions or data structures and may be accessed by a computer, but is not limited to this.

[0153] The memory **9003** is configured to store application program codes for executing the solution of the present disclosure, and the execution is controlled by the processor **9001**. The processor **9001** is used to execute the application program codes stored in the memory **9003** to implement the contents shown in the aforementioned method embodiments.

[0154] The electronic device includes, but is not limited to: a mobile terminal such as a mobile phone, a laptop, a digital broadcast receiver, a personal digital assistant (PDA), a tablet computer (PAD), a portable multimedia player (PMP), a vehicle-mounted terminal (such as a vehicle-mounted navigation terminal), etc.; and a fixed terminal such as a digital television (TV), a desktop computer, etc. The electronic device shown in FIG. 9 is merely an example and should not limit the functions and use scope of the embodiments of the disclosure in any way.

[0155] The server provided in the present disclosure may be an independent physical server, a server cluster or a distributed system composed of a plurality of physical servers, or a cloud server that provides basic cloud computing services such as cloud services, cloud databases, cloud computing, cloud functions, cloud storage, network services, cloud communications, middleware services, domain name services, security services, content distribution network (CDN), as well as big data and artificial intelligence

platforms. The terminal may be a smart phone, a tablet computer, a laptop computer, a desktop computer, a smart speaker, a smart watch, etc., but is not limited thereto. The terminal and the server may be connected directly or indirectly via wired or wireless communication, which is not limited in the present disclosure.

[0156] An embodiment of the present disclosure provides a computer-readable storage medium, on which a computer program is stored. When the computer program runs on a computer, the computer may execute the corresponding contents in the aforementioned method embodiments.

[0157] It should be understood that, although the steps in the flowchart of the accompanying drawings are displayed sequentially as indicated by the arrows, these steps are not necessarily executed sequentially in an order indicated by the arrows. Unless otherwise specified herein, there is no strict order limitation for the execution of these steps, and these steps may be executed in other orders. Moreover, at least some of the steps in the flowchart of the accompanying drawings may include a plurality of sub-steps or a plurality of stages. These sub-steps or stages are not necessarily executed at the same time, but may be executed at different times. The order of execution of these steps is not necessarily sequential, but these steps may be executed in turn or alternately with other steps or at least some of sub-steps or stages of other steps.

[0158] It should be noted that the above-mentioned computer-readable medium in the present disclosure may be a computer-readable signal medium, or a computer-readable storage medium, or any combination of the above two media. The computer-readable storage medium may be, for example, but is not limited to, an electronic, magnetic, optical, electromagnetic, infrared, or semiconducting system, apparatus, or device, or any combination of the foregoing. The more specific examples of the computer-readable storage medium may include, but are not limited to, an electrical connection having one or more wires, a portable computer disk, a hard disk, a random access memory (RAM), a read-only memory (ROM), an erasable programmable read-only memory (EPROM or flash memory), an optical fiber, a portable compact disc read-only memory (CD-ROM), an optical storage device, a magnetic storage device, or any suitable combination of the foregoing. In the present disclosure, the computer-readable storage medium may be any tangible medium that can contain or store a program for use by or in connection with an instruction execution system, apparatus, or device. In the present disclosure, the computer-readable signal medium may include a data signal propagated in a baseband or as a part of a carrier wave, in which computer-readable program codes are carried. Such a propagated data signal may take a variety of forms, including but not limited to electromagnetic signals, optical signals, or any suitable combination of the foregoing. The computer-readable signal medium may also be any computer-readable medium other than computer-readable storage media, which may send, propagate or transport a program for use by or in conjunction with an instruction execution system, apparatus or device. The program code embodied on the computer-readable medium may be transmitted using any appropriate medium, including but not limited to: a wire, an optical cable, a radio frequency (RF), or the like, or any suitable combination of the foregoing.

[0159] The above-mentioned computer-readable medium may be included in the above-mentioned electronic device, or may exist independently without being incorporated into the electronic device.

[0160] The above-mentioned computer-readable medium carries one or more programs. When the one or more programs are executed by the electronic device, the electronic device is configured to execute the method shown in the above embodiments.

[0161] According to one aspect of the present disclosure, a computer program product or a computer program is provided, including computer instructions stored in a computer-readable storage medium. A processor of a computer device reads the computer instructions from the computer-readable storage medium and executes the computer instructions, so that the computer device executes the method in the above various optional implementations.

[0162] The computer program codes for performing operations in the present disclosure may be written in one or more programming languages or their combination, including object-oriented programming languages such as Java, Smalltalk, C++, or conventional procedural programming languages such as "C" or similar programming languages. The program codes may be executed entirely on a user computer, partly on the user computer, as a stand-alone software package, partly on the user computer and partly on a remote computer, or entirely on the remote computer or a server. In case of involving a remote computer, the remote computer may be connected to the user computer through any type of network, including a local area network (LAN) or a wide area network (WAN), or may be connected to an external computer (e.g., through the Internet using an Internet service provider).

[0163] The flowcharts and block diagrams in the accompanying drawings illustrate possibly implemented architectures, functions, and operations of systems, methods, and computer program products according to various embodiments of the present disclosure. In this regard, each block in the flowchart or block diagram may represent a module, a program segment, or a portion of code, which contains one or more executable instructions for implementing the specified logical functions. It should also be noted that, in some alternative implementations, the functions noted in the block may occur out of the order noted in the drawings. For example, two blocks shown in succession may actually be executed substantially in parallel, or they may sometimes be executed in the reverse order, depending on the functionality involved. It should also be noted that each block in the block diagram and/or flowchart, and a combination of blocks in the block diagram and/or flowchart, may be implemented by a dedicated hardware-based system that performs the specified functions or operations, or may be implemented by a combination of dedicated hardware and computer instructions.

[0164] The modules described and involved in the embodiments of the present disclosure may be implemented in software or hardware. The name of a module does not, in some cases, constitute a limitation on the module itself. For example, module A can also be described as "module A for performing operation B".

[0165] The above description is only a preferred embodiment of the present disclosure and an illustration of the technical principles used. Those skilled in the art should understand that the scope of disclosure involved in the

present disclosure is not limited to the technical solutions formed by a specific combination of the above-mentioned technical features, but should also cover other technical solutions formed by any combination of the above-mentioned technical features or their equivalent features without departing from the above-mentioned concepts of the present disclosure. For example, the above-mentioned features are replaced with the technical features with similar functions disclosed in the present disclosure (but not limited to this) to form a technical solution.

1. A communication method, performed by a transmitter, comprising:

sending a first data frame in a first communication link with a receiver; and

sending a first block acknowledgement (BA) Request frame corresponding to the first data frame in a second communication link with the receiver; wherein the first communication link and the second communication link belong to a simultaneous transmit and receive (STR) link pair.

2. The communication method according to claim 1, before sending the first data frame in the first communication link with the receiver, the method comprising:

sending an add BA Request frame to the receiver in the first communication link, wherein the add BA Request frame comprises initial bitmap information and an initial BA starting sequence number; wherein the initial bitmap information indicates a size of a BA buffer.

3. The communication method according to claim 2, after sending the add BA Request frame to the receiver in the first communication link, the method comprising:

receiving an add BA response frame sent by the receiver.

4. The communication method according to claim 2, wherein the first BA Request frame comprises at least one of:

a first BA starting sequence number; or

first bitmap information,

wherein the first BA starting sequence number is the same as the initial BA starting sequence number, and the number of bytes indicated by the first bitmap information is less smaller than that indicated by the initial bitmap information.

5. The communication method according to claim 2, after sending the first BA Request frame corresponding to the first data frame in the second communication link with the receiver, the method comprising:

receiving a BA frame in the second communication link; and

in response to the BA frame, sending a second BA Request frame in the second communication link.

6. The communication method according to claim 5, wherein the second BA Request frame comprises at least one of:

a second BA starting sequence number; or

second bitmap information,

wherein the second BA starting sequence number is a sum of the initial BA starting sequence number and the initial bitmap information, and the number of bytes indicated by the second bitmap information is less than that indicated by the initial bitmap information.

7. A communication method, performed by a receiver, comprising:

receiving a first data frame in a first communication link with a transmitter; and

receiving a first block acknowledgement request (BA) Request frame corresponding to the first data frame in a second communication link with the transmitter;
 wherein the first communication link and the second communication link belong to a simultaneous transmit and receive (STR) link pair.

8. The communication method according to claim **7**, before receiving the first data frame in the first communication link with the transmitter, the method comprising:

receiving an add BA Request frame sent by the transmitter in the first communication link, wherein the add BA Request frame comprises initial bitmap information and an initial BA starting sequence number; wherein the initial bitmap information indicates a size of a BA buffer.

9. The communication method according to claim **8**, after receiving the add BA Request frame sent by the transmitter in the first communication link, the method comprising:

sending an add BA response frame to the receiver.

10. The communication method according to claim **8**, wherein the first BA Request frame comprises at least one of:

a first BA starting sequence number; or
 first bitmap information,

wherein the first BA starting sequence number is the same as the initial BA starting sequence number, and the number of bytes indicated by the first bitmap information is smaller than that indicated by the initial bitmap information.

11. The communication method according to claim **8**, after receiving the first BA Request frame corresponding to the first data frame in the second communication link with the transmitter, the method comprising:

sending a BA frame in the second communication link;
 and
 receiving a second BA Request frame in the second communication link.

12. The communication method according to claim **11**, wherein the second BA Request frame comprises at least one of:

a second BA starting sequence number; or
 second bitmap information,
 wherein the second BA starting sequence number is a sum of the initial BA starting sequence number and the initial bitmap information, and the number of bytes indicated by the second bitmap information is less than that indicated by the initial bitmap information.

13.-16. (canceled)

17. An electronic device, comprising
 a memory,
 a processor, and
 a computer program stored in the memory and executable on the processor,
 wherein the processor is configured to:
 send a first data frame in a first communication link with a receiver; and
 send a first block acknowledgement request (BA Request) frame corresponding to the first data frame in a second communication link with the receiver; wherein the first communication link and the second communication link belong to a simultaneous transmit and receive (STR) link pair.

18. A non-transitory computer-readable storage medium, having stored therein a computer program that, when executed by a processor, causes the method according to claim **1** to be implemented.

19. The electronic device according to claim **17**, wherein the processor is configured to:

send an add BA Request frame to the receiver in the first communication link, wherein the add BA Request frame comprises initial bitmap information and an initial BA starting sequence number; wherein the initial bitmap information indicates a size of a BA buffer.

20. The electronic device according to claim **19**, wherein the processor is configured to:

receive an add BA response frame sent by the receiver.

21. The electronic device according to claim **19**, wherein the processor is configured to:

receive a BA frame in the second communication link;
 and
 in response to the BA frame, send a second BA Request frame in the second communication link.

22. An electronic device, comprising
 a memory,
 a processor, and
 a computer program stored in the memory and executable on the processor,
 wherein the processor is configured to perform the method according to claim **6**.

23. The electronic device according to claim **22**, wherein the processor is configured to:

receive an add BA Request frame sent by the transmitter in the first communication link, wherein the add BA Request frame comprises initial bitmap information and an initial BA starting sequence number; wherein the initial bitmap information indicates a size of a BA buffer.

24. A non-transitory computer-readable storage medium, having stored therein a computer program that, when executed by a processor, causes the method according to claim **6** to be implemented.

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