

US Patent & Trademark Office

Patent Public Search | Text View

United States Design Patent	D1089092
Kind Code	S
Date of Patent	August 19, 2025
Inventor(s)	Zhao; Yucai et al.

Dual input power supply

Inventors: Zhao; Yucai (Shanghai, CN), Fan; Bing (Shanghai, CN), Wei; Tao (Shanghai, CN), Che; Liangsong (Shanghai, CN), Huang; Junlai (Shanghai, CN), Dong; Kai (Shanghai, CN)

Applicant: Delta Electronics (Shanghai) Co., Ltd. (Shanghai, CN)

Assignee: Delta Electronics (Shanghai) Co., Ltd. (Shanghai, CN)

() Term:** 15 Years **Appl. No.:**

D/969256

Filed:

October 22, 2024

Foreign Application Priority Data

CN	202030803435.6	Dec. 25, 2020
CN	202130377403.9	Jun. 18, 2021

Related U.S. Application Data

division parent-doc US 29796551 20210624 PENDING child-doc US 29969256

Publication Classification

LOC Cl.: 1302

U.S. Cl.:

USPC D13/110

Field of Classification Search

CPC: H05K (5/00); H05K (5/02); H05K (5/0004); H05K (5/0247); H05K (7/00); H05K (7/20); H05K (7/20136); H05K (7/20154); H05K (7/209); H05K (7/1417); H05K

USPC: D13/110; D13/107; D13/108; D13/118; D13/123; D13/162; D13/164; D13/168; D13/169; D13/174; D13/177; D13/184; D13/199; D14/257; D14/432; D14/438

Description

(1) The patent or application file contains at least one drawing executed in color. Copies of this patent or patent application publication with color drawing(s) will be provided by the Office upon request and payment of the necessary fee.

(2) FIG. 1 is a front view of a first embodiment of a dual input power supply showing our new design;

(3) FIG. 2 is a back view of a first embodiment thereof;

(4) FIG. 3 is a left view of a first embodiment thereof;

(5) FIG. 4 is a right view of a first embodiment thereof;

(6) FIG. 5 is a top view of a first embodiment thereof;

(7) FIG. 6 is a bottom view of a first embodiment thereof;

(8) FIG. 7 is a front and left side perspective view of a first embodiment thereof;

(9) FIG. 8 is a back and right side perspective view of a first embodiment thereof;

(10) FIG. 9 is a front view of a second embodiment thereof;

(11) FIG. 10 is a back view of a second embodiment thereof;

(12) FIG. 11 is a left view of a second embodiment thereof;

(13) FIG. 12 is a right view of a second embodiment thereof;

(14) FIG. 13 is a top view of a second embodiment thereof;

(15) FIG. 14 is a bottom view of a second embodiment thereof;

(16) FIG. 15 is a front and left side perspective view of a second embodiment thereof;

(17) FIG. 16 is a back and right side perspective of a second embodiment thereof;

(18) FIG. 17 is a front view of a third embodiment thereof;

(19) FIG. 18 is a back view of a third embodiment thereof;

(20) FIG. 19 is a left view of a third embodiment thereof;

(21) FIG. 20 is a right view of a third embodiment thereof;

(22) FIG. 21 is a top view of a third embodiment thereof;

(23) FIG. 22 is a bottom view of a third embodiment thereof;

(24) FIG. 23 is a front and left side perspective view of a third embodiment thereof; and,

(25) FIG. 24 is a back and right side perspective of a third embodiment thereof.

(26) The evenly spaced broken lines shown in the figures contain lightly shaded areas that depict the portions of the dual input power supply that form no part of the claimed design. The design does not claim protection for a specific coloring.

Claims

The ornamental design for a dual input power supply as shown and described.
