



US0D1089101S

(12) **United States Design Patent** (10) **Patent No.:** **US D1,089,101 S**
Ashibu (45) **Date of Patent:** **** Aug. 19, 2025**

(54) **CONNECTOR**

FOREIGN PATENT DOCUMENTS

(71) Applicant: **Japan Aviation Electronics Industry, Limited**, Tokyo (JP)

TW 107663-0001 * 11/2005

(72) Inventor: **Kenta Ashibu**, Tokyo (JP)

(73) Assignee: **Japan Aviation Electronics Industry, Limited**, Tokyo (JP)

OTHER PUBLICATIONS

(**) Term: **15 Years**

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(30) **Foreign Application Priority Data**

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(51) **LOC (15) Cl.** **13-03**

(52) **U.S. Cl.** **D13/133**
USPC **D13/133**

(58) **Field of Classification Search**

USPC D13/110, 123, 133, 146–147, 149, 154,
D13/184, 199; D8/354, 356
CPC H01R 12/00; H01R 12/52; H01R 12/70;
H01R 12/71; H01R 12/73; H01R 12/707;
H01R 12/716; H01R 13/20; H01R 13/24;
H01R 13/42; H01R 13/64; H01R 13/426;
H01R 13/506; H01R 13/518; H01R
13/631; H01R 13/639; H01R 13/641;
H01R 13/648; H01R 13/6271; H01R
13/6272; H01R 13/6315; H01R 13/6471;
H01R 13/6581; H01R 13/6582; H01R
13/6585; H01R 43/16; H01R 43/18;
H01R 31/06; H01R 13/2414; H01R
13/2492; H01R 13/502; H01R 13/405;
H01R 31/065; H01R

(Continued)

(56) **References Cited**

U.S. PATENT DOCUMENTS

D540,258 S * 4/2007 Peng D13/147
D608,292 S * 1/2010 Stutz D13/147

(Continued)

ESD device tray, posted Sep. 18, 2024 [online], [retrieved May 15, 2025]. Retrieved from internet, <https://malaster.com/esd-protection-packaging-solutions/MC-77062TESD-Device-Tray-for-24mm-x-24mm-QFP-p134946457> (Year: 2024).*

Primary Examiner — George D. Kirschbaum

Assistant Examiner — Denis Houyoux

(74) *Attorney, Agent, or Firm* — Kreative IP Management LLC; Fuiyeong Kim

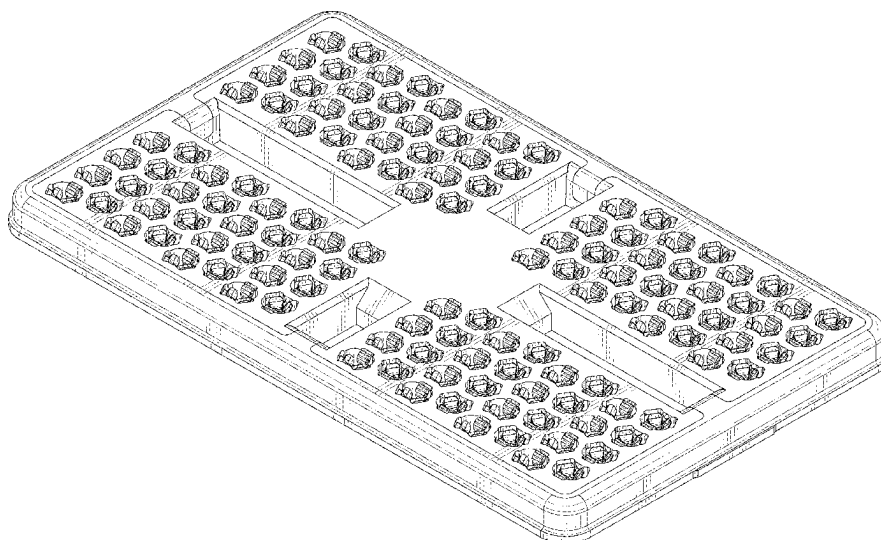
(57) **CLAIM**

The ornamental design for a connector as shown and described.

DESCRIPTION

FIG. 1 is a front elevational view of a connector showing my new design;
FIG. 2 is a rear elevational view thereof;
FIG. 3 is a right side elevational view thereof;
FIG. 4 is a left side elevational view thereof;
FIG. 5 is a top plan view thereof;
FIG. 6 is a bottom plan view thereof;
FIG. 7 is a perspective view showing a front, top and right side thereof;
FIG. 8 is a perspective view showing a rear, bottom and left side thereof;
FIG. 9 is a perspective view showing a front, right and bottom side thereof; and,
FIG. 10 is a perspective view showing a rear, left and top side thereof.
The broken line showing of the connector is for the purpose of illustrating portions of the article and forms no part of the claimed design.

1 Claim, 8 Drawing Sheets



(58) **Field of Classification Search**

CPC 13/512; H01R 13/5202; H01R 13/112;
H01R 13/04; H01R 13/50; H01R 12/714;
H01R 12/57; H01R 12/7011

See application file for complete search history.

(56) **References Cited**

U.S. PATENT DOCUMENTS

D733,662	S	*	7/2015	Harper, Jr.	D13/147
D745,852	S	*	12/2015	Harper, Jr.	D13/154
D855,572	S	*	8/2019	Komoto	D13/147
D967,773	S	*	10/2022	Katou	D13/133
D969,749	S	*	11/2022	Ito	D13/133
D1,053,822	S	*	12/2024	Ashibu	D13/147
2019/0288420	A1	*	9/2019	Hashiguchi	H01R 13/112
2024/0072478	A1	*	2/2024	Ashibu	H01R 12/73

* cited by examiner

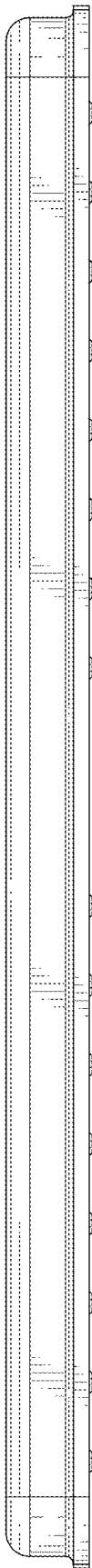


FIG. 1

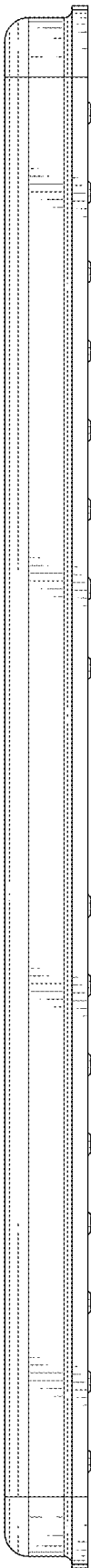


FIG. 2

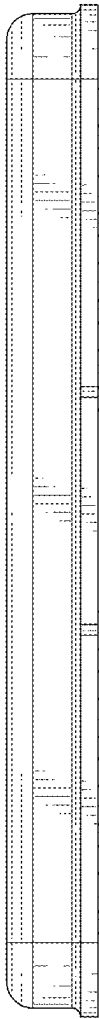


FIG. 3

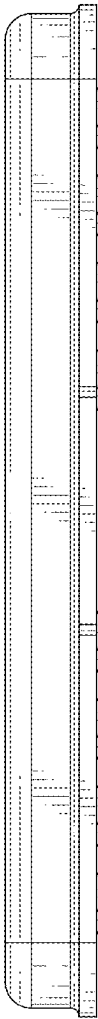


FIG. 4

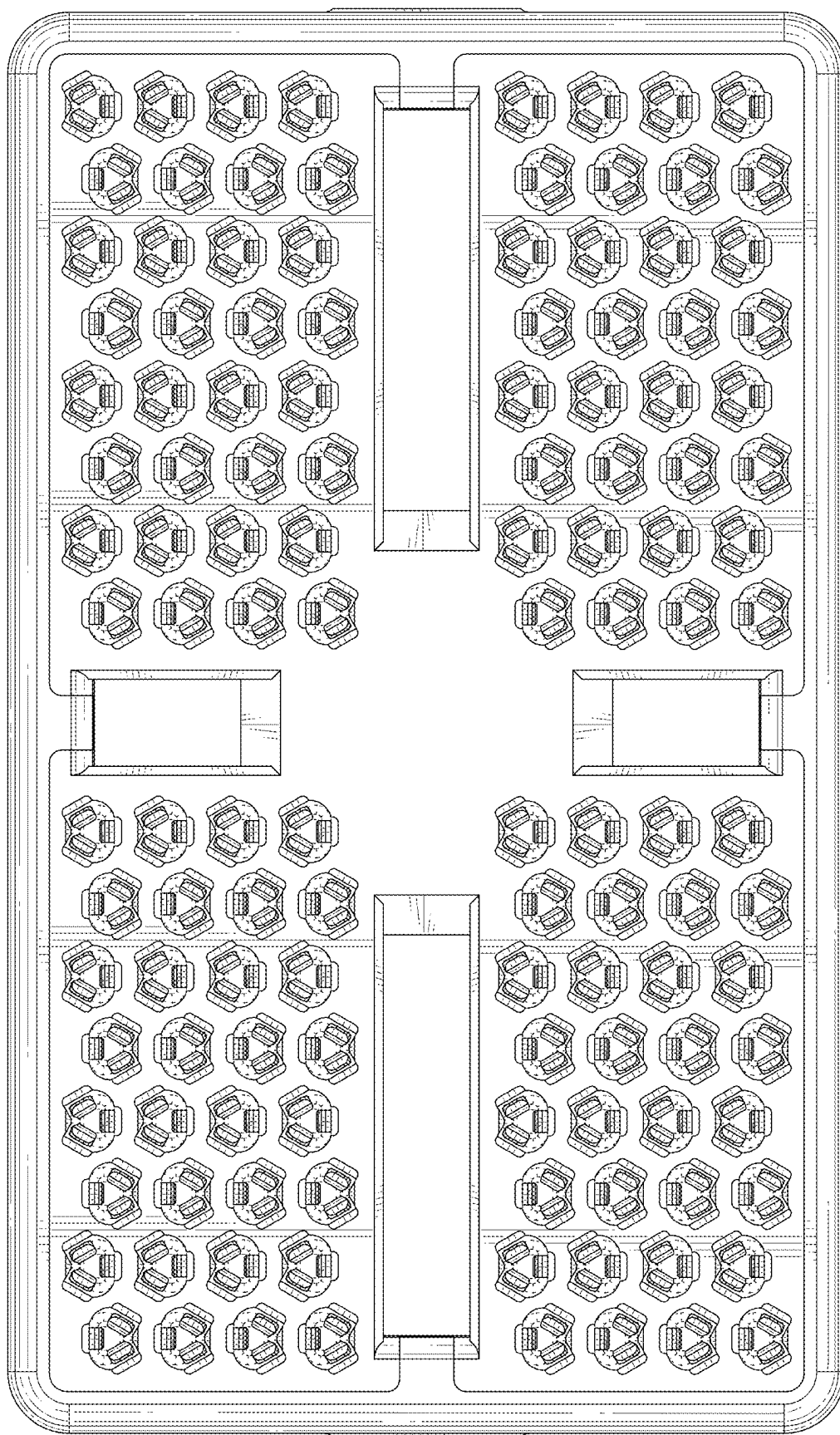


FIG. 5

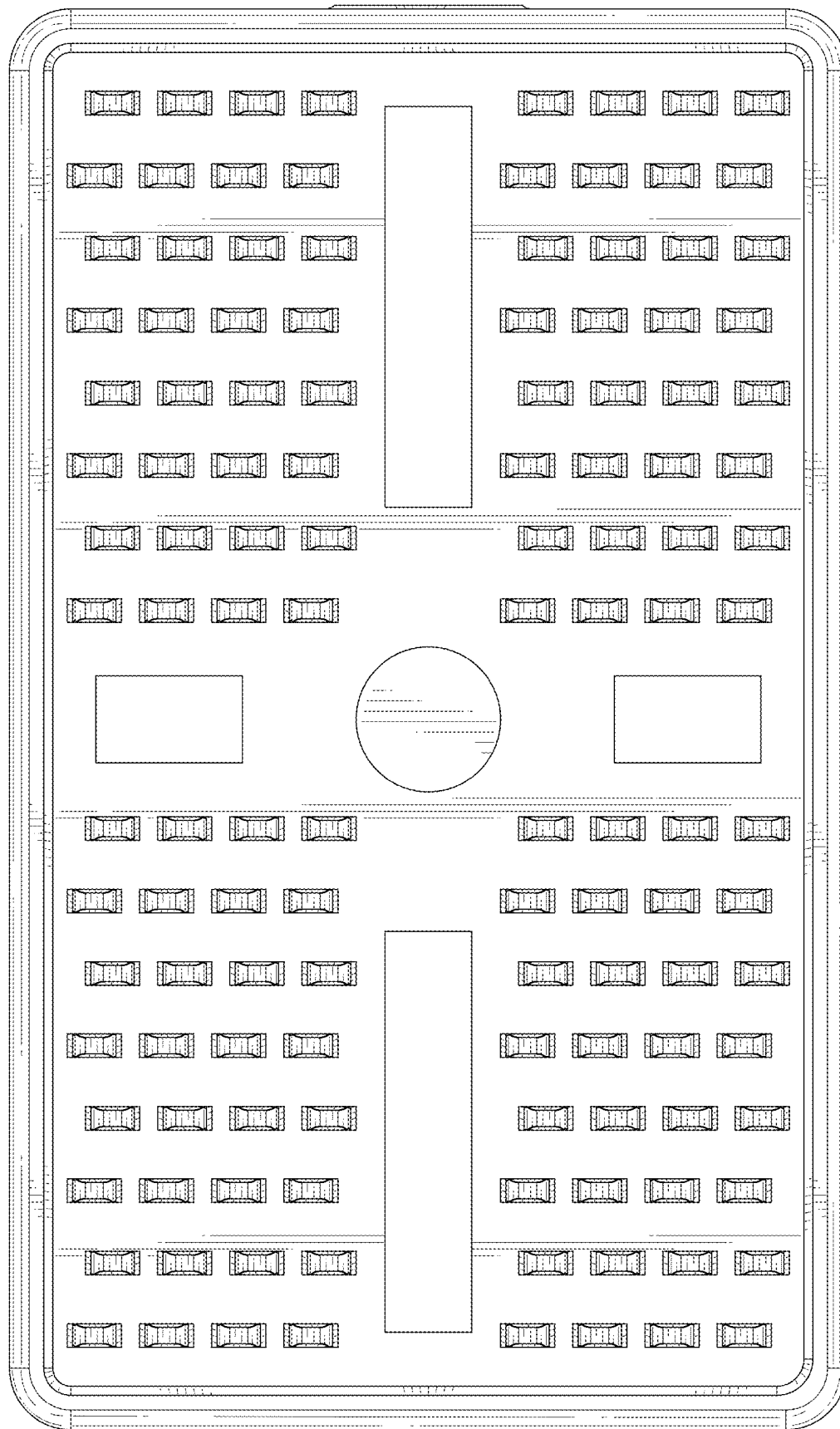


FIG. 6

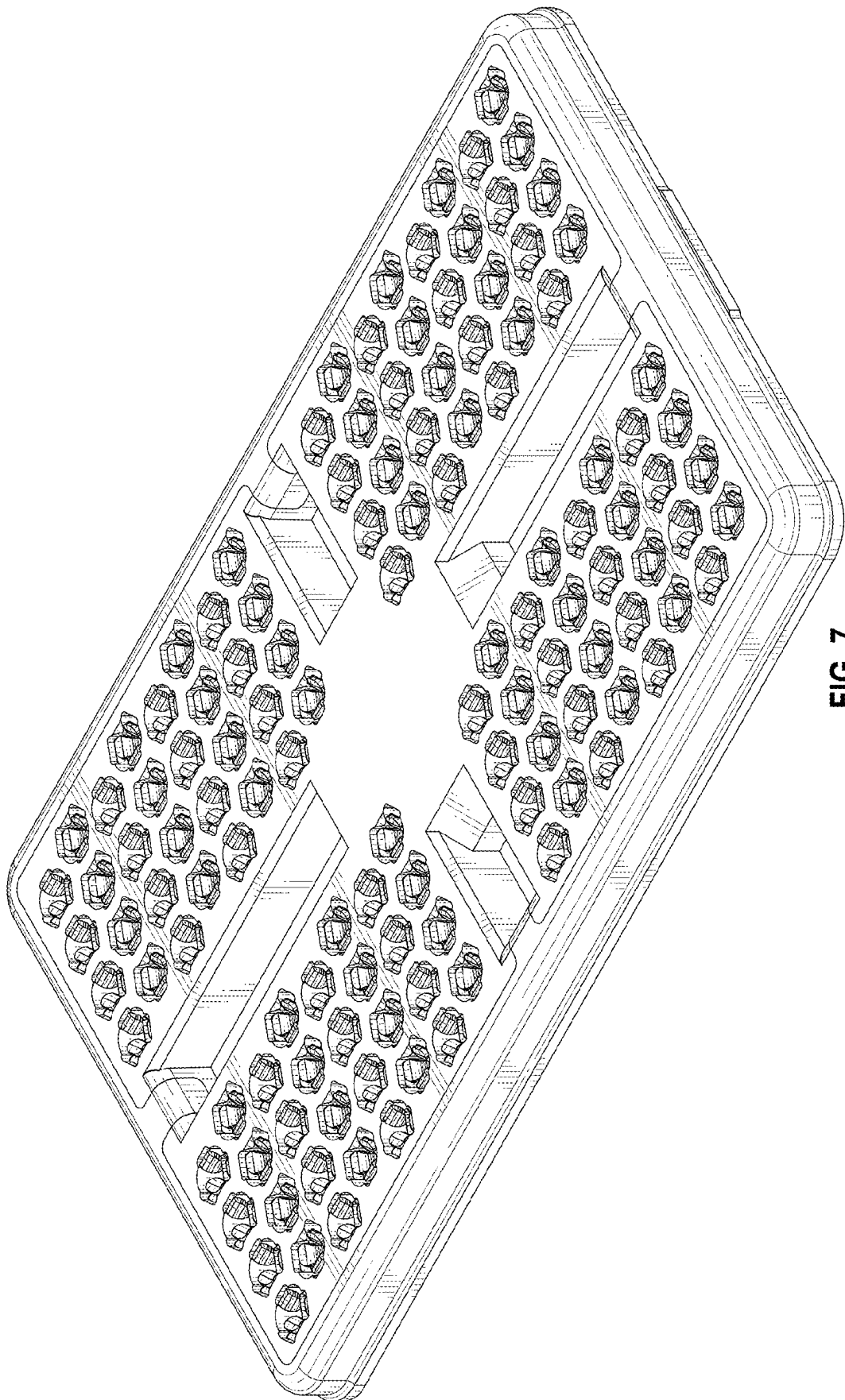


FIG. 7

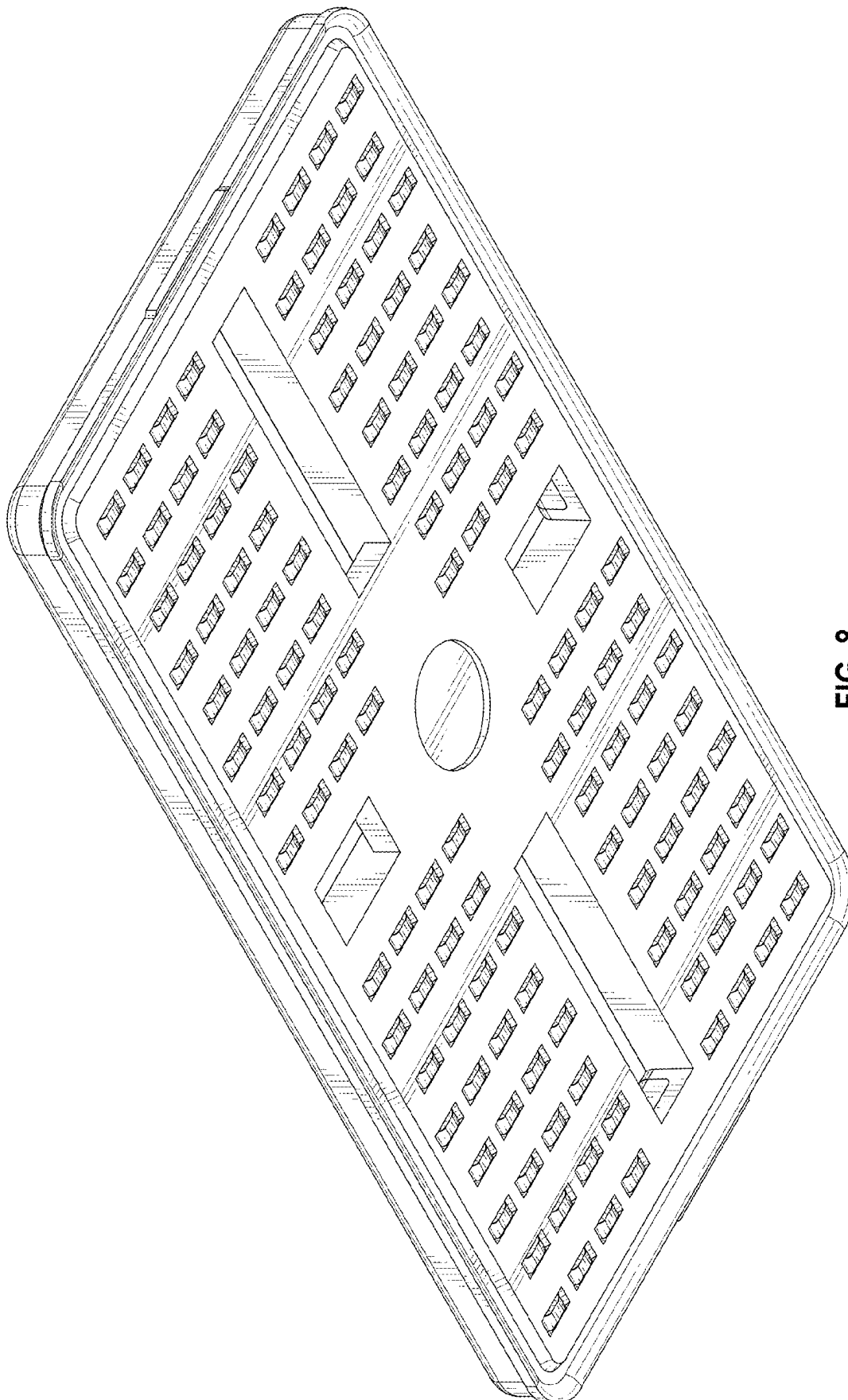


FIG. 8

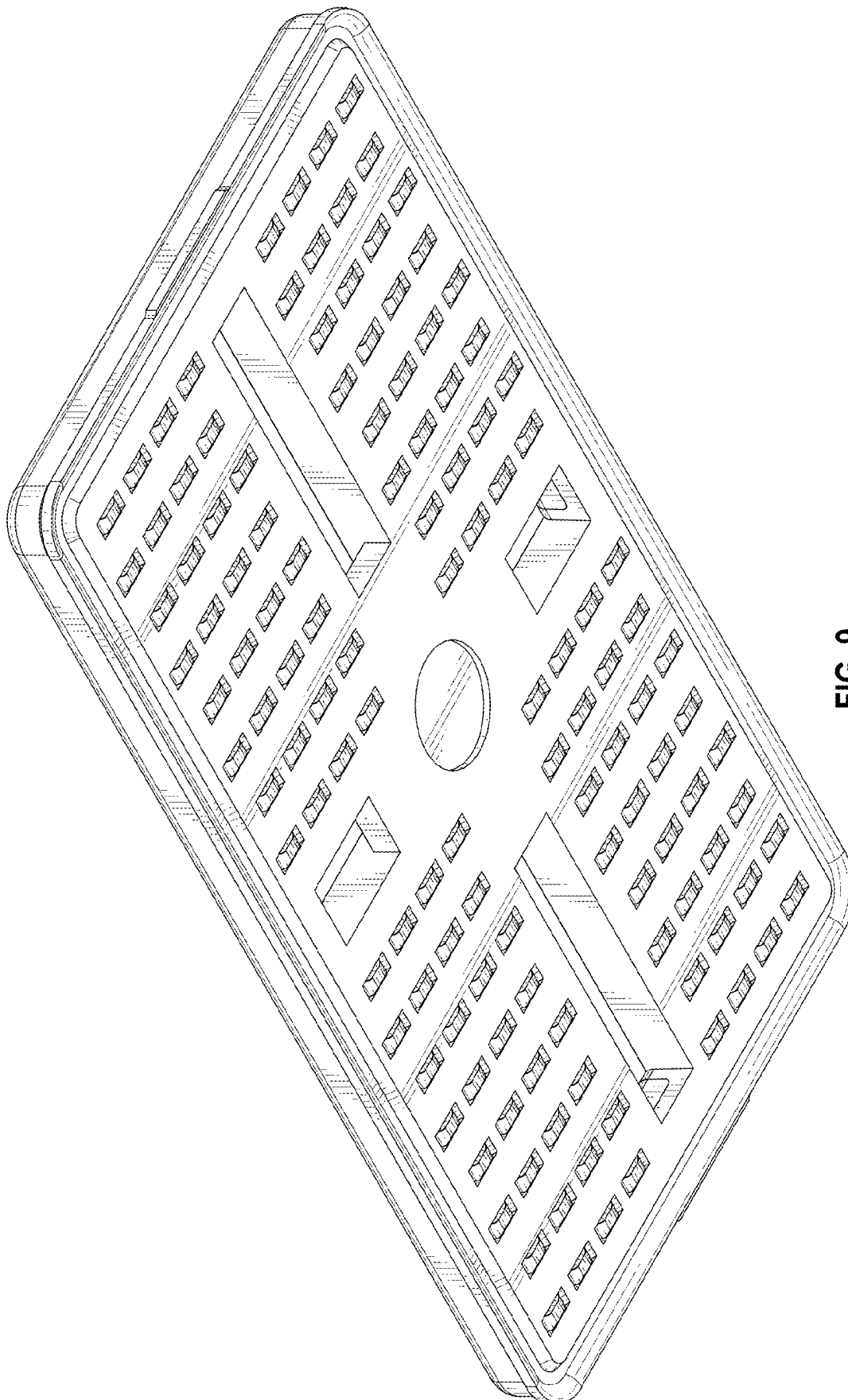


FIG. 9

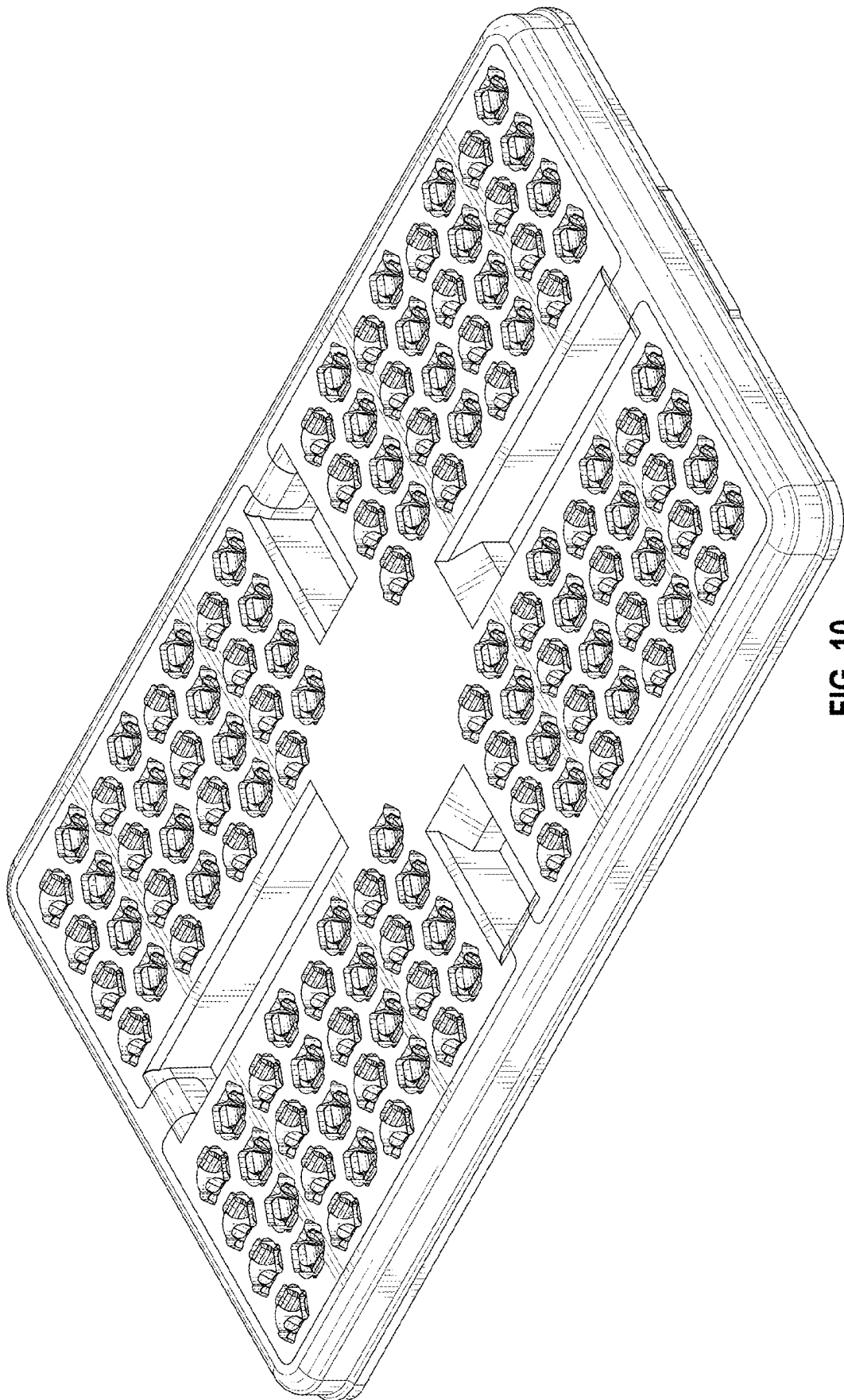


FIG. 10