



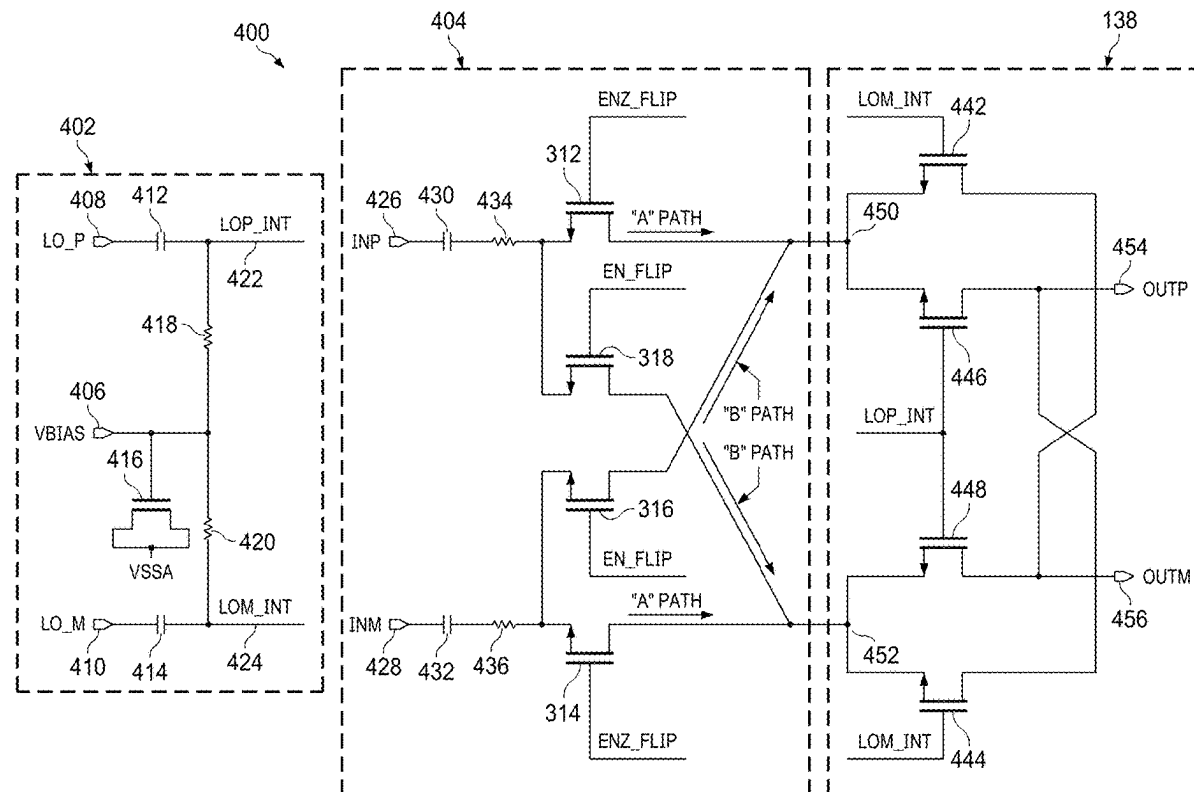
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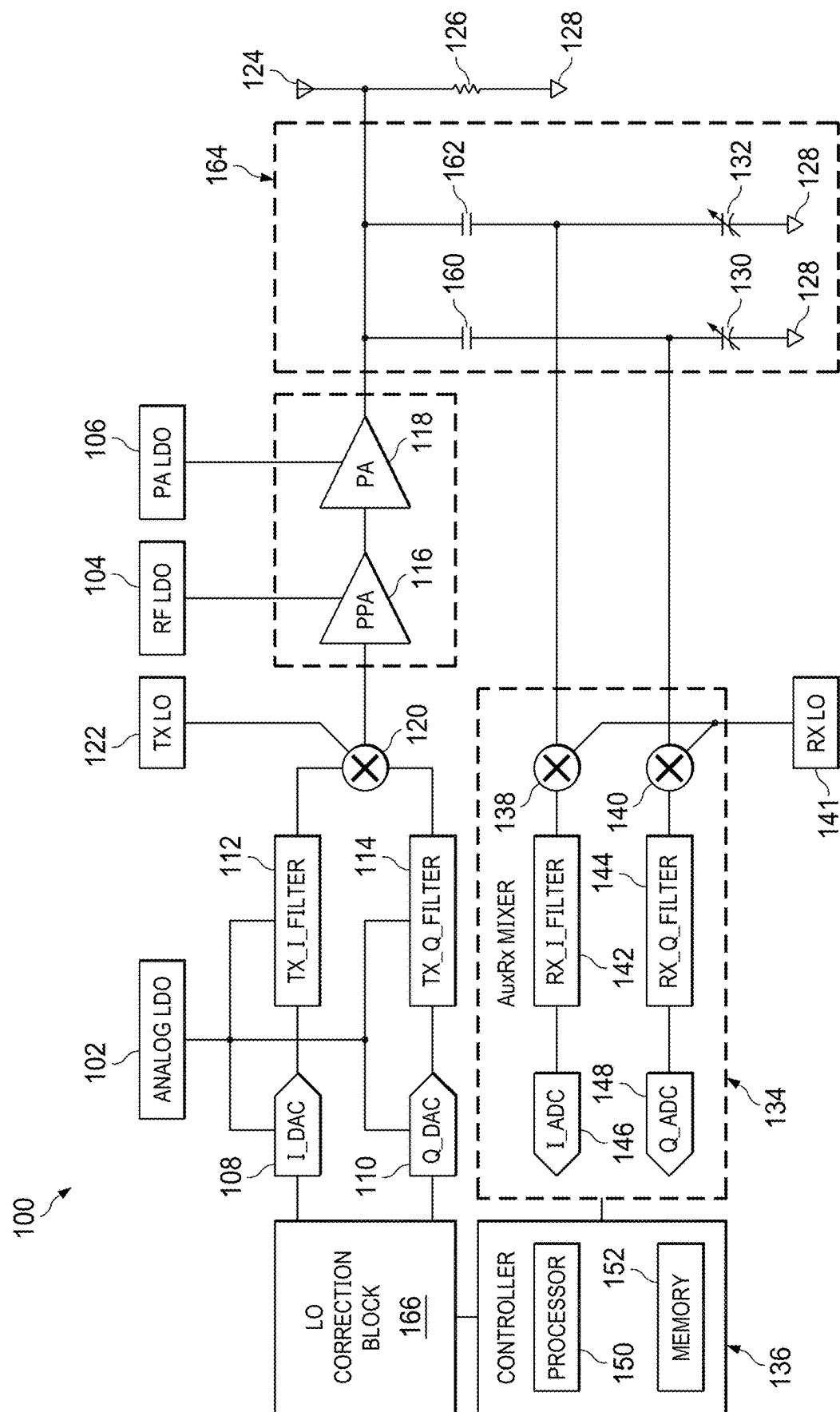
(19) **United States**(12) **Patent Application Publication**
KUNCHAM et al.(10) **Pub. No.: US 2025/0266855 A1**(43) **Pub. Date: Aug. 21, 2025**(54) **ESTIMATING AND CORRECTING
TRANSMITTER LOCAL OSCILLATOR
LEAKAGE IN LOOPBACK PATH**(71) Applicant: **TEXAS INSTRUMENTS
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Bangalore (IN)(21) Appl. No.: **19/198,537**(22) Filed: **May 5, 2025****Related U.S. Application Data**(63) Continuation of application No. 18/308,556, filed on
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(52) **U.S. Cl.**
CPC **H04B 1/109** (2013.01); **H03D 7/12**
(2013.01); **H04L 27/38** (2013.01)(57) **ABSTRACT**

In an example, a system includes a receiver configured to receive a differential input signal from a quadrature amplitude modulation (QAM) transmitter at a differential interface that includes a first input and a second input. The system also includes a first switch coupled to the first input and a second switch coupled to the second input, where the first switch and second switch are configured to couple the first input and the second input to a complex mixer in a non-inverting configuration. The system includes a third switch coupled to the first input and a fourth switch coupled to the second input, where the third switch and the fourth switch are configured to couple the first input and the second input to the complex mixer in an inverting configuration. The complex mixer is configured to produce an output signal based on the non-inverting configuration and the inverting configuration.





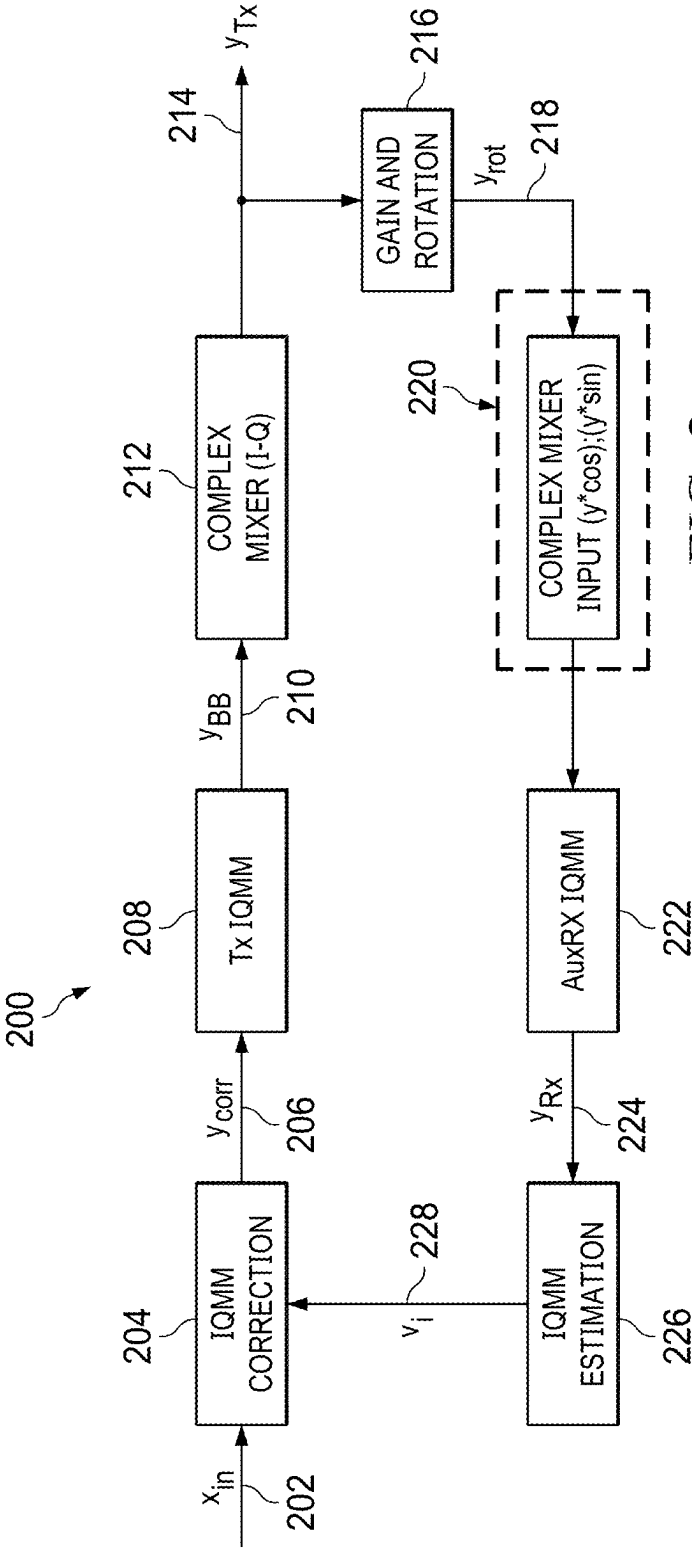


FIG. 2

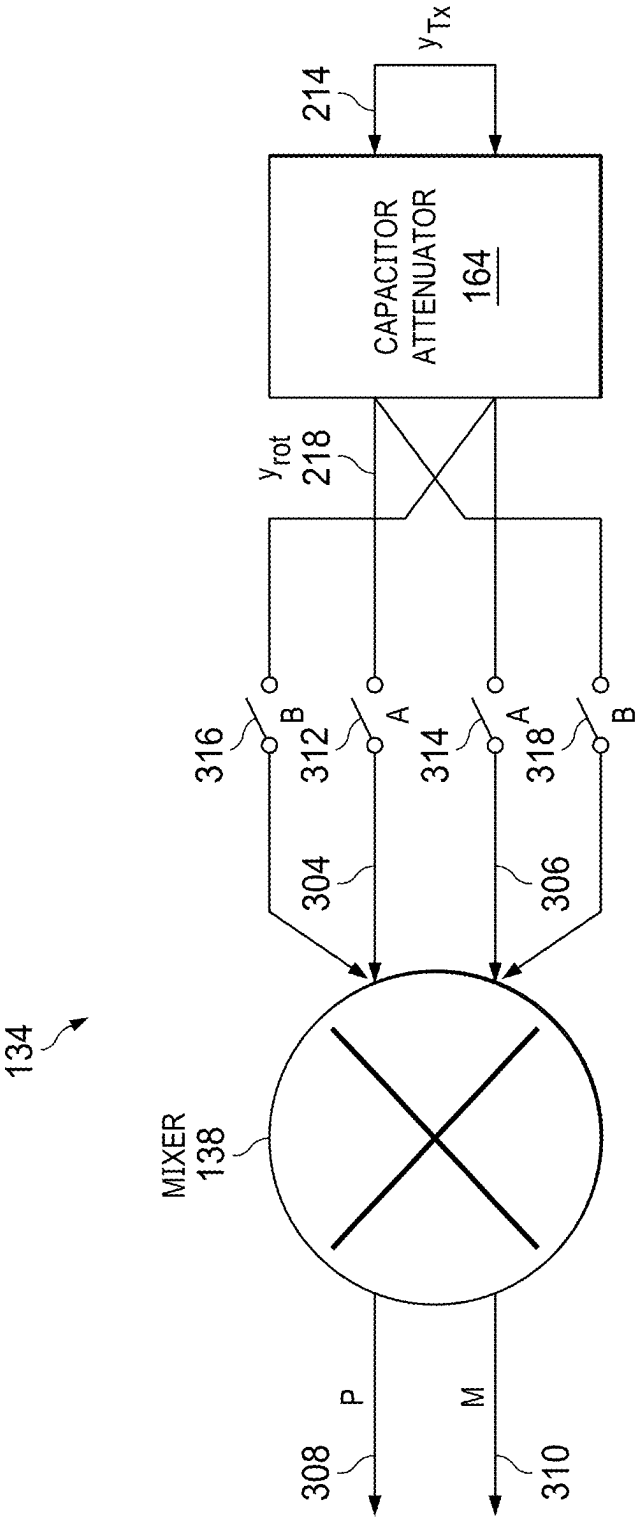


FIG. 3

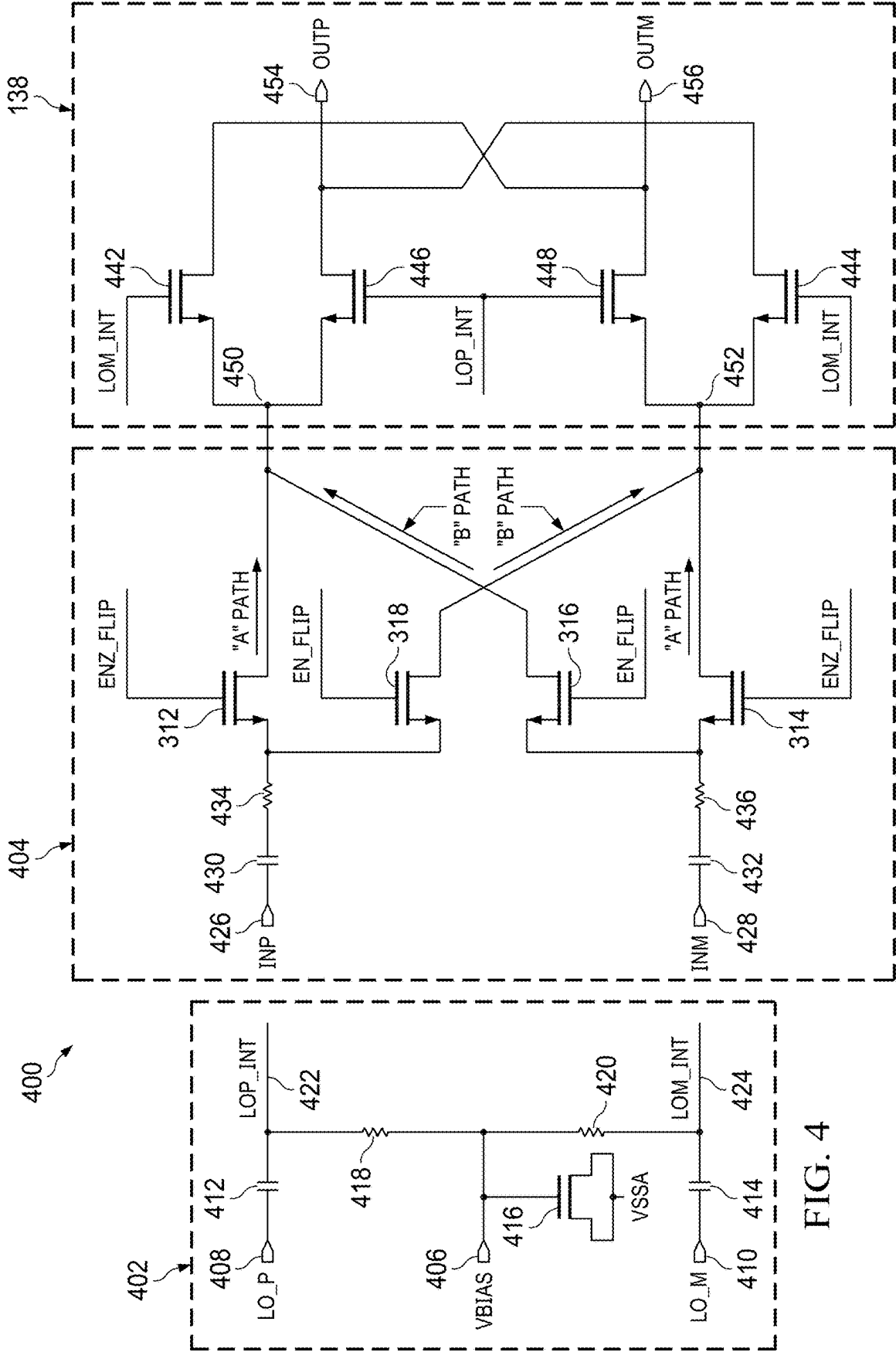


FIG. 4

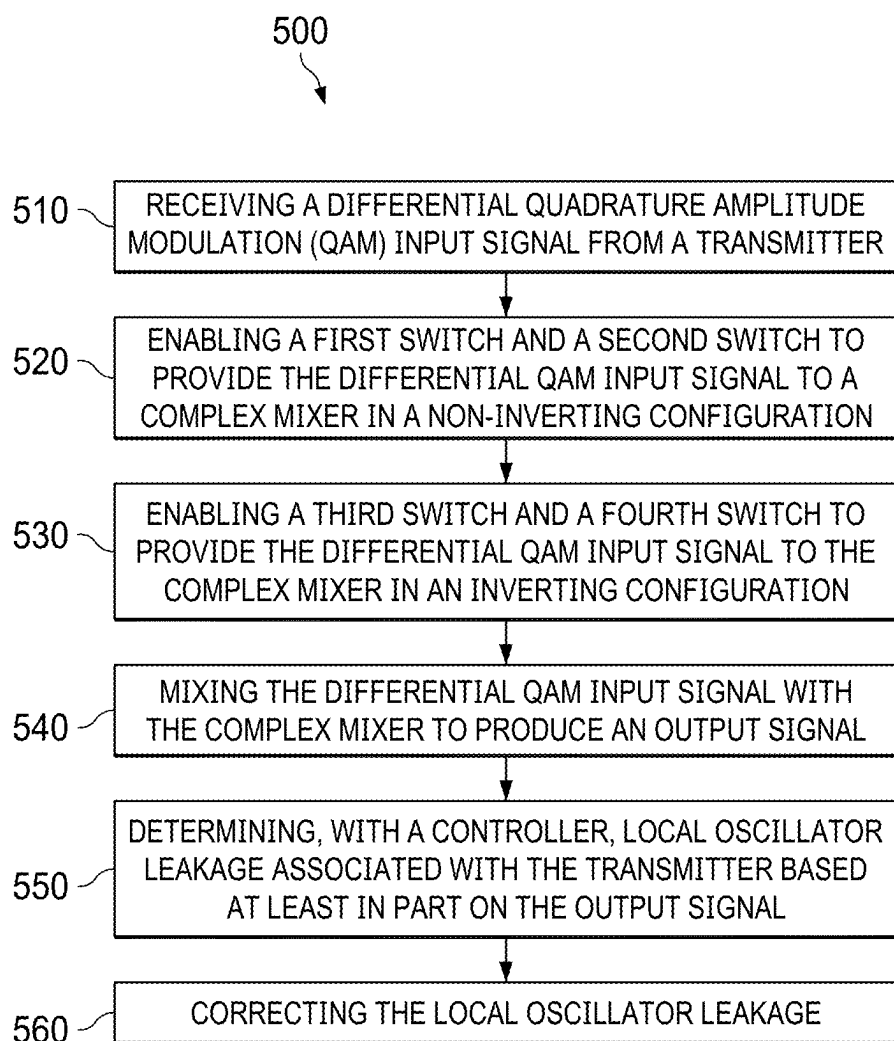


FIG. 5

ESTIMATING AND CORRECTING TRANSMITTER LOCAL OSCILLATOR LEAKAGE IN LOOPBACK PATH

CROSS-REFERENCE TO RELATED APPLICATIONS

[0001] The present application is a continuation of U.S. patent application Ser. No. 18/308,556, filed Apr. 27, 2023, which claims priority to India Provisional Patent Application No. 20/224,1036648, filed Jun. 27, 2022, each of which is hereby incorporated herein by reference in its entirety.

BACKGROUND

[0002] Radio architectures may use a carrier signal produced by a local oscillator (LO) to transmit an information-bearing input signal. The carrier signal may have a much higher frequency than the input signal. The carrier signal may leak into the final transmitted signal, which is referred to as LO leakage. LO leakage may result in efficiency loss. Wireless standards have specifications for the maximum tolerable amount of LO leakage. A correction signal may be added to the transmitted signal to cancel out the LO leakage.

SUMMARY

[0003] In accordance with at least one example of the description, a system includes a receiver configured to receive a differential input signal from a quadrature amplitude modulation (QAM) transmitter at a differential interface that includes a first input and a second input. The system also includes a first switch coupled to the first input and a second switch coupled to the second input, where the first switch and second switch are configured to couple the first input and the second input to a complex mixer in a non-inverting configuration. The system includes a third switch coupled to the first input and a fourth switch coupled to the second input, where the third switch and the fourth switch are configured to couple the first input and the second input to the complex mixer in an inverting configuration. The complex mixer is configured to produce an output signal based on the non-inverting configuration and the inverting configuration.

[0004] In accordance with at least one example of the description, a method includes receiving a differential QAM input signal from a transmitter. The method also includes enabling a first switch and a second switch to provide the differential QAM input signal to a complex mixer in a non-inverting configuration. The method includes enabling a third switch and a fourth switch to provide the differential QAM input signal to the complex mixer in an inverting configuration. The method also includes mixing the differential QAM input signal with the complex mixer to produce an output signal. The method includes determining, with a controller, local oscillator leakage associated with the transmitter based at least in part on the output signal. The method includes correcting the local oscillator leakage.

[0005] In accordance with at least one example of the description, a system includes a transmitter configured to transmit QAM signals. The system also includes a receiver configured to receive a differential QAM input signal from the transmitter at a differential interface that includes a first input and a second input. The system includes a first switch coupled to the first input and a second switch coupled to the second input, where the first switch and second switch are

configured to couple the first input and the second input to a complex mixer in a non-inverting configuration to produce a non-inverting signal provided to the complex mixer. The system also includes a third switch coupled to the first input and a fourth switch coupled to the second input, where the third switch and the fourth switch are configured to couple the first input and the second input to the complex mixer in an inverting configuration to produce an inverting signal input to the complex mixer. The system includes a controller configured to correct a transmitter local oscillator leakage with the inverting signal and the non-inverting signal.

BRIEF DESCRIPTION OF THE DRAWINGS

[0006] FIG. 1 is a block diagram of a transmitter and auxiliary receiver in accordance with various examples.

[0007] FIG. 2 is a block diagram of a transmit/auxiliary receiver (TX/AuxRX) chain in accordance with various examples.

[0008] FIG. 3 is a diagram of an auxiliary receiver in accordance with various examples.

[0009] FIG. 4 is a circuit diagram of a switch network and a complex mixer in accordance with various examples.

[0010] FIG. 5 is a flow diagram of a method for estimating and correcting a transmitter LO leakage in accordance with various examples.

[0011] The same reference numbers or other reference designators are used in the drawings to designate the same or similar (functionally and/or structurally) features.

DETAILED DESCRIPTION

[0012] Quadrature amplitude modulation (QAM) is a modulation method for transmitting information. QAM conveys two message signals by modulating the amplitude of two carrier waves. The two carrier waves have the same frequency but are out of phase with one another by 90°, which is called orthogonality or quadrature. One of the waves is referred to as the in-phase (I) signal (I chain), and the other is referred to as the quadrature (Q) signal (Q chain). The transmitted signal is created by adding the two carrier waves together. At a receiver, the two waves may be separated (e.g., demodulated) because of their orthogonality. The modulated signals are often low-frequency and low-bandwidth waveforms compared to the carrier frequency.

[0013] LO leakage occurs if the carrier signal leaks into the final transmission. The LO leakage should be below a certain level to meet various standards, such as IEEE 802.11 (Wi-Fi) standards. To meet the standards, the carrier signal should not be present at the final output of the transmitter. Some systems have a power detector at the transmitter output that measures various LO leakage values and then performs calculations to iteratively find a correction value to remove the LO leakage. However, these iterative processes take time and consume a large amount of power, which makes it difficult to meet LO leakage standards.

[0014] In examples herein, a loopback path (or feedback path) is implemented to measure and then correct LO leakage. An auxiliary receiver in the loopback path receives the transmitted QAM signals and uses these signals to estimate LO leakage, which appears as a constant value. The auxiliary receiver may also have its own leakage that appears as a constant value. The two constant values (of the transmitter and auxiliary receiver) are decoupled to find the LO leakage of the transmitter. The examples herein also

account for other transmitter impairments, such as IQ mismatch (IQMM), phase noise, radio frequency (RF) nonlinearities, etc. IQMM is caused by non-ideal characteristics of the I path and the Q path.

[0015] In the loopback path described herein, hardware switches compensate for the impairments in the auxiliary receiver path. The loopback path includes a complex mixer with differential inputs. Hardware switches coupled to the differential inputs have two configurations. In the first configuration, the transmitted signal, plus a signal added by the auxiliary path (e.g., leakage), are provided to the complex mixer in a non-inverting configuration. In the second configuration, the switches are switched, and the transmitted signal, minus the signal added by the auxiliary path, are provided to the complex mixer in an inverting configuration (e.g., the inverted signal is 180° out of phase from the non-inverted signal). A controller analyzes the outputs of the complex mixer for both the non-inverting and the inverting configuration, and then removes the constant leakage signal added by the auxiliary path. The controller then performs other calculations described herein to estimate and correct for the transmitter LO leakage. This process is performed by the controller for both the I chain and the Q chain. In the examples herein, the time for performing this process is small compared to other systems. The raw IQMM or LO leakage of the loopback path may be high as long as it is the same for both configurations (non-inverting and inverting), because the IQMM and LO leakage will be removed by switching the non-inverting/inverting configuration of the inputs to the complex mixer. This feature relaxes the analog design requirements for the system. Also, the solution is applicable for all QAM-based architectures.

[0016] FIG. 1 is a block diagram of a transmitter and auxiliary receiver in accordance with various examples herein. System 100 includes some components of a transmitter and auxiliary receiver, but other systems may include components or circuitry that is not shown here for simplicity. System 100 includes low-dropout (LDO) voltage regulators 102, 104, and 106. Analog LDO 102 provides a supply voltage to transmit analog components 108, 110, 112, and 114, which are described below. RF LDO 104 provides a supply voltage to pre-power amplifier (PPA) 116. PA LDO 106 provides a supply voltage to power amplifier (PA) 118.

[0017] The transmitter portion of system 100 includes digital-to-analog converter (DAC) 108 (I_DAC) for the I chain and DAC 110 (Q_DAC) for the Q chain. DAC 108 is coupled to I chain filter 112 (TX_I_FILTER), and DAC 110 is coupled to Q chain filter 114 (TX_Q_FILTER). I chain filter 112 and Q chain filter 114 are coupled to TX mixer 120. TX mixer 120 is also coupled to TX local oscillator (TX LO) 122. An output of TX mixer 120 is coupled to PPA 116. An output of PPA 116 is coupled to PA 118. An output of PA 118 is coupled to antenna 124. Antenna 124 is represented with a load 126 coupled to ground 128. Antenna 124 may also be coupled to variable capacitors 130 and 132, which are each coupled to ground 128. Antenna 124 may also be coupled to capacitors 160 and 162 in one example. Capacitors 130, 132, 160, and 162 form an attenuator network 164 (e.g., capacitor attenuator 164) for auxiliary receiver 134. Capacitors 130, 132, 160, and 162 attenuate the power of the output of PA 118 before the output is provided to the complex mixers in auxiliary receiver 134. In another example, other circuitry may attenuate the output of PA 118.

[0018] The components of system 100 described above are part of the transmitter path for system 100. The loopback path in this example includes the transmitter path and the auxiliary receiver 134. Auxiliary receiver 134 may include many other components not shown in FIG. 1, such as LOs, LDOs, and other circuitry. Auxiliary receiver 134 may be coupled to a controller 136 configured to perform the operations described herein. Auxiliary receiver 134 includes complex mixers 138 and 140. Complex mixer 138 is a complex mixer for the I chain, and complex mixer 140 is a complex mixer for the Q chain. Complex mixer 138 is coupled to I chain filter (RX_I_FILTER) 142, and complex mixer 140 is coupled to Q chain filter (RX_Q_FILTER) 144. A receive local oscillator (RX LO) 141 provides an LO signal to complex mixers 138 and 140 for mixing the signals. The RX LO 141 signal may be the same LO signal as the LO signal from TX LO 122 in one example. I chain filter 142 is coupled to I chain analog-to-digital converter (ADC) 146 (I_ADC). Q chain filter 144 is coupled to Q chain ADC 148 (Q_ADC).

[0019] In an example operation, a signal is transmitted in system 100 via the I chain and the Q chain. The message signal on the I chain passes through DAC 108, where the digital signal is converted to an analog signal. The analog signal on the I chain is received by I chain filter 112 and filtering is performed. Likewise, the message signal on the Q chain passes through DAC 110, where the digital signal is converted to an analog signal. The analog signal on the Q chain is received by Q chain filter 114. Both message signals (the I chain and the Q chain) are sent to TX mixer 120. The signals are mixed with a carrier signal from TX LO 122 by TX mixer 120. As described above, the I and Q message signals are out of phase with another by 90° in the signal provided by the TX mixer 120. The TX mixer 120 output signal that includes the I and Q message signals is then received by PPA 116 and PA 118, where the mixer 120 output signal is amplified before being transmitted via antenna 124.

[0020] Auxiliary receiver 134 receives a copy of the transmitted signal and uses the message signals therein in a loopback path to compensate for the LO leakage as described herein. The transmitter LO leakage and other impairments of both the transmitter and the auxiliary receiver 134 are estimated and corrected with the techniques herein. Auxiliary receiver 134 includes two complex mixers 138 and 140. One complex mixer 138 receives and mixes the I chain, and the other complex mixer 140 receives and mixes the Q chain. Complex mixer 138 provides the I chain signal to I chain filter 142, and then the filtered I chain signal is provided to I chain ADC 146. I chain ADC 146 provides the I chain signal to controller 136. Complex mixer 140 provides the Q chain signal to Q chain filter 144, and then the filtered Q chain signal is provided to Q chain ADC 148. Q chain ADC 148 provides the Q chain signal to controller 136.

[0021] As described below, complex mixers 138 and 140 each produce a non-inverted signal and an inverted signal. Therefore, four signals are provided to controller 136 (non-inverted and inverted signals for both the I chain and the Q chain). With these four signals, four equations may be created and solved by controller 136 to compensate for the transmitter LO leakage. Using the compensation, the constant leakage in auxiliary receiver 134 may also be removed. The controller 136 may produce a correction signal that is

provided to the transmitter to correct for the LO leakage and other impairments. The correction signal may be supplied at an LO correction block 166, which provides the LO correction signal to the inputs of DACs 108 and 110. LO correction block 166 may include any suitable hardware to receive a correction signal and provide the correction signal to the transmit chain. In other examples, the LO correction signal may be provided to other components in the transmit chain.

[0022] Hardware switches for creating the non-inverting and inverting signals may be included within complex mixers 138 and 140. FIGS. 3-4, described below, provide one example configuration of the hardware switches within complex mixers 138 and 140.

[0023] Controller 136 may include a processor 150 and memory 152 in one example. The memory 152 may include any suitable data, code, logic, or instructions. The processor 150 is configured to read and execute computer-readable instructions. For example, the processor 150 is configured to invoke and execute instructions in a program stored in the memory 152, including instructions. Instructions may perform the actions described herein, such as estimating and providing a correction for LO leakage and other impairments.

[0024] In an example, the memory 152 may be integrated with the processor 150. The memory 152 is configured to store various software programs and/or multiple groups of instructions. In some examples, the memory 152 is configured to store the instructions for implementing some or all of the various methods and processes provided in accordance with the various examples of this description.

[0025] In another example, elements of controller 136 disclosed herein may use any combination of dedicated hardware and instructions stored in a non-transitory medium, such as the memory 152. The non-transitory medium includes all electronic mediums or media of storage, except signals. The processor 150 may include one or more microcontrollers, application-specific integrated circuits (ASICs), central processing units (CPUs), graphics processing units (GPUs), and/or other processing resources configured to execute instructions stored on the medium. Examples of suitable non-transitory computer-readable media include one or more flash memory devices, battery-backed random access memory (RAM), solid state drives (SSDs), hard disk drives (HDDs), optical media, and/or other memory devices suitable for storing the instructions for the processor 150.

[0026] FIG. 2 is a block diagram of a transmit/auxiliary receiver (TX/AuxRX) chain in accordance with various examples herein. Some components in system 200 represent functions, and may be embodied in hardware, software, or may be performed by any suitable component, such as controller 136.

[0027] In one example operation, a signal x_{in} 202 to be transmitted receives IQ correction at IQMM correction 204 (from the loopback loop, described below). IQMM correction 204 produces a corrected output signal y_{corr} 206. TX IQMM 208 represents the transmit IQMM, which is corrected by y_{corr} 206 from the loopback path. A signal y_{BB} 210 (e.g., a baseband signal) is provided to a complex mixer 212. The complex mixer 212 mixes the I and Q chain signals, and transmits the signals, where the transmitted signal is y_{Tx} 214.

[0028] The other components in system 200 provide the loopback path for LO leakage estimation and correction. The transmitted signal y_{Tx} 214 is provided to the auxiliary

receiver 134 for correction. The transmitted signal y_{Tx} 214 undergoes gain and rotation 216 as the signal passes through the auxiliary receiver 134 loop. The signal that results after gain and rotation is designated by y_{rot} 218. y_{rot} 218 is provided to complex mixer 220. The operation of complex mixer 220 is described below. Complex mixer 220 includes the hardware switches that produce a non-inverted signal and an inverted signal for each of the I and Q chains. After the non-inverted signals and the inverted signals are produced by complex mixer 220, AuxRx IQMM 222 is applied to the signals to produce a y_{RX} 224 signal. y_{RX} 224 is provided to IQMM estimation 226, where a controller or processor performs an IQMM estimation so the IQMM may be corrected. The process for performing IQMM estimation and correction is described below. After IQMM estimation is performed, a signal v_i 228 is provided to IQMM correction 204 to correct LO leakage and other impairments in the transmitted signal.

[0029] In examples described herein, switches in complex mixer 220 are activated to create the non-inverting and inverting signals to properly determine the constant leakage values. Complex mixer 220 has differential inputs. The switches in complex mixer 220 invert the connections at the input of complex mixer 220 to negate the transmit local oscillator leakage (TX LOL) as described below.

[0030] FIG. 3 is a diagram of selected components of auxiliary receiver 134 in accordance with various examples herein. Auxiliary receiver 134 may include other circuitry not shown in FIG. 3.

[0031] In this example, a capacitor attenuator 164, hardware switches, and complex mixer 138 of auxiliary receiver 134 are shown.

[0032] In operation, auxiliary receiver 134 resides in the loopback path and receives the transmitted signal, shown here as y_{Tx} 214. y_{rot} 218 is the transmitted signal y_{Tx} 214 that has undergone some gain and rotation in the loopback path. The signal y_{Tx} 214 is received by capacitor attenuator 164, which attenuates the signal and produces y_{rot} 218. Capacitor attenuator 164 provides the attenuated signal y_{rot} 218 to inputs 304 and 306 of complex mixer 138 via the switches 312, 314, 316, and 318. Complex mixer 138 produces a differential output signal at output 308 (plus output P) and output 310 (minus output M).

[0033] Switches 312, 314, 316, and 318 provide complex mixer 138 with either a non-inverted signal or an inverted signal. In a first phase of operation, the switches in the "A" path (switches 312 and 314) are closed, and the switches in the "B" path (switches 316 and 318) are open. In this phase, the signal from capacitor attenuator 164 is provided to complex mixer 138 via the "A" path in a non-inverting configuration.

[0034] In the second phase of operation, the switches are switched. In the second phase, the switches in the "A" path (switches 312 and 314) are open, and the switches in the "B" path (switches 316 and 318) are closed. In this phase, the signal from capacitor attenuator 164 is provided to complex mixer 138 via the "B" path in an inverting configuration. In one example, a controller or processor provides signals to switches 312, 314, 316, and 318 to open or close the switches at the appropriate time.

[0035] After both phases are complete, two readings are collected from the output of complex mixer 138. The first "A" path produces a signal X at the output of the complex mixer 138, and a signal Y is added to X by the auxiliary

receiver **134** path. The second “B” path produces a signal minus X at the output of the complex mixer **138**, and a signal Y is added to X from the auxiliary receiver **134** path. A controller or processor can then solve for the signal X using equations that represent these two readings from the output of complex mixer **138**. One example estimation and correction process is described below.

[0036] Complex mixer **140** for the Q chain may be implemented similar to complex mixer **138** and may include a set of differential inputs coupled to a set switches similar to switches **312-318** that are similarly coupled to the capacitor attenuator **164** to receive the signal y_{rot} **218**.

[0037] FIG. **4** is a circuit diagram of the switches and complex mixer in accordance with various examples herein. System **400** may be located within auxiliary receiver **134** in one example. System **400** includes bias voltage circuitry **402**, switch network **404**, and complex mixer **138**. The voltage VSSA may be a ground voltage in one example. VBIAS is a middle voltage (between the supply voltage and ground) that the input signal swings around.

[0038] Bias voltage circuitry **402** includes a terminal **406** that provides VBIAS, and terminals **408** and **410**. Terminals **408** and **410** provide LO_P and LO_M, which are the carrier signals, e.g., the switching signal that mixes the higher frequency of the complex mixer **138**. Bias voltage circuitry **402** includes capacitors **412** and **414**, transistor **416**, and resistors **418** and **420**. Transistor **416** may be one or more transistors in a transmission gate configuration that provides a control terminal to turn on or off the circuit. Bias voltage circuitry **402** is configured to provide signal LOP_INT at terminal **422** and signal LOM_INT at terminal **424**. These signals are voltages used by complex mixer **138** to control the operation of complex mixer **138** by switching the switches in complex mixer **138** on and off.

[0039] Switch network **404** includes switches **312**, **314**, **316**, and **318** as described above with respect to FIG. **3**. The switches are configured to produce an “A” path and a “B” path to provide the input signal to complex mixer **138**. The “A” path is a non-inverting configuration, and the “B” path is an inverting configuration. Switch network **404** also includes input terminals **426** and **428**, capacitors **430** and **432**, and resistors **434** and **436**.

[0040] In this example, switches **312**, **314**, **316**, and **318** are embodied in transistors. Other examples may use different circuit components to create switches that provide complex mixer **138** with a non-inverting signal and an inverting signal. Complex mixer **138** includes transistors **442**, **444**, **446**, and **448**. Complex mixer **138** also includes input terminals **450** and **452**, and output terminals **454** and **456**.

[0041] In an example operation, switch network **404** receives the differential input signal (such as y_{rot} **218**) at input terminals **426** and **428**. INP represents the plus input signal, and INM represents the minus input signal. Capacitors **430** and **432** remove the constant portion of the input signal, so only the high frequency components of the signal are coupled from the input to the output. Resistors **434** and **436** are sized to be larger than the resistance of the switches in one example. The “on” resistance of the switches may be less than 5% of the size of resistors **434** and **436** in one example. Therefore, if the “on” resistance of the switches is slightly non-linear, the entire system will not become non-linear.

[0042] The switches **312**, **314**, **316**, and **318** allow the switch network **404** to achieve either the non-inverting or the inverting configuration. The switches are controlled by ENZ_FLIP (flip disable) and EN_FLIP (flip enable) signals, which are complementary signals. An ENZ_FLIP signal is provided at a gate or control terminal of the transistors that make up switches **312** and **314**. If the ENZ_FLIP signal is high, EN_FLIP is low, and the transistors of switches **312** and **314** are on, and therefore operate as closed switches. The input signal traverses along the “A” path (designated by arrows) to the input terminals **450** and **452** of complex mixer **138**. This is the non-inverting configuration. If the ENZ_FLIP signal is high, the EN_FLIP signal is low. A low EN_FLIP signal “opens” switches **316** and **318** by turning off the associated transistors. A controller or processor (not shown in FIG. **4**) controls the operation of switches **312**, **314**, **316**, and **318** via the ENZ_FLIP and EN_FLIP signals.

[0043] For the inverting configuration, the controller or processor provides a high EN_FLIP signal and a low ENZ_FLIP signal. A high EN_FLIP signal turns on the transistors of switches **316** and **318**. A low ENZ_FLIP signal turns off the transistors of switches **312** and **314**. In this configuration, the input signal traverses along the “B” path (designated by arrows) to the input terminals **450** and **452** of complex mixer **138**.

[0044] Complex mixer **138** receives either the non-inverting signal or the inverting signal at input terminals **450** and **452**, depending on the configuration of switches **312**, **314**, **316**, and **318**. Transistors **442**, **444**, **446**, and **448** are mixer switches that control the operation of complex mixer **138** to produce a differential output signal at output terminals **454** and **456**. These transistors switch the complex mixer **138** on or off at the carrier frequency, which mixes the input signal and produces a differential output signal. The differential output signal is represented by output plus (OUTP) and output minus (OUTM). The transistors **442** and **444** are biased at a gate or control terminal with the LOM_INT signal, provided by bias voltage circuitry **402** at terminal **424**. The transistors **446** and **448** are biased at a gate or control terminal with the LOP_INT signal, provided by bias voltage circuitry **402** at terminal **422**. The LOM_INT and LOP_INT signals switch the transistors **442**, **444**, **446**, and **448** on or off at the carrier frequency to mix the signal and produce the differential output signal. A controller or processor (such as controller **136**) receives the differential output signal from output terminals **454** and **456** for both the non-inverting and the inverting configurations, and then provides the estimation and correction for LO leakage and other impairments as described below.

[0045] In an example, the “A” and “B” paths and the other circuitry in switch network **404** should not add any distortions to the signal, and both paths should have similar LO leakage. Also, the switches should be sized properly so they are not too large or too small. Switches that are too small may reduce the gain of auxiliary receiver **134** and cause signal distortion. Switches that are too large may have a large switch capacitance when in the off state, which may couple the signal through the non-active path (either the “A” or the “B” path) and cause signal distortion.

[0046] Complex mixer **140** for the Q chain may be implemented similar to complex mixer **138**, and in an example, the circuitry in system **400** is duplicated, with a first copy associated with complex mixer **138** and a second copy associated with complex mixer **140** in FIG. **1**. One complex

mixer receives the I chain signal and the other complex mixer receives the Q chain signal. A controller or processor (such as controller **136**) receives both the I chain signals (inverted and non-inverted) and the Q chain signals (inverted and non-inverted) and then provides the estimation and correction.

[0047] One example process for estimating and correcting the TX LO leakage is described herein. Other processes may be useful in other examples. In this example process, the LO leakage of the signal for the I chain and the Q chain may be represented by Equations (1) and (2):

$$y_{DC-Tx} = I_{Tx}; Q_{Tx} \quad (1)$$

$$y_{Tx-DC}(t) = I_{Tx} \cdot a \cdot \cos(\Phi) \cdot \text{Re}\{e^{j\omega_c t}\} + (Q_{Tx} + I_{Tx} \cdot a \cdot \sin(\Phi)) \cdot \text{Im}\{e^{j\omega_c t}\} \quad (2)$$

[0048] As the signal passes through the auxiliary receiver **134** loop, the signal is subjected to gain (A) and rotation (θ), as shown in Equation (3) for both the I chain and the Q chain:

$$G_{Rx} = A_I \cdot \cos(\theta_I); A_Q \cdot \cos(\theta_Q) \quad (3)$$

[0049] The auxiliary receiver mixer input to the ADCs (e.g., ADC **146** and **148**) provides additional gain as shown in Equation (4):

$$G_{Rx} = B_I; B_Q \quad (4)$$

[0050] The auxiliary receiver **134** adds its own constant leakage, as shown in Equation (5):

$$y_{DC-Rx} = I_{Rx}; Q_{Rx}$$

[0051] In the non-inverted configuration, the constant leakage may be represented by Equation (6) for the I chain and Equation (7) for the Q chain:

$$y_{DC-ADC-I} = I_{Tx} A_I B_I \cos(\theta_I + \Phi) + Q_{Tx} A_Q B_Q \sin(\theta_Q) + I_{Rx} B_I \quad (6)$$

$$y_{DC-ADC-Q} = I_{Tx} A_I B_I \sin(\theta_I + \Phi) + Q_{Tx} A_Q B_Q \cos(\theta_Q) + Q_{Rx} B_Q \quad (7)$$

[0052] In the inverted configuration, the constant leakage may be represented by Equation (8) for the I chain and Equation (9) for the Q chain:

$$y_{DC-ADC-I} = -I_{Tx} A_I B_I \cos(\theta_I + \Phi) - Q_{Tx} A_Q B_Q \sin(\theta_Q) + I_{Rx} B_I \quad (8)$$

$$y_{DC-ADC-Q} = -I_{Tx} A_I B_I \sin(\theta_I + \Phi) - Q_{Tx} A_Q B_Q \cos(\theta_Q) + Q_{Rx} B_Q \quad (9)$$

[0053] The four Equations (6, 7, 8, and 9) may be solved to determine the transmitter (TX) LO leakage I_{Tx} and Q_{Tx} . For simplicity, substitutions may be performed as follows:

$$x = I_{Tx}$$

$$y = Q_{Tx}$$

$$a = A_I B_I \cos(\theta_I + \Phi)$$

$$b = A_Q B_Q \sin(\theta_Q)$$

$$c = A_I B_I \sin(\theta_I + \Phi)$$

$$d = A_Q B_Q \cos(\theta_Q)$$

$$p = I_{Rx} B_I$$

$$s = Q_{Rx} B_Q$$

$$y_I = y_{DC-ADC-I}$$

$$y_Q = y_{DC-ADC-Q}$$

[0054] With these substitutions, the variables to solve are a, b, c, and d. Therefore, four linear equations may be used to solve for these variables. The overall constant leakage from the ADC is shown in Equation (10):

$$\begin{bmatrix} a & b \\ c & d \end{bmatrix} \begin{bmatrix} I_{Tx} \\ Q_{Tx} \end{bmatrix} + \begin{bmatrix} p & 0 \\ 0 & s \end{bmatrix} \begin{bmatrix} I_{Rx} \\ Q_{Rx} \end{bmatrix} = \begin{bmatrix} y_I \\ y_Q \end{bmatrix} \quad (10)$$

[0055] I_{Tx} and Q_{Tx} are the I and Q chain signals in the transmitter, respectively. I_{Rx} and Q_{Rx} are the I and Q chain signals received by the auxiliary receiver **134**, respectively. For simplicity, more substitutions may be performed:

$$A = \begin{bmatrix} a & b \\ c & d \end{bmatrix}$$

$$B = \begin{bmatrix} p & 0 \\ 0 & s \end{bmatrix}$$

$$T = \begin{bmatrix} I_{Tx} \\ Q_{Tx} \end{bmatrix}$$

$$R = \begin{bmatrix} I_{Rx} \\ Q_{Rx} \end{bmatrix}$$

$$Y = \begin{bmatrix} y_I \\ y_Q \end{bmatrix}$$

[0056] In this solution, two insertions of constant leakage for each chain are used, which are given by I_{Tx-1} , I_{Tx-2} , Q_{Tx-1} , and Q_{Tx-2} . Signals I_{Tx-1} , I_{Tx-2} , Q_{Tx-1} , and Q_{Tx-2} are transmitted, and some LO leakage occurs. These insertions are shown in Equations (11) and (12):

$$T_1 = \begin{bmatrix} I_{Tx} + I_{Tx-1} \\ Q_{Tx} + Q_{Tx-1} \end{bmatrix} \quad (11)$$

$$T_2 = \begin{bmatrix} I_{Tx} + I_{Tx-2} \\ Q_{Tx} + Q_{Tx-2} \end{bmatrix} \quad (12)$$

[0057] After these signals are transmitted, the three captures S_1 , S_2 , and S_3 are determined. Equations (13), (14), and (15) show the three captures. Y , $Y1$, and $Y2$ are the resulting signals at the output:

$$S_1 \rightarrow AT + BR = Y \quad (13)$$

$$S_2 \rightarrow A(T + T_1) + BR = Y_1 \quad (14)$$

$$S_3 \rightarrow A(T + T_2) + BR = Y_2 \quad (15)$$

[0058] With these captures, T_1 , T_2 , Y , Y_1 , and Y_2 are known. T_1 and T_2 are transmitted signals, and Y , Y_1 , and Y_2 are received signals. Therefore, the rest of the variables in Equations (13), (14), and (15) may be solved for. If S_1 is subtracted from S_2 , and S_1 is also subtracted from S_3 , BR and T may be removed. These subtractions eliminate the BR part (which is the constant leakage from the auxiliary receiver 134). These operations are shown in Equations (16) and (17):

$$S_2 - S_1 \rightarrow AT_1 = Y_1 - Y \quad (16)$$

$$S_3 - S_1 \rightarrow AT_2 = Y_2 - Y \quad (17)$$

[0059] The matrices above may be rewritten as Equations (18) and (19):

$$Y_1 - Y = \begin{matrix} y_1 \\ y_2 \end{matrix} \quad (18)$$

$$Y_2 - Y = \begin{matrix} y_3 \\ y_4 \end{matrix} \quad (19)$$

[0060] The only remaining unknown variable is A , which may be estimated by rewriting the matrices for T_1 and T_2 , shown in Equations (11) and (12) above. Equation (20) may be used to estimate A :

$$\begin{bmatrix} I_{Tx-1} & Q_{Tx-1} & 0 & 0 \\ 0 & 0 & I_{Tx-1} & Q_{Tx-1} \\ I_{Tx-2} & Q_{Tx-2} & 0 & 0 \\ 0 & 0 & I_{Tx-2} & Q_{Tx-2} \end{bmatrix} \begin{bmatrix} a \\ b \\ c \\ d \end{bmatrix} = \begin{bmatrix} y_1 \\ y_2 \\ y_3 \\ y_4 \end{bmatrix} \quad (20)$$

[0061] The transmit features are represented by A , and Equation (20) provides this estimate so the transmit features may be removed, such as the TX LO leakage. However, the auxiliary receiver 134 components still remain. The auxiliary receiver 134 components (e.g., constant leakage and other impairments) may be removed by switching the switch network 404 as described above and providing a non-inverted signal and an inverted signal. From Equation (20), A is known, and then T (the constant leakage represented by I_{Tx} and Q_{Tx}) may be estimated using Equations (21) and (22):

$$AT + BR = Y_1 \quad (21)$$

$$-AT + BR = Y_2 \quad (22)$$

[0062] Equation (21) represents the non-inverting path (Y_1), and Equation (22) represents the inverting path (Y_2). By subtracting these two equations, T can be determined, as shown below in Equations (23) and (24):

$$AT = \frac{1}{2}(Y_1 - Y_2) \quad (23)$$

$$T = \frac{1}{2}A^{-1}(Y_1 - Y_2) \quad (24)$$

[0063] Therefore, two processes are performed to estimate leakage measurement T and provide a correction signal to the transmitter. The first process is to estimate A , which requires the three captures S_1 , S_2 , and S_3 . The second process is estimating T , which uses one more capture. After T is estimated with the loopback path, a negative T may be produced in the transmit path to correct for T . In the examples herein, four signals provide the information for estimating and correcting T (the non-inverted I signal, the inverted I signal, the non-inverted Q signal, and the inverted Q signal). The circuitry and hardware described herein produce these four signals, and the method performs the calculations to estimate and correct the TX LO leakage and other impairments.

[0064] The process for estimating and correcting constant leakage described with Equations (1) through (24) is merely one example, and other examples may also be useful. This process may be performed with software or firmware by a controller, such as controller 136.

[0065] FIG. 5 is a flow diagram of a method 500 for estimating and correcting a transmitter local oscillator leakage in accordance with various examples herein. The steps of method 500 may be performed in any suitable order. The hardware components described above with respect to FIGS. 1-4 may perform method 500 in some examples. Any suitable hardware, software, or digital logic may perform method 500 in some examples.

[0066] Method 500 begins at 510, where an auxiliary receiver 134 receives a differential QAM input signal from a transmitter. The input signal may include an I chain signal and a Q chain signal. The auxiliary receiver 134 may include hardware, such as a mixer, that is duplicated so the auxiliary receiver 134 may process both the I chain signal and the Q chain signal as described herein.

[0067] Method 500 continues at 520, where a controller, such as controller 136, enables a first switch 312 and a second switch 314 to provide the differential QAM input signal to a complex mixer 138 in a non-inverting configuration. The switches may be transistors in one example. In other examples, a different number of switches may be useful.

[0068] Method 500 continues at 530, where a controller, such as controller 136, enables a third switch 316 and a fourth switch 318 to provide the differential QAM input signal to the complex mixer 138 in an inverting configuration. The switches may be transistors in one example. In other examples, a different number of switches may be useful.

[0069] Method 500 continues at 540, where the complex mixer 138 mixes the differential QAM input signal as provided in blocks 520 and 530 to produce an output signal. The complex mixer may use transistors that switch at a frequency of a carrier frequency to mix the differential QAM input signal in one example.

[0070] Method 500 continues at 550, where the controller 136 estimates or determines a local oscillator leakage associated with the transmitter based at least in part on the differential output signal. In one example, the controller

receives an inverted differential output signal and a non-inverted differential output signal. The controller also receives both the inverted and the non-inverted differential output signal from the I chain and from the Q chain. With these four signals (non-inverted in-phase, inverted in-phase, non-inverter quadrature, inverted quadrature), the controller 136 may estimate the local oscillator leakage from the transmitter, and may also correct some impairments of the auxiliary receiver 134.

[0071] Method 500 continues at 560, where the controller 136 corrects the local oscillator leakage. In an example, the controller 136 creates a correction signal that is the inverse of the LO leakage. The correction signal may be produced in the transmitter based on the estimation. In one example, the correction signal may be an offset within the data provided to DACs 108 and 110 by LO correction block 166, where the offset corrects the LO leakage.

[0072] In examples herein, a loopback path is implemented to measure and then correct transmitter LO leakage using an auxiliary receiver. The examples herein also account for other transmitter impairments, such as IQ mismatch (IQMM), phase noise, radio frequency (RF) nonlinearities, etc. In the loopback path, hardware switches compensate for the impairments in the auxiliary receiver path with two configurations. In the first configuration, the transmitted signal plus a signal added by the auxiliary path (e.g., leakage) are provided to the complex mixer in a non-inverting configuration. In the second configuration, the switches are switched, and the transmitted signal minus the signal added by the auxiliary path are provided to the complex mixer in an inverting configuration. A controller analyzes the outputs of the complex mixer for both the non-inverting and the inverting configuration, and then removes the constant signal added by the auxiliary path. This process is performed by the controller for both the I chain and the Q chain. In the examples herein, the time for performing this process is small compared to other systems. The raw IQMM or LO leakage of the loopback path may be high as long as it is the same for both configurations (non-inverting and inverting), which relaxes the analog design requirements for the system. Also, the solution is applicable for all QAM-based architectures.

[0073] In this description, the term “couple” may cover connections, communications, or signal paths that enable a functional relationship consistent with this description. For example, if device A generates a signal to control device B to perform an action: (a) in a first example, device A is coupled to device B by direct connection; or (b) in a second example, device A is coupled to device B through intervening component C if intervening component C does not alter the functional relationship between device A and device B, such that device B is controlled by device A via the control signal generated by device A.

[0074] A device that is “configured to” perform a task or function may be configured (e.g., programmed and/or hardwired) at a time of manufacturing by a manufacturer to perform the function and/or may be configurable (or reconfigurable) by a user after manufacturing to perform the function and/or other additional or alternative functions. The configuring may be through firmware and/or software programming of the device, through a construction and/or layout of hardware components and interconnections of the device, or a combination thereof.

[0075] A circuit or device that is described herein as including certain components may instead be coupled to those components to form the described circuitry or device. For example, a structure described as including one or more semiconductor elements (such as transistors), one or more passive elements (such as resistors, capacitors, and/or inductors), and/or one or more sources (such as voltage and/or current sources) may instead include only the semiconductor elements within a single physical device (e.g., a semiconductor die and/or integrated circuit (IC) package) and may be coupled to at least some of the passive elements and/or the sources to form the described structure either at a time of manufacture or after a time of manufacture, for example, by an end-user and/or a third-party.

[0076] Circuits described herein are reconfigurable to include the replaced components to provide functionality at least partially similar to functionality available prior to the component replacement. Components shown as resistors, unless otherwise stated, are generally representative of any one or more elements coupled in series and/or parallel to provide an amount of impedance represented by the shown resistor. For example, a resistor or capacitor shown and described herein as a single component may instead be multiple resistors or capacitors, respectively, coupled in parallel between the same nodes. For example, a resistor or capacitor shown and described herein as a single component may instead be multiple resistors or capacitors, respectively, coupled in series between the same two nodes as the single resistor or capacitor.

[0077] Uses of the phrase “ground” in the foregoing description include a chassis ground, an Earth ground, a floating ground, a virtual ground, a digital ground, a common ground, and/or any other form of ground connection applicable to, or suitable for, the teachings of this description. In this description, unless otherwise stated, “about,” “approximately” or “substantially” preceding a parameter means being within ± 10 percent of that parameter. Modifications are possible in the described examples, and other examples are possible within the scope of the claims.

What is claimed is:

1. A system comprising:

processing circuitry capable of providing a first set of values;

transmission circuitry coupled to the processing circuitry and capable of producing a signal based on the first set of values;

receiver circuitry coupled to the transmission circuitry that includes:

a first set of switching circuitry that includes:

first and second inputs coupled to the transmission circuitry;

first and second outputs; and

a first set of switches capable of:

in a first configuration, coupling the first input of the first set of switching circuitry to the first output of the first set of switching circuitry and coupling the second input of the first set of switching circuitry to the second output of the first set of switching circuitry; and

in a second configuration, coupling the first input of the first set of switching circuitry to the second output of the first set of switching circuitry and coupling the second input of the first

set of switching circuitry to the first output of the first set of switching circuitry;

- a first mixer coupled to the first and second outputs of the first set of switching circuitry;
- a first filter coupled to the first mixer;
- a first analog to digital converter coupled to the first filter and capable of providing a second set of values based on the first set of values;
- a second set of switching circuitry that includes:
 - first and second inputs coupled to the transmission circuitry;
 - first and second outputs; and
 - a second set of switches capable of:
 - in a first configuration, coupling the first input of the second set of switching circuitry to the first output of the second set of switching circuitry and coupling the second input of the second set of switching circuitry to the second output of the second set of switching circuitry; and
 - in a second configuration, coupling the first input of the second set of switching circuitry to the second output of the second set of switching circuitry and coupling the second input of the second set of switching circuitry to the first output of the second set of switching circuitry;
- a second mixer coupled to the first and second outputs of the second set of switching circuitry;
- a second filter coupled to the second mixer; and
- a second analog to digital converter coupled to the second filter and capable of providing a third set of values based on the first set of values;

wherein the processing circuitry is capable of determining an adjustment to apply to transmission of a fourth set of values based on the second set of values and the third set of values.

2. The system of claim 1, wherein:

- the producing of the signal by the transmission circuitry uses a local oscillator signal; and
- the adjustment is a compensation for an effect of the local oscillator signal.

3. The system of claim 1, wherein:

- the second set of values includes:
 - a first subset associated with the first set of switches being in the first configuration; and
 - a second subset associated with the first set of switches being in the second configuration; and
- the adjustment is based on a difference between the first subset of the second set of values and the second subset of the second set of values.

4. The system of claim 1 further comprising an antenna coupled to the transmission circuitry.

5. The system of claim 1, wherein the first set of switching circuitry includes:

- a first transistor that includes a source and a drain;
- a second transistor that includes a source coupled to the source of the first transistor and includes a drain;
- a third transistor that includes a source and includes a drain coupled to the drain of the first transistor; and
- a fourth transistor that includes a source coupled to the source of the third transistor and includes a drain coupled to the drain of the second transistor.

6. The system of claim 5, wherein the first set of switching circuitry includes:

- a first capacitor and a first resistor coupled in series between the first input of the first set of switching circuitry and the source of the first transistor; and
- a second capacitor and a second resistor coupled in series between the second input of the first set of switching circuitry and the source of the third transistor.

7. The system of claim 1, wherein:

- the first set of switching circuitry, the first mixer, the first filter, and the first analog to digital converter are associated with an in-phase signal chain; and
- the second set of switching circuitry, the second mixer, the second filter, and the second analog to digital converter are associated with a quadrature signal chain.

8. The system of claim 1 further comprising correction circuitry coupled between the processing circuitry and the transmission circuitry and capable of applying the adjustment to the fourth set of values.

9. The system of claim 1 further comprising a set of capacitors coupled between the transmission circuitry and the receiver circuitry.

10. A system comprising:

- receiver circuitry capable of receiving a signal that is based on a first set of values, wherein the receiver circuitry includes:
 - switching circuitry that includes:
 - first and second inputs;
 - first and second outputs; and
 - a set of switches capable of:
 - in a first configuration, coupling the first input of the set of switching circuitry to the first output of the set of switching circuitry and coupling the second input of the set of switching circuitry to the second output of the set of switching circuitry; and
 - in a second configuration, coupling the first input of the set of switching circuitry to the second output of first set of switching circuitry and coupling the second input of the set of switching circuitry to the first output of the set of switching circuitry;
 - a mixer coupled to the first and second outputs of the set of switching circuitry;
 - a filter coupled to the mixer; and
 - an analog to digital converter coupled to the filter and capable of providing a second set of values based on the first set of values; and
- processing circuitry coupled to the receiver circuitry and capable of determining a transmission adjustment based on the second set of values.

11. The system of claim 10, wherein:

- the signal is associated with a local oscillator signal; and
- the transmission adjustment is a compensation for an effect of the local oscillator signal.

12. The system of claim 10, wherein:

- the second set of values includes:
 - a first subset associated with the set of switches being in the first configuration; and
 - a second subset associated with the set of switches being in the second configuration; and
- the transmission adjustment is based on a difference between the first subset of the second set of values and the second subset of the second set of values.

13. The system of claim 10, wherein the first set of switching circuitry includes:

a first transistor that includes a source and a drain;
 a second transistor that includes a source coupled to the source of the first transistor and includes a drain;
 a third transistor that includes a source and includes a drain coupled to the drain of the first transistor; and
 a fourth transistor that includes a source coupled to the source of the third transistor and includes a drain coupled to the drain of the second transistor.

14. The system of claim **13**, wherein the first set of switching circuitry includes:

a first capacitor and a first resistor coupled in series between the first input of the first set of switching circuitry and the source of the first transistor; and
 a second capacitor and a second resistor coupled in series between the second input of the first set of switching circuitry and the source of the third transistor.

15. The system of claim **10**, wherein the set of switching circuitry, the mixer, the filter, and the analog to digital converter are associated with an in-phase signal chain.

16. A system comprising:

receiver circuitry that includes:

a first set of switching circuitry that includes:

first and second inputs;
 first and second outputs; and

a first set of switches capable of:

in a first configuration, coupling the first input of the first set of switching circuitry to the first output of the first set of switching circuitry and coupling the second input of the first set of switching circuitry to the second output of the first set of switching circuitry; and

in a second configuration, coupling the first input of the first set of switching circuitry to the second output of the first set of switching circuitry and coupling the second input of the first set of switching circuitry to the first output of the first set of switching circuitry;

a first mixer coupled to the first and second outputs of the first set of switching circuitry;

a second set of switching circuitry that includes:

first and second inputs;
 first and second outputs; and

a second set of switches capable of:

in a first configuration, coupling the first input of the second set of switching circuitry to the first output of the second set of switching circuitry and coupling the second input of the second set of switching circuitry to the second output of the second set of switching circuitry; and

in a second configuration, coupling the first input of the second set of switching circuitry to the second output of the second set of switching circuitry and coupling the second input of the second set of switching circuitry to the first output of the second set of switching circuitry; and

a second mixer coupled to the first and second outputs of the second set of switching circuitry.

17. The system of claim **16**, wherein the receiver circuitry includes:

a first filter coupled to the first mixer;

a first analog to digital converter coupled to the first filter and capable of providing a first set of values;

a second filter coupled to the second mixer; and

a second analog to digital converter coupled to the second filter and capable of providing a second set of values.

18. The system of claim **17** further comprising processing circuitry coupled to the receiver circuitry and capable of determining a transmission adjustment based on the first set of values and the second set of values.

19. The system of claim **18**, wherein the transmission adjustment is a compensation for an effect of a local oscillator signal.

20. The system of claim **18**, wherein:

the first set of values includes:

a first subset associated with the first set of switches being in the first configuration; and

a second subset associated with the first set of switches being in the second configuration; and

the transmission adjustment is based on a difference between the first subset of the first set of values and the second subset of the first set of values.

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