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DISPLAY PANEL AND METHOD FOR PREPARING DISPLAY PANEL

Abstract

The present disclosure discloses a display panel and a method for preparing a display panel. The display panel includes a first substrate, a second substrate, a signal wiring portion, a first outer lead wiring portion, a second outer lead wiring portion, a through-hole portion, and a bridging wiring portion. The first outer lead wiring portion is disposed on a side surface of the first substrate along a light emitting direction of the display panel, and the second outer lead wiring portion is disposed on a side surface of the second substrate facing away from the first substrate and is electrically connected to the first outer lead wiring portion through the bridging wiring portion disposed in the through-hole portion.

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Background/Summary

CROSS-REFERENCE TO RELATED APPLICATION [0001] This application is a continuation application of U.S. application Ser. No. 17/600,167, filed on Sep. 30, 2021, which is a US national phase application based upon an International Application No. PCT/CN2021/115278, filed on Aug. 30, 2021, which claims priority to Chinese Patent Application No. 202110709453.1, filed on Jun. 25, 2021. The entire disclosures of the above applications are incorporated herein by reference in their entireties.

FIELD OF INVENTION

[0002] The present disclosure relates to the field of display technologies, and in particular, to a display panel and a method for preparing a display panel.

BACKGROUND OF INVENTION

[0003] A light emitting diode (LED) is a semiconductor electronic component that can convert electric energy into light energy. Having features like small size, long service life, rich color, and low energy consumption, the LED is widely used in the fields of lighting, a display screen, signal light, backlight, toy, and the like. A mini LED, also known as a sub-millimeter light emitting diode and being 80 microns to 200 microns in size usually, is a new generation of LED technology taking on the characteristics of high efficiency, high reliability, high brightness, and fast response time of a small-pitch LED, and having lower power consumption and cost than the small-pitch LED.

[0004] Currently, a relatively long outer lead bonding (OLB) region is usually required to be reserved on an edge of a mini LED display panel, for bonding a chip on film (COF) and a printed circuit board (PCB), resulting in a relatively wide frame of the display panel. In addition, a large-sized mini LED display panel is usually formed by splicing a plurality of small-sized mini LED display panels. The relatively wide frame may cause a relatively large splice joint during the splicing, affecting the final display effect.

SUMMARY OF INVENTION

[0005] The present disclosure provides a display panel and a method for preparing a display plane, which can effectively narrow the width of a frame of a display panel and optimize the display effect.

[0006] The present disclosure provides a display panel, including a display region and a non-display region disposed outside at least one side of the display region, and the display panel further includes: [0007] a first substrate; [0008] a second substrate, disposed on a side surface of the first substrate facing away from a light emitting direction of the display panel; [0009] a signal wiring portion, disposed on a side surface of the first substrate along the light emitting direction of the display panel and disposed in the display region; [0010] a first outer lead wiring portion, disposed on the side surface of the first substrate along the light emitting direction of the display panel and in the non-display region, and electrically connected to the signal wiring portion; [0011] a second outer lead wiring portion, disposed on a side surface of the second substrate facing away from the first substrate; [0012] a through-hole portion, running through the first substrate and the second substrate; and [0013] a bridging wiring portion, filled in the through-hole portion, and electrically

connected to the first outer lead wiring portion and the second outer lead wiring portion respectively.

[0014] In the display panel provided in an embodiment of the present disclosure, the second outer lead wiring portion is disposed on an inner side of the first outer lead wiring portion close to the display region.

[0015] In the display panel provided in an embodiment of the present disclosure, the signal wiring portion includes a plurality of signal wirings, the first outer lead wiring portion includes a plurality of first outer lead wirings electrically connected to the plurality of signal wirings one by one, the through-hole portion includes a plurality of through holes, the bridging wiring portion includes a plurality of bridging wirings correspondingly formed in the plurality of through holes, and the second outer lead wiring portion includes a plurality of second outer lead wirings, any one of the second outer lead wirings being electrically connected to the corresponding first outer lead wiring through the bridging wiring.

[0016] In the display panel provided in an embodiment of the present disclosure, an orthographic projection of the through hole on the first substrate overlaps with an orthographic projection of the first outer lead wiring on the first substrate, and/or the orthographic projection of the through hole on the first substrate overlaps with an orthographic projection of the second outer lead wiring on the first substrate.

[0017] In the display panel provided in an embodiment of the present disclosure, the through hole runs through the first outer lead wiring, and/or the through hole runs through the second outer lead wiring, and allows the bridging wiring to be electrically connected to a corresponding side wall of the first outer lead wiring and/or the second outer lead wiring.

[0018] In the display panel provided in an embodiment of the present disclosure, the orthographic projections of the plurality of through holes on the first substrate are arranged in at least two columns along a direction perpendicular to an extending direction of the first outer lead wirings, and two adjacent through holes are arranged alternately along the extending direction of the first outer lead wirings.

[0019] In the display panel provided in an embodiment of the present disclosure, the through hole is provided corresponding to a center of the first outer lead wiring along the extending direction.

[0020] In the display panel provided in an embodiment of the present disclosure, a shape of the orthographic projection of the through hole on the first substrate is a circle, an ellipse, a square, or a rectangle.

[0021] In the display panel provided in an embodiment of the present disclosure, a material of the first substrate includes a polyimide material or a film material, and a material of the second substrate includes a film material.

[0022] An embodiment of the present disclosure also provides a method for preparing a display panel, including steps of: [0023] **S10**: providing a first substrate, and forming a signal wiring portion and a first outer lead wiring portion on the first substrate, the signal wiring portion being electrically connected to the first outer lead wiring portion; [0024] **S20**: providing a second substrate, and forming a second outer lead wiring portion on the second substrate; [0025] **S30**: aligning and attaching a side surface of the second substrate facing away from the second outer lead wiring portion to a side surface of the first substrate facing away from the signal wiring portion and the first outer lead wiring portion; [0026] **S40**: forming a through-hole portion running through the first substrate and the second substrate; and [0027] **S50**: forming a bridging wiring portion in the through-hole portion, the bridging wiring portion being electrically connected to the first outer lead wiring portion and the second outer lead wiring portion respectively.

[0028] In the method for preparing a display panel provided in an embodiment of the present disclosure, the second outer lead wiring portion is formed on an inner side of the first outer lead wiring portion close to the display region.

[0029] In the method for preparing a display panel provided in an embodiment of the present

disclosure, the signal wiring portion includes a plurality of signal wirings, the first outer lead wiring portion includes a plurality of first outer lead wirings electrically connected to the plurality of signal wirings one by one, the through-hole portion includes a plurality of through holes, the bridging wiring portion includes a plurality of bridging wirings correspondingly formed in the plurality of through-holes, and the second outer lead wiring portion includes a plurality of second outer lead wirings, any one of the second outer lead wirings being electrically connected to the corresponding first outer lead wiring through the bridging wiring.

[0030] In the method for preparing a display panel provided in an embodiment of the present disclosure, an orthographic projection of the through hole on the first substrate overlaps with an orthographic projection of the first outer lead wiring on the first substrate, and/or the orthographic projection of the through hole on the first substrate overlaps with an orthographic projection of the second outer lead wiring on the first substrate.

[0031] In the method for preparing a display panel provided in an embodiment of the present disclosure, the through hole runs through the first outer lead wiring, and/or the through hole runs through the second outer lead wiring, and allows the bridging wiring to be electrically connected to a corresponding side wall of the first outer lead wiring and/or the second outer lead wiring.

[0032] In the method for preparing a display panel provided in an embodiment of the present disclosure, the orthographic projections of the plurality of through holes on the first substrate are arranged in at least two columns along a direction perpendicular to an extending direction of the first outer lead wirings, and two adjacent through holes are arranged alternately along the extending direction of the first outer lead wirings.

[0033] In the method for preparing a display panel provided in an embodiment of the present disclosure, the through hole is provided corresponding to a center of the first outer lead wiring along the extending direction.

[0034] In the method for preparing a display panel provided in an embodiment of the present disclosure, a shape of the orthographic projection of the through hole on the first substrate is a circle, an ellipse, a square, or a rectangle.

[0035] In the method for preparing a display panel provided in an embodiment of the present disclosure, a material of the first substrate includes a polyimide material or a film material, and a material of the second substrate includes a film material.

[0036] Compared with the prior art, the present disclosure provides a display panel and a method for preparing a display panel. In the display panel, an outer lead bonding (OLB) region for bonding a chip on film (COF) and a printed circuit board (PCB) is divided into two parts. One part is a first outer lead wiring portion disposed on a side surface of a first substrate facing a light emitting direction, and the other part is a second outer lead wiring portion disposed on a side surface of a second substrate facing away from the first substrate. In addition, a through hole is provided in the first substrate and the second substrate, and a bridging wiring portion is disposed in the through hole to electrically connect the first outer lead wiring portion and the second outer lead wiring portion, wherein the second outer lead wiring is used for bonding, while the first outer lead wiring only serves as a signal transmission function and is not used for bonding, making a designed width thereof narrower, thereby effectively narrowing the width of a frame of the display panel, and optimizing the display effect.

Description

BRIEF DESCRIPTION OF DRAWINGS

[0037] To describe the technical solutions in the embodiments of the present disclosure more clearly, the following briefly describes accompanying drawings required for describing the embodiments. Apparently, the accompanying drawings in the following descriptions show merely

some embodiments of the present disclosure, and a person skilled in the art may still derive other accompanying drawings from these accompanying drawings without creative efforts.

[0038] FIG. 1 is a schematic structural cross-sectional view of a display panel according to an embodiment of the present disclosure.

[0039] FIG. 2 is a schematic structural top view of a first outer lead wiring portion, a second outer lead wiring portion, and a through-hole portion in a display panel according to an embodiment of the present disclosure.

[0040] FIG. 3 is a schematic structural top view of a first outer lead wiring, a second outer lead wiring, and a through hole in a display panel according to an embodiment of the present disclosure.

[0041] FIG. 4 is a schematic structural top view of a first outer lead wiring, a second outer lead wiring, and a through hole in another display panel according to an embodiment of the present disclosure.

[0042] FIG. 5 is a schematic structural top view of a first outer lead wiring, a second outer lead wiring, and a through hole in another display panel according to an embodiment of the present disclosure.

[0043] FIG. 6 is a schematic structural top view of a first outer lead wiring, a second outer lead wiring, and a through hole in another display panel according to an embodiment of the present disclosure.

[0044] FIG. 7 is a schematic structural cross-sectional view of another display panel according to an embodiment of the present disclosure.

[0045] FIG. 8 is a schematic structural cross-sectional view of another display panel according to an embodiment of the present disclosure.

[0046] FIG. 9 is a schematic structural cross-sectional view of another display panel according to an embodiment of the present disclosure.

[0047] FIG. 10 is a schematic structural top view of a first outer lead wiring portion and a through-hole portion in a display panel according to an embodiment of the present disclosure.

[0048] FIG. 11 is a schematic structural top view of a first outer lead wiring portion and a through-hole portion in another display panel according to an embodiment of the present disclosure.

[0049] FIG. 12 is a schematic flowchart of a method for preparing a display panel in a text form according to an embodiment of the present disclosure.

[0050] FIGS. 13a to 13e are schematic flowcharts of a structure of a method for preparing a display panel according to an embodiment of the present disclosure.

DETAILED DESCRIPTION OF PREFERRED EMBODIMENTS

[0051] The technical solutions in the embodiments of the present disclosure are clearly and completely described below with reference to the accompanying drawings in the embodiments of the present disclosure. Apparently, the described embodiments are merely some rather than all of the embodiments of the present disclosure. All other embodiments obtained by a person skilled in the art based on the embodiments of the present disclosure without creative efforts shall fall within the protection scope of the present disclosure.

[0052] In the description of the present disclosure, it should be understood that orientation or position relationships indicated by the terms such as “center”, “longitudinal”, “lateral”, “length”, “width”, “thickness”, “above”, “below”, “front”, “rear”, “left”, “right”, “vertical”, “horizontal”, “top”, “bottom”, “inside”, and “outside” are based on orientation or position relationships shown in the accompanying drawings, and are used only for ease and brevity of illustration and description, rather than indicating or implying that the mentioned apparatus or component needs to have a particular orientation or needs to be constructed and operated in a particular orientation. Therefore, such terms should not be construed as limiting of the present disclosure. In addition, the terms “first” and “second” are used for descriptive purposes only and are not to be construed as indicating or implying relative importance or implicitly indicating the number of technical features indicated. Thus, features defined by “first” and “second” may explicitly or implicitly include one or more of

the features. In the descriptions of the present disclosure, “a plurality of” means two or more, unless otherwise definitely and specifically limited.

[0053] In the present disclosure, the word “exemplary” is used to mean “serving as an example, illustration, or description.” Any embodiment described as “exemplary” in the present disclosure is not necessarily construed as being more preferred or advantageous over other embodiments. In order to enable any person skilled in the art to implement and use the present disclosure, the following description is given herein. In the following description, the details are listed for the purpose of explanation. It should be understood that a person of ordinary skill in the art may realize that the present disclosure can also be implemented without using the specific details. In other instances, well-known structures and processes will not be described in detail, so as to avoid unnecessary details from obscuring the description of the present disclosure. Therefore, the present disclosure is not intended to be limited to the illustrated embodiments, but is consistent with the widest scope that conforms to the principles and features disclosed in the present disclosure.

[0054] An embodiment of the present disclosure provides a display panel, which will be described in detail below with reference to a schematic structural cross-sectional view of a display panel shown in FIG. 1.

[0055] In detail, the display panel includes a display region A1 and a non-display region A2 arranged outside at least one side of the display region A1, and includes a first substrate 110, a second substrate 160, a signal wiring portion 120, a first outer lead wiring portion 130, a second outer lead wiring portion 140, a through-hole portion H, and a bridging wiring portion 150.

[0056] The first substrate 110 may be a rigid glass substrate or a flexible substrate according to an actual process requirement.

[0057] The second substrate 160 is disposed on a side surface of the first substrate 110 facing away from a light emitting direction of the display panel.

[0058] The signal wiring portion 120 is disposed on a side surface of the first substrate 110 along the light emitting direction of the display panel and disposed in the display region A1.

[0059] The first outer lead wiring portion 130 is disposed on the side surface of the first substrate 110 along the light emitting direction of the display panel, is connected to, in particularly electrically connected to the signal wiring electrical portion 120, and is disposed in the non-display region A2.

[0060] The second outer lead wiring portion 140 is disposed on a side surface of the second substrate 160 facing away from the first substrate 110, and is configured to bond external driving components, such as a chip on film (COF), a printed circuit board (PCB), thereby driving the display panel.

[0061] The through-hole portion H runs through the first substrate 110 and the second substrate 160.

[0062] The bridging wiring portion 150 is filled in the through-hole portion H, and is electrically connected to the first outer lead wiring portion 130 and the second outer lead wiring portion 140 respectively, that is, the first outer lead wiring portion 130 and the second outer lead wiring portion 140 are electrically connected through the bridging wiring portion 150.

[0063] In the display panel provided in the present embodiment, an outer lead bonding (OLB) region for bonding a COF and a PCB is divided into two parts. One part is a first outer lead wiring portion disposed on a side surface of a first substrate facing a light emitting direction, and the other part is the second outer lead wiring portion on a side of the first substrate facing away from the light emitting direction. In addition, a through hole is provided in the first substrate, and a bridging wiring portion is disposed in the through hole to electrically connect the first outer lead wiring portion and the second outer lead wiring portion, wherein the second outer lead wiring, disposed on the side surface of the first substrate facing away from the light emitting direction, is used for bonding, while the first outer lead wiring, disposed on the side surface of the first substrate facing the light emitting direction, only serves as a signal transmission function and is not used for

bonding, making a designed width of the first outer lead wiring narrower, thereby effectively narrowing the width of a frame of the display panel, and optimizing the display effect.

Furthermore, when splicing is performed using the display plane to achieve large-size display, a splice joint may be also effectively narrowed, thereby avoiding a problem of affecting the display effect due to an excessively wide splice joint.

[0064] Preferably, the first substrate **110** according to an embodiment of the present disclosure is a flexible substrate, a material of the first substrate **110** includes a polyimide material or a film material, and a material of the second substrate **160** includes a film material.

[0065] In an embodiment of the present disclosure, only a conductive component (the second outer lead wiring portion **140**) is disposed on the second substrate **160**. Therefore, the requirement for heat resistance is not high, and film materials may be used to reduce the cost. The side surface of the first substrate **110** facing away from the signal wiring portion **120** and the first outer lead wiring portion **130** is aligned and attached to the side surface of the second substrate **160** facing away from the second outer lead wiring portion **140**.

[0066] Referring to FIG. 1, in some embodiments, in order to achieve the narrowest frame of the display panel as much as possible, the second outer lead wiring portion **140** is disposed closer to an inner side of the display region A1 than the first outer lead wiring portion **130**. Correspondingly, external bonding components (such as the COF and the PCB) are also bonded to one end of the second outer lead wiring portion **140** close to the display region A1, that is, the second outer lead wiring portion **140** and the bonding components are more disposed corresponding to the display region A1 of the display panel, and do not cause a frame of the display panel to be widened.

[0067] In some embodiments, referring to a schematic diagram of a plane structure of the display panel shown in FIG. 1 and FIG. 2, in a detailed structure of the display panel, the display panel further includes a plurality of light emitting diode (LED) chips (not shown in the figure) disposed on the side surface of the first substrate **110** along the light emitting direction of the display panel and located in the display region A1. The signal wiring portion **120** includes a plurality of signal wirings **121** connected to the plurality of LED chips, the first outer lead wiring portion **130** includes a plurality of first outer lead wiring **131** electrically connected to the plurality of signal wirings **121** one by one, and each of the LED chips is electrically connected to a corresponding first outer lead wiring **131** through a corresponding signal wiring **121**. Furthermore, the through-hole portion H includes a plurality of through holes H1 that are provided corresponding to the plurality of first outer lead wirings **131**, the bridging wiring portion **150** includes a plurality of bridging wirings **151** that are correspondingly formed in the plurality of through-holes H1, the second outer lead wiring portion **140** includes a plurality of second outer lead wirings **141**, and any one of the second outer lead wirings **141** is electrically connected to the corresponding first outer lead wiring **131** through a bridging wiring **151**, that is, each of the LED chips is electrically connected to the corresponding second outer lead wiring **141** through the corresponding first outer lead wiring **131** and the bridging wiring **151**. In addition, the second outer lead wiring portion **140** is also required to be bonded with a COF and a PCB (not shown in the figure), to transmit an electrical signal from the second outer lead wiring **141** to the corresponding LED chip, so that each LED chip emits light.

[0068] Furthermore, referring to FIG. 3 to FIG. 6, an orthographic projection of the through hole H1 on the first substrate overlaps with an orthographic projection of the first outer lead wiring **131** on the first substrate, and/or the orthographic projection of the through hole H1 on the first substrate overlaps with an orthographic projection of the second outer lead wiring **141** on the first substrate, that is, relative positions of the first outer lead wiring **131**, the through hole H1, and the second outer lead wiring **141** may be disposed differently according to an actual process. Several detailed structures are shown below for further explanation:

[0069] In an implementation, the orthographic projection of the through hole H1 on the first substrate may alternatively overlap with both the orthographic projection of the first outer lead

wiring **131** on the first substrate and the orthographic projection of the second outer lead wiring **141** on the first substrate. For example, as shown in FIG. **3**, the orthographic projection of the through hole **H1** on the first substrate completely falls into the orthographic projection of the first outer lead wiring **131** on the first substrate and the orthographic projection of the second outer lead wiring **141** on the first substrate; or as shown in FIG. **4**, the orthographic projection of the through hole **H1** on the first substrate partially falls into the orthographic projection of the first outer lead wiring **131** on the first substrate and the orthographic projection of the second outer lead wiring **141** on the first substrate, and the orthographic projection of the first outer lead wiring **131** on the first substrate overlaps with the orthographic projection of the second outer lead wiring **141** on the first substrate; or as shown in FIG. **5**, the orthographic projection of the through hole **H1** on the first substrate partially falls into the orthographic projection of the first outer lead wiring **131** on the first substrate and the orthographic projection of the second outer lead wiring **141** on the first substrate, and the orthographic projection of the first outer lead wiring **131** on the first substrate does not overlap with the orthographic projection of the second outer lead wiring **141** on the first substrate.

[0070] In an implementation, the orthographic projection of the through hole **H1** on the first substrate may alternatively overlap with only the orthographic projection of the first outer lead wiring **131** on the first substrate, but does not overlap with the orthographic projection of the second outer lead wiring **141** on the first substrate; or the orthographic projection of the through hole **H1** on the first substrate may alternatively not overlap with only the orthographic projection of the first outer lead wiring **131** on the first substrate, but overlap with the orthographic projection of the second outer lead wiring **141** on the first substrate. Accompanying drawings are not given here for detailed description, which should be easily understood by a person skilled in the art.

[0071] Definitely, the orthographic projection of the through hole **H1** on the first substrate may alternatively overlap with neither the orthographic projection of the first outer lead wiring **131** on the first substrate nor the orthographic projection of the second outer lead wiring **141** on the first substrate, as shown in FIG. **6**.

[0072] In some embodiments, when the orthographic projection of the through hole **H1** on the first substrate overlaps with the orthographic projection of the first outer lead wiring **131** on the first substrate, and/or the orthographic projection of the through-hole **H1** on the first substrate overlaps with the orthographic projection of the second outer lead wiring **141** on the first substrate, and when the orthographic projection of the through hole **H1** on the first substrate overlaps with the orthographic projection of the first outer lead wiring **131** on the first substrate, the through hole **H1** runs through the first outer lead wiring **131** while running through the first substrate, so as to increase the reliability of a connection between the bridging wiring **151** in the through hole **H1** and the first outer lead wiring **131** as well as the second outer lead wiring **141**; and when the orthographic projection of the through hole **H1** on the first substrate overlaps with the orthographic projection of the second outer lead wiring **141** on the first substrate, the through hole **H1** runs through the second outer lead wiring **141** while running through the first substrate, and the bridging wiring **151** disposed in the through hole **H1** is electrically connected to a corresponding side wall of the first outer lead wiring **131** and/or the second outer lead wiring **141**, to increase a contact area between the bridging wiring **151** and the first outer lead wiring **131** and/or the second outer lead wiring **141**, and improve the reliability of the connection.

[0073] In detail, in an embodiment, when the through hole **H1** runs through the first outer lead wiring **131** and the second outer lead wiring **141**, and allows the bridging wiring **151** to be electrically connected to corresponding side walls of the first outer lead wiring **131** and the second outer lead wiring **141**, a structure is formed shown in FIG. **7**.

[0074] In an embodiment, when the through hole **H1** runs through the first outer lead wiring **131** and allows the bridging wiring **151** to be electrically connected to the corresponding first outer lead wiring **131**, a structure is formed shown in FIG. **8**.

[0075] In an embodiment, when the through hole H1 runs through the second outer lead wiring **141** and allows the bridging wiring **151** to be electrically connected to the corresponding second outer lead wiring **141**, a structure is formed shown in FIG. **9**.

[0076] In some embodiments, referring to FIG. **10**, orthographic projections of the plurality of through holes H1 on the first substrate are arranged in at least two columns along a direction Y perpendicular to an extending direction X of the first outer lead wirings **131**, and two adjacent through holes H1 are arranged alternately along a length direction X of the first outer lead wirings, to reduce the arrangement density of the through holes H1, thereby avoiding the occurrence of a short circuit due to interference between the adjacent through holes H1.

[0077] In some embodiments, in an actual preparation process, considering the limited precision of a hole-opening process, particular buffer regions are usually required to be reserved at two ends of a preset region of the through hole H1 region. Therefore, the through hole H1 is provided corresponding to a center of the first outer lead wiring **131** along the extending direction X, as shown in FIG. **11**, to minimize a length of the first outer lead wiring **131**, that is, further compress the width of a frame of a display panel. Generally, the length of the first outer lead wiring may be set to be less than 30 microns.

[0078] In some embodiments, a shape of the orthographic projection of the through hole on the first substrate is a circle, an ellipse, a square, or a rectangle.

[0079] It should be noted that, only the above structures are described in the above embodiments of the display panel. It may be understood that, in addition to the above structures, the display panel according to the embodiments of the present disclosure may also include any other necessary structures as required, which is not specifically limited here.

[0080] In addition, the type of the display panel is not particularly limited, and may be a micro LED display panel, a mini LED display panel, an organic light emitting diode (OLED) display panel, a liquid crystal display (LCD) panel, or the like.

[0081] An embodiment of the present disclosure also provides a method for preparing a display panel, which will be described in detail with reference to FIG. **12** and FIGS. **13a** to **13e** as follows.

[0082] In detail, the method includes the steps of: [0083] **S10**: providing a first substrate **110**, and forming a signal wiring portion **120** and a first outer lead wiring portion **130** on the first substrate **110**, the signal wiring portion **120** being electrically connected to the first outer lead wiring portion **130**, the signal wiring portion **120** being correspondingly formed in a display region A1, and the first outer lead wiring portion **130** being correspondingly formed in a non-display region A2, so as to form a structure shown in FIG. **13a**; [0084] **S20**: providing a second substrate **160**, and forming a second outer lead wiring portion **140** on the second substrate **160**, so as to form a structure shown in FIG. **13b**; [0085] **S30**: aligning and attaching a side surface of the second substrate **160** facing away from the second outer lead wiring portion **140** to a side surface of the first substrate **110** facing away from the signal wiring portion **120** and the first outer lead wiring portion **130**, so as to form a structure shown in FIG. **13c**; [0086] **S40**: forming a through-hole portion H running through the first substrate **110** and the second substrate **160**, so as to form a structure shown in FIG. **13d**, wherein the through-hole portion H is usually formed by using a laser opening process; and [0087] **S50**: forming a bridging wiring portion **150** in the through-hole portion H, the bridging wiring portion **150** being electrically connected to the first outer lead wiring portion **130** and the second outer lead wiring portion **140** respectively, so as to form a structure shown in FIG. **13e**, wherein the bridging wiring portion **150** is formed by the steps below in detail: injecting silver paste into the through-hole portion H and curing to form the bridging wiring portion **150**, and causing the bridging wiring portion **150** to be in an optimal form by controlling the viscosity of the silver paste. [0088] Only the second outer lead wiring portion **140** is required to be formed on the second substrate **160**, and no high-temperature manufacturing process is required. Therefore, the second substrate **160** may be a lower-cost and non-high-temperature-resistant substrate, for example, a film substrate commonly used in the art.

[0089] In the above embodiments, the description of each embodiment has respective focuses. For a part that is not described in detail in an embodiment, refer to a detailed description of other embodiments above. Details are not described herein again.

[0090] A display panel and a method for preparing a display panel according to the embodiments of the present disclosure are described above in detail. Although the principles and implementations of the present disclosure are described by using specific examples in this specification, the descriptions of the above embodiments are merely intended to help understand the method and the core idea of the method of the present disclosure. Meanwhile, a person skilled in the art may make modifications to the specific implementations and application range according to the idea of the present disclosure. In conclusion, the content of this specification is not construed as a limit on the present disclosure.

Claims

1. A method for preparing a display panel, comprising steps of: **S10**: providing a first substrate, and forming a signal wiring portion and a first outer lead wiring portion on the first substrate, the signal wiring portion being electrically connected to the first outer lead wiring portion; **S20**: providing a second substrate, and forming a second outer lead wiring portion on the second substrate; **S30**: aligning and attaching a side surface of the second substrate facing away from the second outer lead wiring portion to a side surface of the first substrate facing away from the signal wiring portion and the first outer lead wiring portion; **S40**: forming a through-hole portion running through the first substrate and the second substrate; and **S50**: forming a bridging wiring portion in the through-hole portion, the bridging wiring portion being electrically connected to the first outer lead wiring portion and the second outer lead wiring portion respectively.
2. The method for preparing a display panel as claimed in claim 1, wherein the second outer lead wiring portion is formed on an inner side of the first outer lead wiring portion close to the display region.
3. The method for preparing a display panel as claimed in claim 1, wherein the signal wiring portion comprises a plurality of signal wirings, the first outer lead wiring portion comprises a plurality of first outer lead wirings electrically connected to the plurality of signal wirings one by one, the through-hole portion comprises a plurality of through holes, the bridging wiring portion comprises a plurality of bridging wirings correspondingly formed in the plurality of through holes, and the second outer lead wiring portion comprises a plurality of second outer lead wirings, any one of the second outer lead wirings being electrically connected to the corresponding first outer lead wiring through the bridging wiring.
4. The method for preparing a display panel as claimed in claim 1, wherein an orthographic projection of the through hole on the first substrate overlaps with an orthographic projection of the first outer lead wiring on the first substrate, and/or the orthographic projection of the through hole on the first substrate overlaps with an orthographic projection of the second outer lead wiring on the first substrate.
5. The method for preparing a display panel as claimed in claim 1, wherein the through hole runs through the first outer lead wiring, and/or the through hole runs through the second outer lead wiring, and allows the bridging wiring to be electrically connected to a corresponding side wall of the first outer lead wiring and/or the second outer lead wiring.
6. The method for preparing a display panel as claimed in claim 3, wherein orthographic projections of the plurality of through holes on the first substrate are arranged in at least two columns along a direction perpendicular to an extending direction of the first outer lead wirings, and two adjacent through holes are arranged alternately along the extending direction of the first outer lead wirings.
7. The method for preparing a display panel as claimed in claim 3, wherein the through hole is

provided corresponding to a center of the first outer lead wiring along an extending direction.

8. The method for preparing a display panel as claimed in claim 3, wherein a shape of an orthographic projection of the through hole on the first substrate is a circle.

9. The method for preparing a display panel as claimed in claim 3, wherein a shape of an orthographic projection of the through hole on the first substrate is an ellipse.

10. The method for preparing a display panel as claimed in claim 3, wherein a shape of an orthographic projection of the through hole on the first substrate is a square.

11. The method for preparing a display panel as claimed in claim 3, wherein a shape of an orthographic projection of the through hole on the first substrate is a rectangle.

12. The method for preparing a display panel as claimed in claim 1, wherein a material of the first substrate comprises a polyimide material, and a material of the second substrate comprises a film material.

13. The method for preparing a display panel as claimed in claim 1, wherein a material of the first substrate comprises a film material, and a material of the second substrate comprises a film material.

14. The method for preparing a display panel as claimed in claim 1, wherein the second substrate is in direct contact with the side surface of the first substrate facing backward from the light emitting direction of the display panel.

15. The method for preparing a display panel as claimed in claim 1, wherein the bridging wiring portion is formed by injecting silver paste into the through-hole portion and curing the injected silver paste to form the bridging wiring portion.

16. The method for preparing a display panel as claimed in claim 1, wherein a viscosity of the silver paste is controlled when the silver paste is injected.
