Cpr E 281 LAB1
ELECTRICAL AND COMPUTER ENGINEERING
IOWA STATE UNIVERSITY

Lab 1 Answer Sheet

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PRELAB:

Q1. Fill in the Truth Table below for an AND gate:

A	B	C
0	0	0
0	1	0
1	0	0
1	1	1

Q2. What does the .bdf file extension stand for?

Block Design Format

LAB:
2.0 Fill in the Truth Table for *lab1step1*:

A	B	C
0	0	0
0	1	0
1	0	0
1	1	1

Logic Expression: AB = C

4.0 Fill in the Truth Table for *lab1step2*:

W	X	Y	Z
0	0	0	0
0	0	1	0
0	1	0	1
0	1	1	0
1	0	0	1
1	0	1	0
1	1	0	0
1	1	1	1

Logic Expression: !wx!y+w!x!y+wxy = z

4.0 Fill in the Truth Table for *lab1step3*:

A	B	C	F
0	0	0	0
0	0	1	1
0	1	0	0
0	1	1	1
1	0	0	0
1	0	1	1
1	1	0	1
1	1	1	0

Logic Expression: !A!BC + !ABC + A!BC + AB!C = F