
Embedded Systems International

Lab 3 Prelab

Name: Riley Lawson

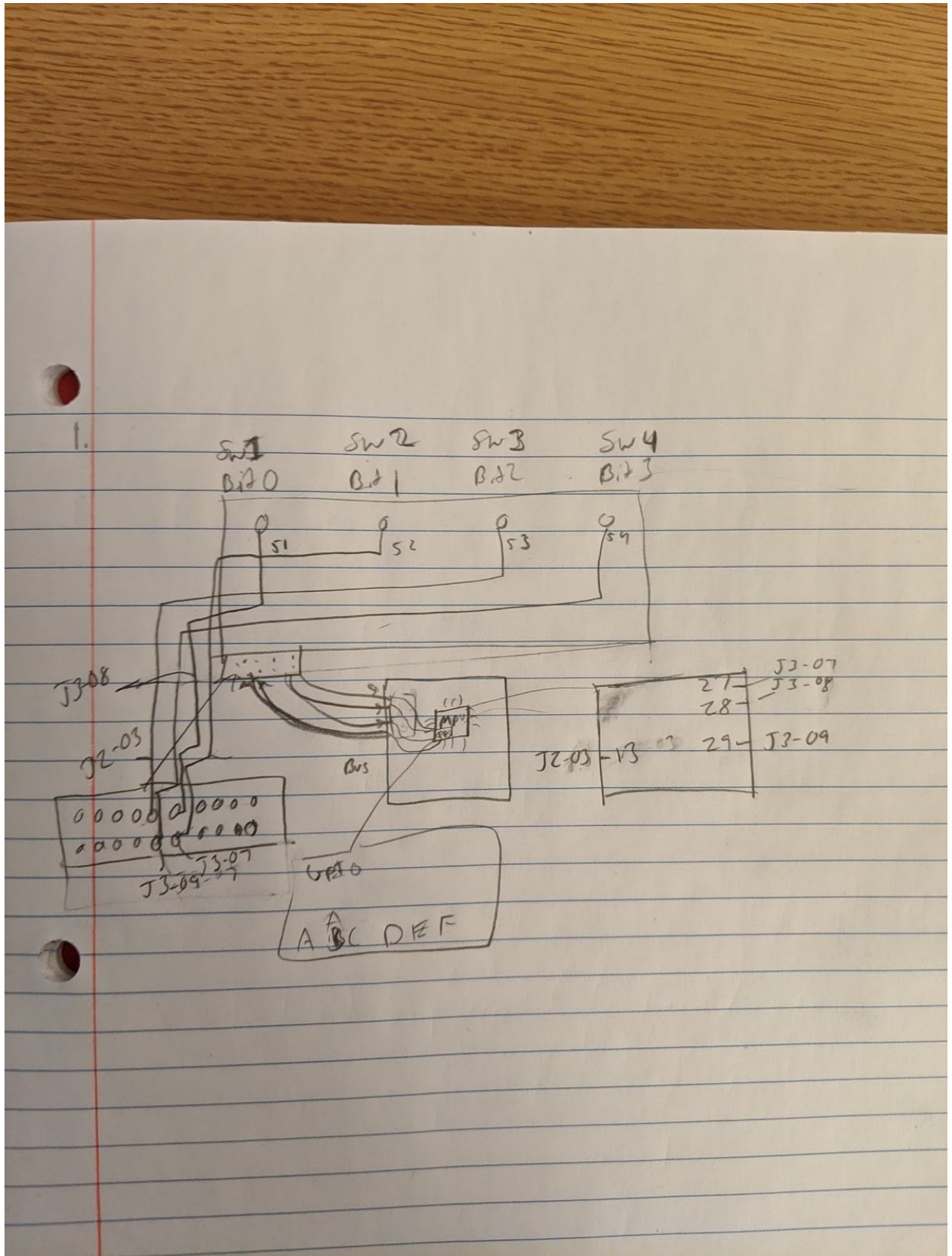
Lab Partner Name (if you worked together and are submitting the same document or mostly the same answers):

Lab Section:9

Submit your prelab document as a PDF file in Canvas under the corresponding prelab assignment. Every student submits their own prelab. Lab partners are allowed to work on the prelab together and submit the same document (if there is actual collaboration on the document). For full credit, the prelab must be submitted prior to the start of lab. Text responses should be typed or printed neatly. You can draw a sketch by hand, or you can use a drawing tool. Try to have started a rough draft of the prelab when you come to class on Tuesday.

1. System sketch

Similar to the system sketch you did in Lab 1, sketch a diagram that shows how the four push buttons connect to the microcontroller. Your diagram should show the port and the pin (or bit) number of the port that each button is connected to. Give the port name and pin numbers used in the microcontroller. The microcontroller package pin numbers (1-64) are different than the port pin names (e.g, Port A pin 0, PA0). Use the following resources as needed: (1) in-class notes (**see [mtg06-notes-concepts-GPIO.pdf](#)**), (2) CyBot board schematics and GPIO pin usage, (3) button.c code, and (4) Tiva datasheet. As part of your sketch, indicate the port and bit of the port that each button is connected to.



2. Data register for a GPIO port

The GPIO port connected to the push buttons is associated with a specific GPIO data register. Answer these questions for the actual port used with the push buttons.

- a) What is the specific name of the data register as given in the Tiva TM4C Datasheet (shown in all capital letters)?
- a. GPIODATA

Hint: see the List of Registers in the Table of Contents, or open the Table of Contents in the sidebar. Excerpts shown below.

General-Purpose Input/Outputs (GPIOs)	649
Register 1: GPIO Data (GPIODATA), offset 0x000	662
Register 2: GPIO Direction (GPIODIR), offset 0x400	663
Register 3: GPIO Interrupt Sense (GPIOIS), offset 0x404	664

▼ 10. General-Purpose Input/Outputs (GPIOs)
10.1. Signal Description
▶ 10.2. Functional Description
10.3. Initialization and Configuration
10.4. Register Map
▼ 10.5. Register Descriptions
Register 1: GPIO Data (GPIODATA), offset 0x000
Register 2: GPIO Direction (GPIODIR), offset 0x400
Register 3: GPIO Interrupt Sense (GPIOIS), offset 0x404

- b) What is the specific name of the data register as given by the #define macro in the header file, tm4c123gh6pm.h (TM4C123GH6PM Register Definitions)?

- a. GPIO_PORTA_DATA_R (*((volatile uint32_t*) 0x40004000))

Hint: search for “GPIO registers” in the file, ignore the AHB version (if you find it), ignore the “DATA_BITS_R” name, the 32-bit memory address should start with “0x4...”

c) What is the 32-bit memory address of the data register (in hex)?

- 0x0000 0000
- or 0x400F E000