$\underbrace{0\ 0\ 0\ 1}_{v}\ \underbrace{0\ 0\ 0\ 1}_{1}$ 0×11

ICE 3

Digital Input

6.1 DIR = Bit 0

PUR = 0×0

PCTL = bit 1

AFSEL = Set 6.2 1

AMSEL = Clear 0

DEN = Set 6.2 1

Digital output

6.2 DIR = bit 1

PUR = 0

PCTL = 0×0

AFSEL = 0×0

AMSEL = 6.2 1

DEN = bit 1

6.3 GPIO_PORTB_DATA_R |= 0×04

6.4 GPIO_PORTB_DATA_R &= 0×04

6.5 Oscilloscope

6.6 Logic Analyzer