

Memory access	Direct mapped	4-way set associative
0x1001FA0	miss (invalid)	miss (4x invalid)
0x1001FA4	miss (tag mismatch)	miss (tag mismatch) 3x invalid
0x1001FA8	miss (tag mismatch)	Hit (way 0)
0x1001FA0	Miss (tag mismatch)	miss (2x tag mismatch, 2x invalid)
0x1001FAB0	miss (invalid)	miss (4x invalid)
0x1001FAC0	miss (invalid)	miss (4x invalid)
0x1001FA1	miss (tag mismatch)	miss (3x tag mismatch, 1x invalid)
0x1001FA2	miss (tag mismatch)	miss (4x tag mismatch)
0x1001FAF	miss (tag mismatch)	miss (4x tag mismatch)
0x1001FA2	miss (tag mismatch)	Hit (way 2)

Set case =

Direct mapped

< set A, way 0, 0x100100 >

< set B, way 0, 0x100100 >

< set C, way 0, 0x100100 >

4 way set associative:

< set A, way 0, 0x1001EF >

< set A, way 1, 0x1001EE >

< set A, way 2, 0x100100 >

< set A, way 3, 0x10011F >

< set B, way 0, 0x100100 >

< set C, way 0, 0x100100 >

2. Exam question

You have a 4-way set associative cache which has a total of 4096 bytes and a cache line of 128 bytes?

a) What is the total block size?

b) What is the number of cache lines?

c) Number of sets in cache?

d) What is the word offset (in bits)?

e) What is the set offset (in bits)?

Answer(s):

a) 128 bytes

b) $4096 / 128 = 32 \text{ lines}$

c) $\frac{32}{4} = 8 \text{ sets}$

d) $\log_2 8 = 3 \text{ bits}$

e) $\log_2 128 = 7 \text{ bits}$