

Schematics of EBS SDC logic and supervisor CPU
Formula Technion Driverless
#555

The Technion - Israel Institute of Technology

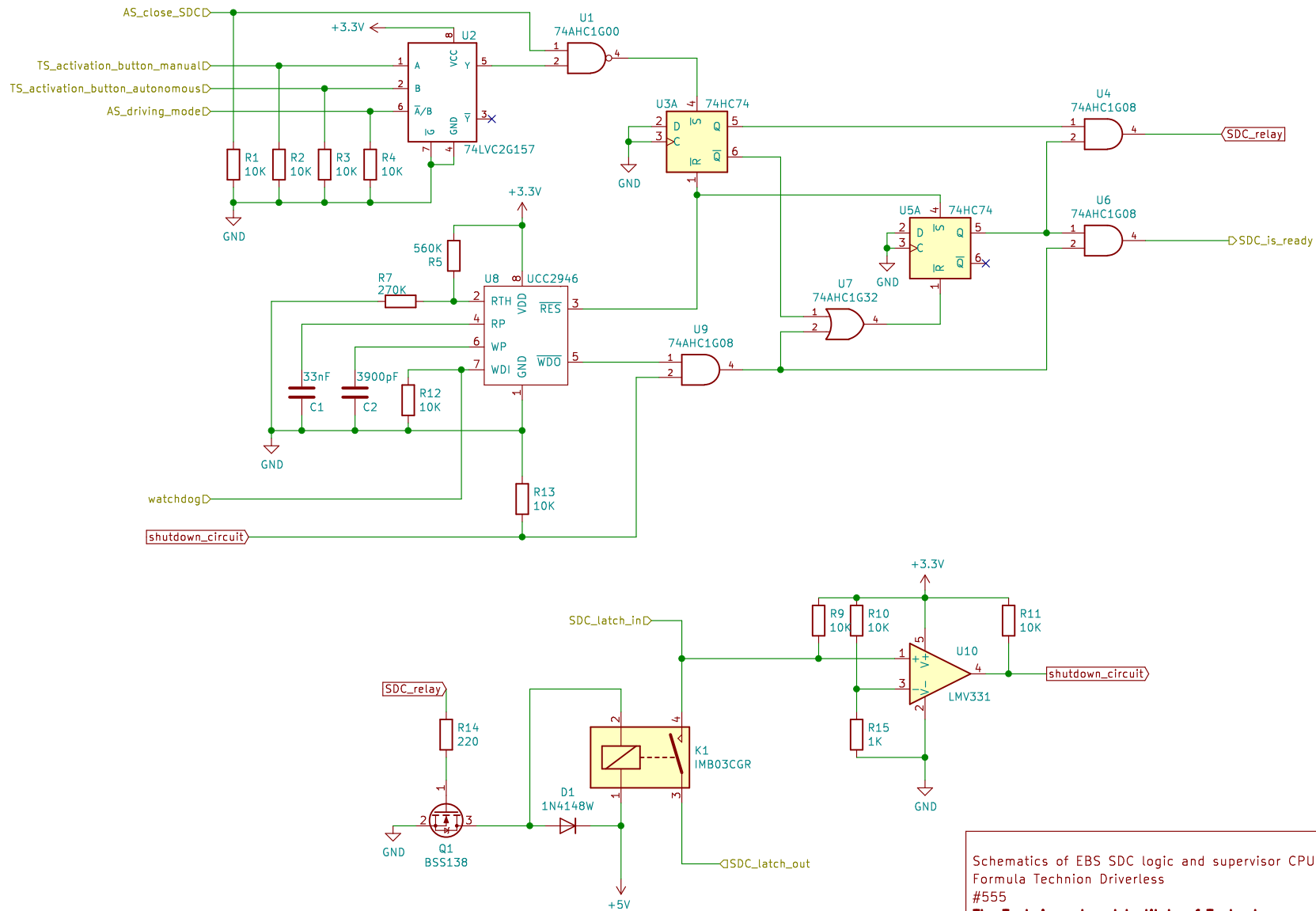
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File: EBS.sch

Title: EBS schematics 2020

Size: A4 Date: 2020-03-19
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SDC_logic



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CPU_Supervisor

U11
Teensy_3.2

USB
micro

3.6-6v
3.3v

GND

5 D5
6 D6
7 D7
8 D8
9 D9
10 D10
11 D11
12 D12

AS_close_SDC
AS_driving_mode
watchdog
SDC_is_ready
EBS_valve_1_control
EBS_valve_2_control

3 CAN_TX
4 CAN_RX

A0
A1
A2
A3
A4
A5

BP1
BP2
BP3
BP4
BP5
BP6

C3
100nF

U12

1 D
4 R
5 Vref
8 Rs

3 VCC
7 CANH
6 CANL
2 GND

SN65HVD230

R18
120

CAN_H
CAN_L

R16
220

EBS_valve_1_control

10K
R19

Q3
Si2302CDS
Q4
Si2302CDS

2
3

10K
R20

R17
220

EBS_valve_2_control

EBS_valve_1_out
EBS_valve_1_in
EBS_valves_power
EBS_valve_2_in
EBS_valve_2_out

D2
1N4148W
D3
1N4148W

1
2
3

GND

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