Power supply unit

v1.0.0

Thursday, 13 October 2016

# Revision history

|  |  |  |  |
| --- | --- | --- | --- |
| Date | Version | Description | Author |
| 2016-10-13 | 1.0 | Initial specification | Dominik Guz |
|  |  |  |  |

Table of contents

[Revision history 2](#_Toc460673410)

[Architectural Representation 5](#_Toc460673411)

[purpose 5](#_Toc460673412)

[scope 5](#_Toc460673413)

[references 5](#_Toc460673414)

[definitions 5](#_Toc460673415)

[Architectural Goals and Constraints 6](#_Toc460673416)

[Non-functional requirements 6](#_Toc460673417)

[functional requirements 6](#_Toc460673418)

[environmental requirements 6](#_Toc460673419)

[Dependency requirements 6](#_Toc460673420)

[Operating conditions 6](#_Toc460673421)

[Requirements analysis 7](#_Toc460673422)

[Circuit parameters 7](#_Toc460673423)

[Operating condtions 7](#_Toc460673424)

[Voltage ranges 7](#_Toc460673425)

[Power consumption 7](#_Toc460673426)

[Frequency ranges 7](#_Toc460673427)

[Dimensions 7](#_Toc460673428)

[Components comparision 8](#_Toc460673429)

[Manufacturers list 8](#_Toc460673430)

[Comparison table 8](#_Toc460673431)

[Component selection 9](#_Toc460673432)

[Matching components list 9](#_Toc460673433)

[Optimization goals 9](#_Toc460673434)

[Component specification 10](#_Toc460673435)

[Circuit model 11](#_Toc460673436)

[Block diagram 11](#_Toc460673437)

[Circuit drawing 12](#_Toc460673438)

[Circuit simulation results 13](#_Toc460673439)

[Implementation notes 14](#_Toc460673440)

[Issues and concerns 15](#_Toc460673441)

# Architectural Representation

## purpose

Circuit specification for RS485 transceiver.

## scope

Specification of circuit parameters along with list of used components.

Proposed circuit model and drawing of specified circuit.

## references

|  |  |
| --- | --- |
| [1] | T. Kugelstadt, The RS-485 Design Guide, Texas Instruments, 2008. |

## definitions

# Architectural Goals and Constraints

## Non-functional requirements

1. Middle sized package, TSOP preferable.
2. ESD protection standards compliant

## functional requirements

1. Transmit and receive capability on RS-485 half-duplex bus.
2. Low power consumption
3. Transmit enabling signal polarity compatible with built-in MCU UASRT modules
4. Connector for wires 24-28 AWG
5. Minimum 32 bus nodes limit

## environmental requirements

## Dependency requirements

## Operating conditions

1. Temperature ranges : -20°C -> 80°C

# Requirements analysis

## Circuit parameters

### Operating condtions

### Voltage ranges

1. Power supply 2.8 – 3.5V
2. Logic levels CMOS compatible

### Power consumption

1. <5mA Active mode consumption

### Frequency ranges

1. >100Kbps data rates

### Dimensions

1. < 2 cm2

## Components comparision

### Manufacturers list

1. Texas Instruments
2. Maxim Integrated
3. Linear Technology

### Comparison table

|  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- |
| Part Code | Manufacturer | Available | Price/1unit($) | Meets Non-Func Req | Publication Date | Extras |
| SN65HVD1781-Q1 | Texas Instruments | Y | 4.55 | Y | 2010 | 320 Nodes, 1Mbps |
| LTC1480 | Linear Technology | Y | 5.15 | Y |  | Very low power |
| MAX3483ECSA+T | Maxim Integrated | Y | 5.22 | Y |  | Slew-rate redo, EMI filter |
|  |  |  |  |  |  |  |
|  |  |  |  |  |  |  |
|  |  |  |  |  |  |  |

## Component selection

### Matching components list

1. SN65HVD1781-Q1
2. LTC1480
3. MAX3483ECSA+T

### Optimization goals

1. Price for single unit
2. Favor extra features for comparably priced units

## Component specification

# Circuit model

## Block diagram

# Circuit drawing



# Circuit simulation results

# Implementation notes

* 1. Differential pair of A/B signals between transceiver output and connector. Keep as short as possible.

# Issues and concerns