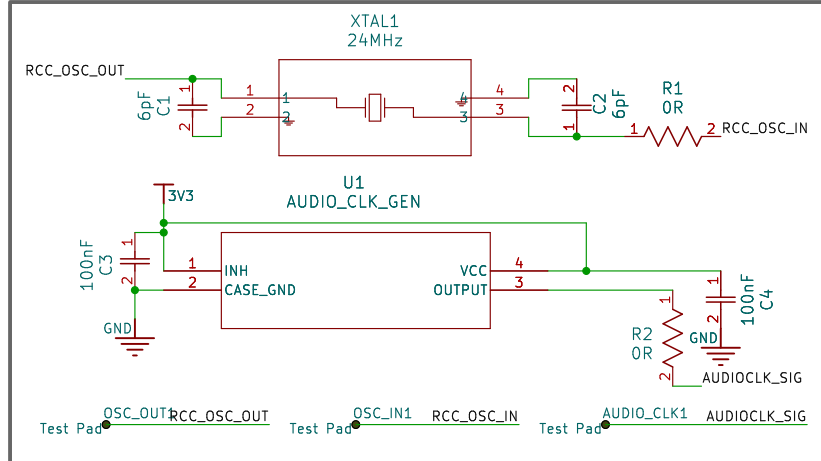
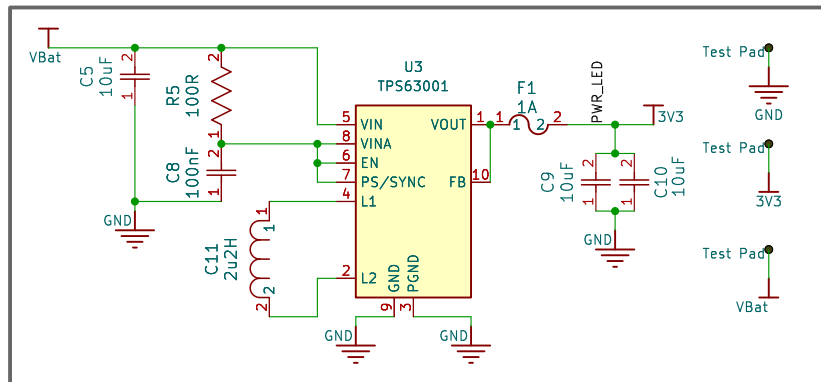


Sheet: /		
File: gap9_deck_v2.kicad_sch		
Title:		
Size: A4	Date:	Rev:
KiCad E.D.A. 9.0.5	Id: #/5	

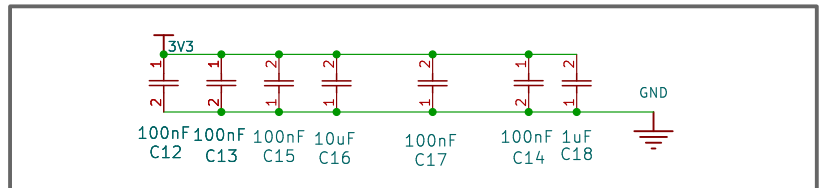
Oscillators



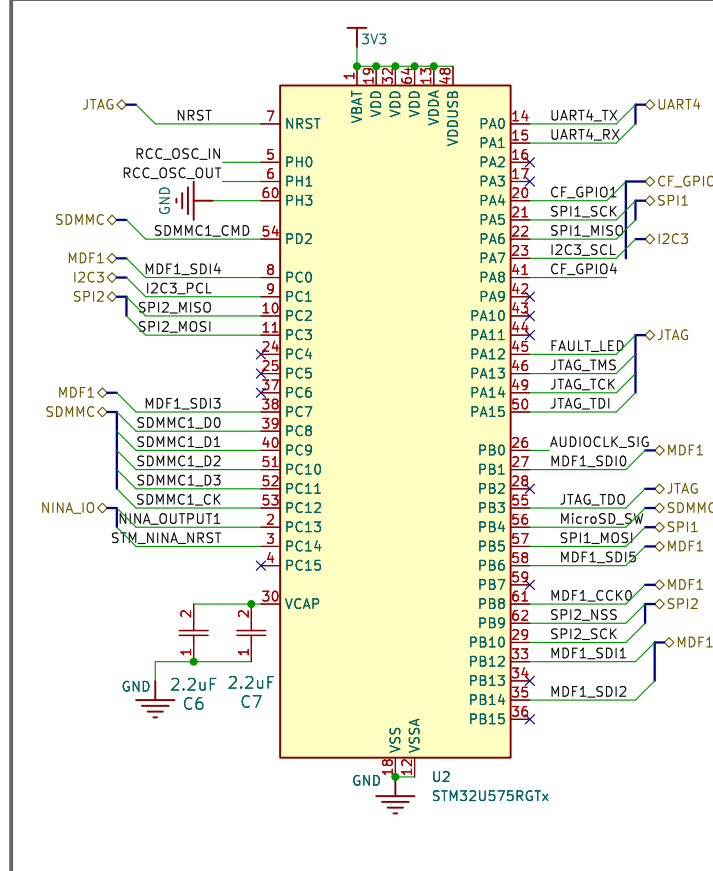
Power



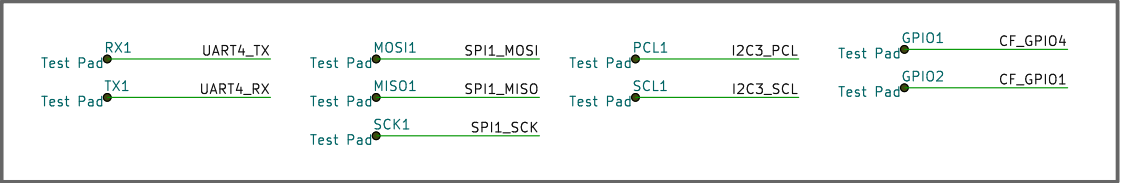
Decoupling Caps



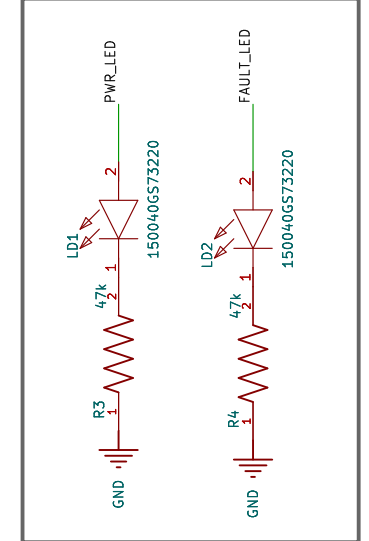
MCU



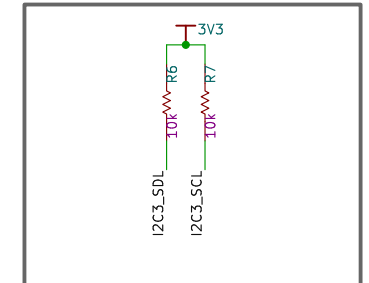
Signal Test Pads



MCU LEDs



I2C PULLUPS



Sheet: /Core/
File: Core.kicad_sch

Title:

Size: A4

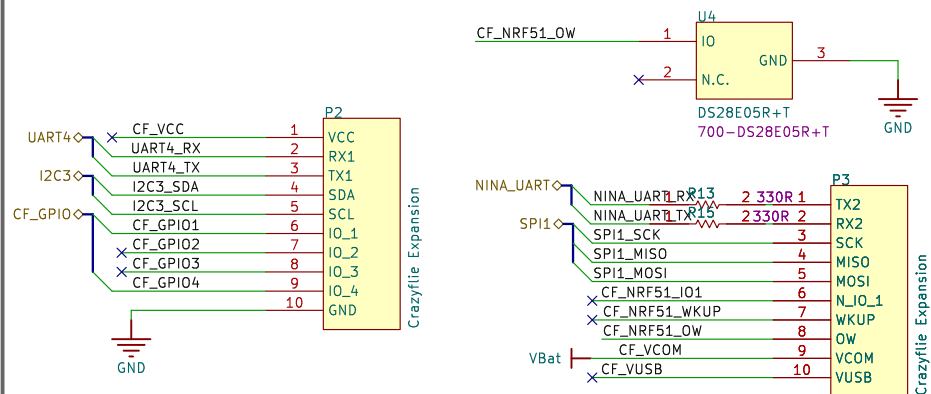
Date:

KiCad E.D.A. 9.0.5

Rev:

Id: 2/5

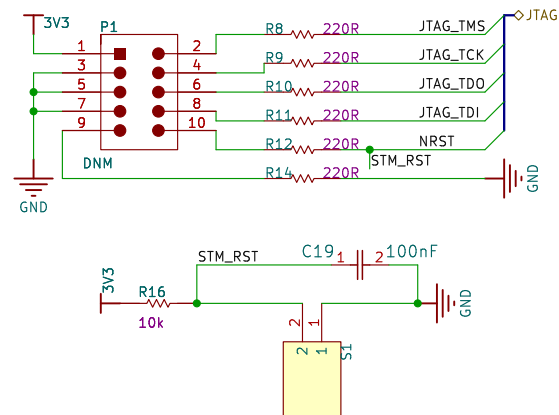
Crazyflie Expansions



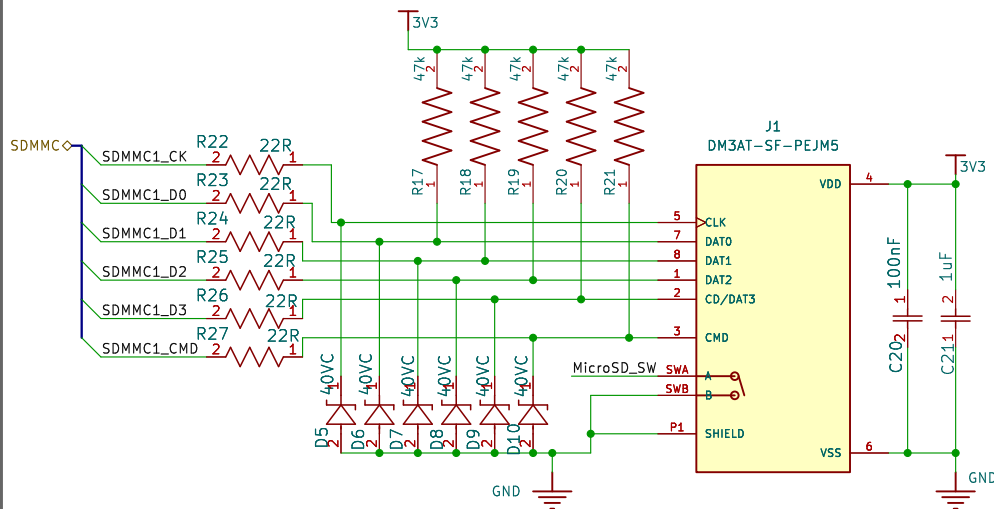
Please note the following about the Crazyflie expansion connectors:

- * VCOM is power directly from the battery/USB and is not regulated (max 1A)
- * VUSB is connected directly to the USB which means this will supply at 4.5–5.5V when the USB is connected and can be used for charging the battery when the
- * VCC is regulated to 3V0 (max. 100mA)

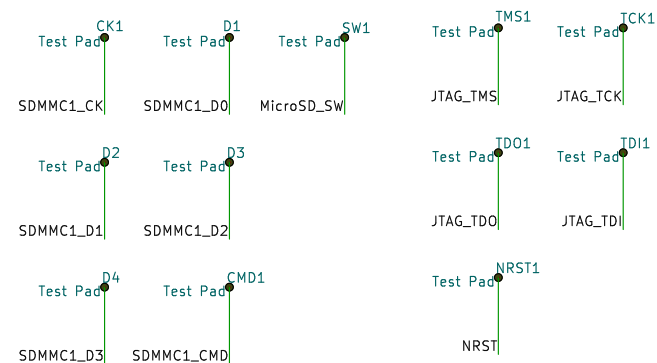
JTAG



SD Card Slot



Signal Test Pads



Signal Test Pads



Sheet: /connectors/
File: connectors.kicad_sch

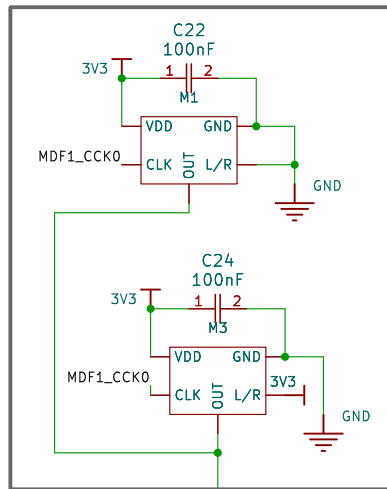
Title:

Size: A4	Date:
KiCad E.D.A. 9.0.5	

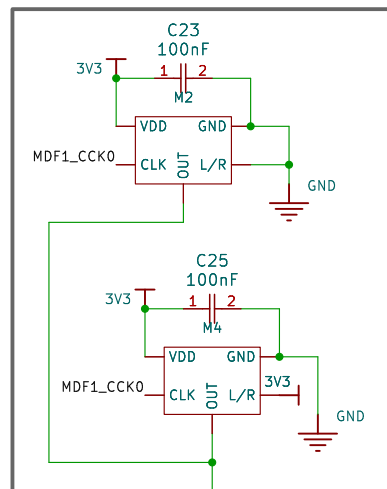
Date:

Rev:
Id: 3/5

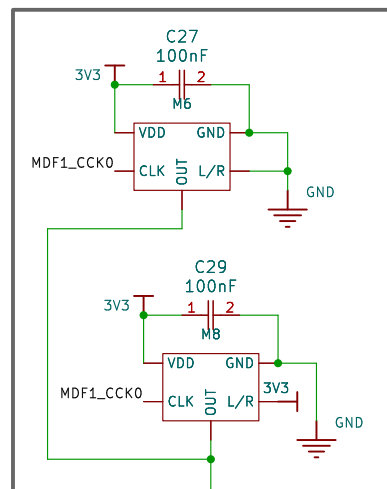
Pair 0



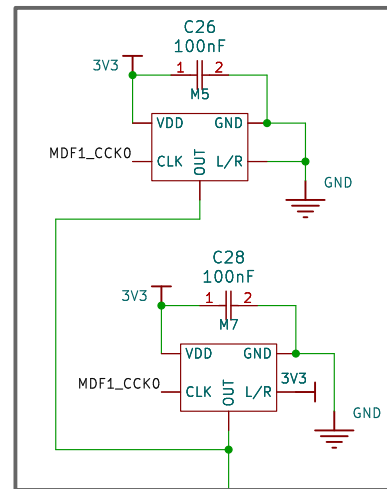
Pair 2



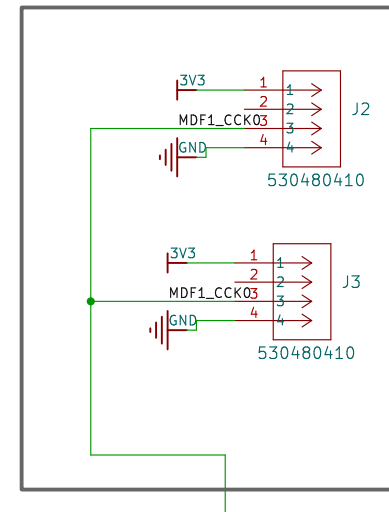
Pair 1



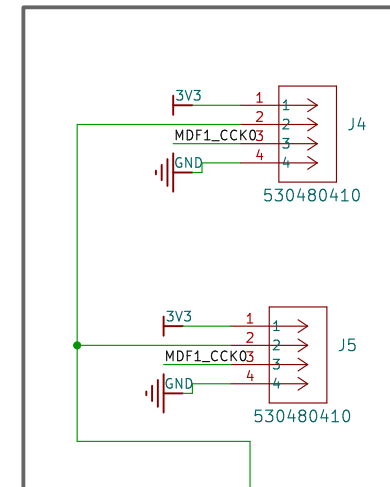
Pair 3



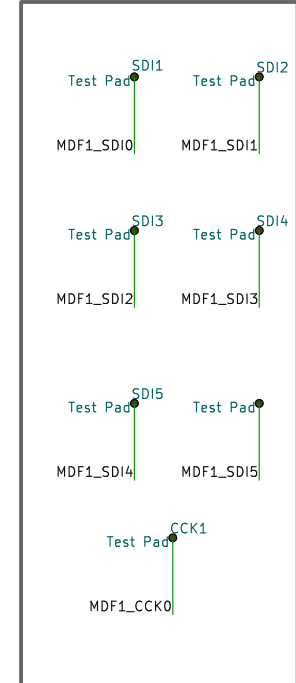
Pair 4



Pair 5



Test Pads



MDF1_SDIO
MDF1_SD12
MDF1_SD14

MDF1_CCK0

MDF1

MDF1_SD11
MDF1_SD13
MDF1_SD15

Sheet: /microphones/
File: microphones.kicad_sch

Title:

Size: A4
KiCad E.D.A. 9.0.5

Date:

Rev:
Id: 5/5

Pin Connections:

Module Pin	Signal	Target Pin	Target Signal
9	VCC_I0	1	3V3
10	VCC	1	3V3
19	RESET	1	NINA_NRST
1	GPIO_1/SPLV_MOSI	1	SPI2_MOSI
2	GPIO_2/ADC_2	1	STM_NINA_NRST
3	GPIO_3/ADC_3	1	NINA_I0
4	GPIO_4/ADC_4	1	SPI2
5	GPIO_5	1	NINA_UART
7	GPIO_7/LPO_IN	1	NINA_LED
8	GPIO_8/RMIL_TXEN/SPLV_HD	1	NINA_OUTPUT1
16	GPIO_16/RMIL_RXD0/DAC_16	1	NINA_I0
17	GPIO_17/RMIL_RXD1/DAC_17	1	SPI2
18	GPIO_18/RMIL_CRSDV	1	SPI2_NSS
20	GPIO_20/UART_RTS/RMIL_TXD1/SPLV_WP	1	SPI2_SCK
21	GPIO_21/UART_CTS/RMIL_TXD0/SPLV_MISO	1	NINA_JTAG_TMS
22	GPIO_22/UART_TXD	1	NINA_JTAG_TDO
23	GPIO_23/UART_RXD	1	NINA_JTAG_TCK
24	GPIO_24/RMIL_MDIO	1	NINA_JTAG_TDI
25	GPIO_25/RMIL_MDCLK	1	
27	GPIO_27/RMIL_CLK/SYS_BOOT	1	
28	GPIO_28/SPLV_CS	1	
29	GPIO_29/SPLV_CLK	1	
31	GPIO_31/JTAG_TMS	1	
32	GPIO_32/JTAG_TDO	1	
34	GPIO_34/ADC_34	1	
35	GPIO_35/JTAG_CLK	1	
36	GPIO_36/JTAG_TDI	1	

Offsheet Connections:

Module Pin	Signal	Target Pin	Target Signal
11	RSVD	11	RSVD
15	RSVD	15	RSVD
33	RSVD	33	RSVD
6	GND	6	GND
12	GND	12	GND
14	GND	14	GND
30	GND	30	GND
37	GND	37	GND
38	GND	38	GND
39	GND	39	GND
40	GND	40	GND
41	GND	41	GND
42	GND	42	GND
43	GND	43	GND
44	GND	44	GND
45	GND	45	GND
46	GND	46	GND
47	GND	47	GND
48	GND	48	GND

Module Information:

Module: NINA-W102-01B

Antenna: ANT 13

The diagram illustrates the connections between the Test Pads of the STM32F769I-DT microcontroller and its internal pins. The connections are as follows:

- MOSI3** is connected to **Test Pad**.
- SPD_MISO1** is connected to **Test Pad**.
- SCK3** is connected to **Test Pad**.
- NSS1** is connected to **Test Pad**.
- NRST2** is connected to **Test Pad**.
- OUT1** is connected to **Test Pad**.
- RX3** is connected to **Test Pad**.
- TX3** is connected to **Test Pad**.

On the right side, the corresponding pin names are listed:

- SPD_MISO1**
- SPD_SCK**
- SPD_NSS**
- STM_NINA_NRST**
- NINA_OUTPUT1**
- USART1_RX**
- USART1_TX**

Rev:
Id: 6/5