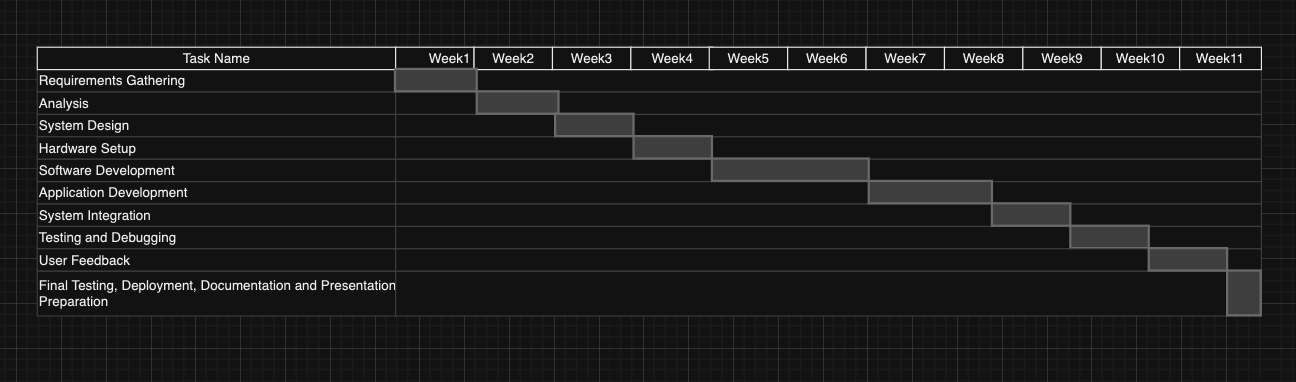
TimeLines



System design

