

Overall Statistical Feature [USA, Semiconductor]

*****situation: beat_up*****

number of beat_up is: 68

Surprise

count 68.000000
mean 0.148768
std 0.155949
min 0.050300
25% 0.066100
50% 0.108350
75% 0.163400
max 0.907000

*****situation: beat_down*****

number of beat_down is: 69

Surprise

count 69.000000
mean 0.129790
std 0.092848
min 0.051400
25% 0.071000
50% 0.100100
75% 0.158200
max 0.543300

*****situation: miss_up*****

number of miss_up is: 4

Surprise

count 4.000000
mean 4.370900
std 7.433050
min 0.011000
25% 0.011975
50% 1.022000
75% 5.380925
max 15.428600

*****situation: miss_down*****

number of miss_down is: 10

Surprise

count 10.000000
mean 0.421920
std 0.647541
min 0.016900
25% 0.023275
50% 0.100150
75% 0.550350
max 2.076900