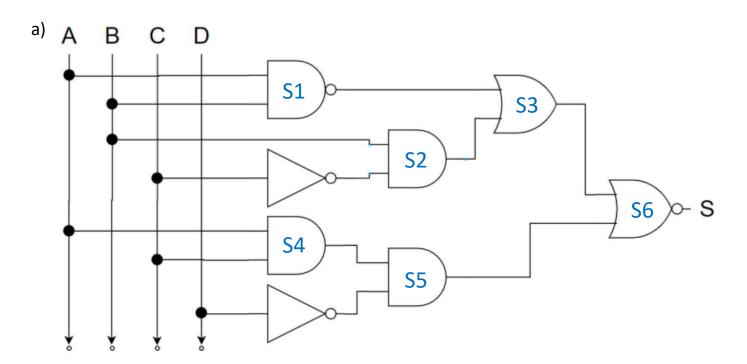
Activitat 2. Portes lògiques

1. Obté les taules de veritat i les funcions dels següents circuits:

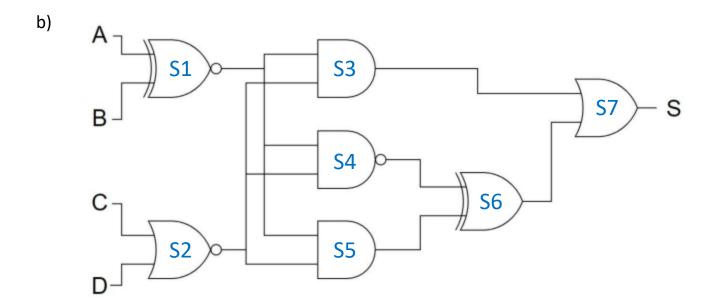


Taula de veritat

								S1	S2	S3	S4	S5	S6
Α	В	C	D	A'	B'	C'	D'	(A ⋅ B)'	B·C'	S1 + S2	A·C	\$4 ⋅ D'	(S3 + S5)'
0	0	0	0	1	1	1	1	1	0	1	0	0	0
0	0	0	1	1	1	1	0	1	0	1	0	0	0
0	0	1	0	1	1	0	1	1	0	1	0	0	0
0	0	1	1	1	1	0	0	1	0	1	0	0	0
0	1	0	0	1	0	1	1	1	1	1	0	0	0
0	1	0	1	1	0	1	0	1	1	1	0	0	0
0	1	1	0	1	0	0	1	1	0	1	0	0	0
0	1	1	1	1	0	0	0	1	0	1	0	0	0
1	0	0	0	0	1	1	1	1	0	1	0	0	0
1	0	0	1	0	1	1	0	1	0	1	0	0	0
1	0	1	0	0	1	0	1	1	0	1	1	1	0
1	0	1	1	0	1	0	0	1	0	1	1	0	0
1	1	0	0	0	0	1	1	0	1	1	0	0	0
1	1	0	1	0	0	1	0	0	1	1	0	0	0
1	1	1	0	0	0	0	1	0	0	0	1	1	0
1	1	1	1	0	0	0	0	0	0	0	1	0	1

Funció del circuit

 $((A \cdot B)' + B \cdot C' + A \cdot C \cdot D')'$

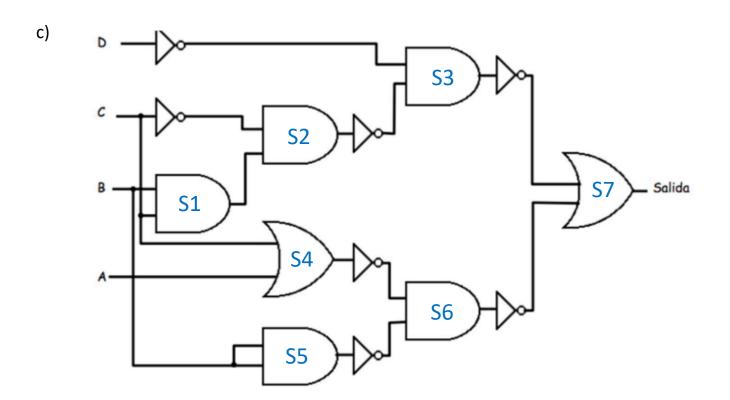


Taula de veritat

								S1	S2	S3	S4	S5	S6	S7
Α	В	С	D	A'	B'	C'	D'	A ⊙ B	(C + D)'	S1 · S2	(S1 · S2)'	S1 · S2	S4 ⊕ S5	S3 + S6
0	0	0	0	1	1	1	1	1	1	1	0	1	1	1
0	0	0	1	1	1	1	0	1	0	0	1	0	1	1
0	0	1	0	1	1	0	1	1	0	0	1	0	1	1
0	0	1	1	1	1	0	0	1	0	0	1	0	1	1
0	1	0	0	1	0	1	1	0	1	0	1	0	1	1
0	1	0	1	1	0	1	0	0	0	0	1	0	1	1
0	1	1	0	1	0	0	1	0	0	0	1	0	1	1
0	1	1	1	1	0	0	0	0	0	0	1	0	1	1
1	0	0	0	0	1	1	1	0	1	0	1	0	1	1
1	0	0	1	0	1	1	0	0	0	0	1	0	1	1
1	0	1	0	0	1	0	1	0	0	0	1	0	1	1
1	0	1	1	0	1	0	0	0	0	0	1	0	1	1
1	1	0	0	0	0	1	1	1	1	1	0	1	1	1
1	1	0	1	0	0	1	0	1	0	0	1	0	1	1
1	1	1	0	0	0	0	1	1	0	0	1	0	1	1
1	1	1	1	0	0	0	0	1	0	0	1	0	1	1

Funció del circuit

$$(A \odot B) \cdot (C + D)' + ((A \odot B) \cdot (C + D)')' \oplus (A \odot B) \cdot (C + D)'$$

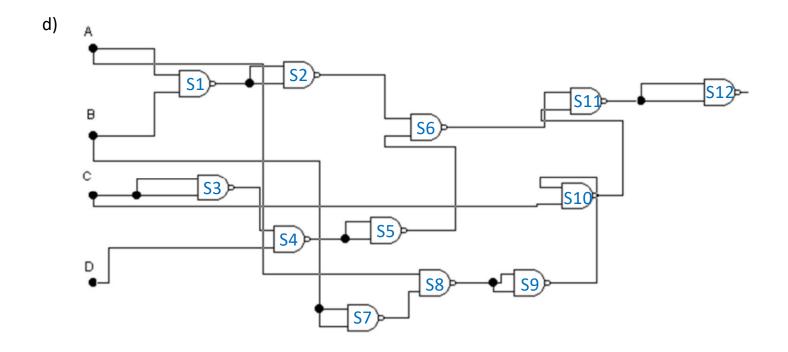


Taula de veritat

								S1	S2	S3	S4	S5	S6	S7
A	В	С	D	A'	B'	C'	D'	B · C	(C'·S1)'	(D' · S2)'	(C + A)'	(B · B)'	(S4 · S5)'	S3 + S6
0	0	0	0	1	1	1	1	0	1	0	1	1	0	0
0	0	0	1	1	1	1	0	0	1	1	1	1	0	1
0	0	1	0	1	1	0	1	0	1	0	0	1	1	1
0	0	1	1	1	1	0	0	0	1	1	0	1	1	1
0	1	0	0	1	0	1	1	0	1	0	1	0	1	1
0	1	0	1	1	0	1	0	0	1	1	1	0	1	1
0	1	1	0	1	0	0	1	1	1	0	0	0	1	1
0	1	1	1	1	0	0	0	1	1	1	0	0	1	1
1	0	0	0	0	1	1	1	0	1	0	0	1	1	1
1	0	0	1	0	1	1	0	0	1	1	0	1	1	1
1	0	1	0	0	1	0	1	0	1	0	0	1	1	1
1	0	1	1	0	1	0	0	0	1	1	0	1	1	1
1	1	0	0	0	0	1	1	0	1	0	0	0	1	1
1	1	0	1	0	0	1	0	0	1	1	0	0	1	1
1	1	1	0	0	0	0	1	1	1	0	0	0	1	1
1	1	1	1	0	0	0	0	1	1	1	0	0	1	1

Funció del circuit

$$(\mathsf{D}'\cdot(\mathsf{C}'\cdot\mathsf{B}\cdot\mathsf{C})')'+((\mathsf{C}+\mathsf{A})'\cdot(\mathsf{B}\cdot\mathsf{B}))'$$



Taula de veritat (pt. 1)

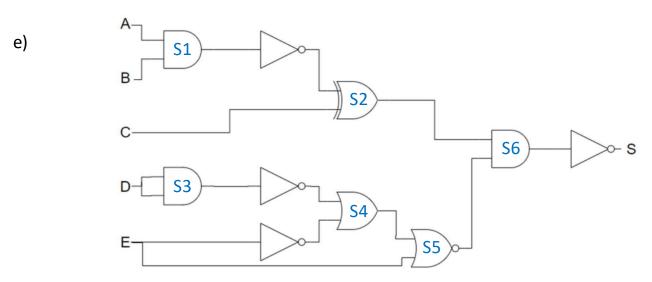
								S1	S2	S3	S4	S5
Α	В	С	D	A'	B'	C'	D'	(A ⋅ B)'	(S1 · S1)'	(C · C)'	(S3 · D)'	(S4 · S4)'
0	0	0	0	1	1	1	1	1	0	1	1	0
0	0	0	1	1	1	1	0	1	0	1	0	1
0	0	1	0	1	1	0	1	1	0	0	1	0
0	0	1	1	1	1	0	0	1	0	0	1	0
0	1	0	0	1	0	1	1	1	0	1	1	0
0	1	0	1	1	0	1	0	1	0	1	0	1
0	1	1	0	1	0	0	1	1	0	0	1	0
0	1	1	1	1	0	0	0	1	0	0	1	0
1	0	0	0	0	1	1	1	1	0	1	1	0
1	0	0	1	0	1	1	0	1	0	1	0	1
1	0	1	0	0	1	0	1	1	0	0	1	0
1	0	1	1	0	1	0	0	1	0	0	1	0
1	1	0	0	0	0	1	1	0	1	1	1	0
1	1	0	1	0	0	1	0	0	1	1	0	1
1	1	1	0	0	0	0	1	0	1	0	1	0
1	1	1	1	0	0	0	0	0	1	0	1	0

Taula de veritat (pt. 2)

S6 (S2 · S5)'	S7 (B · B)'	S8 (A · S7)'	S9 (S8 · S8)'	S10 (S9 · C)'	S11 (S6 · S10)'	S12 (S11 · S11)'
1	1	1	0	1	0	1
1	1	1	0	1	0	1
1	1	1	0	1	0	1
1	1	1	0	1	0	1
1	0	1	0	1	0	1
1	0	1	0	1	0	1
1	0	1	0	1	0	1
1	0	1	0	1	0	1
1	1	0	1	1	0	1
1	1	0	1	1	0	1
1	1	0	1	0	1	0
1	1	0	1	0	1	0
1	0	1	0	1	0	1
0	0	1	0	1	1	0
1	0	1	0	1	0	1
1	0	1	0	1	0	1

Funció del circuit

$$(((((A \cdot B)' \cdot (A \cdot B)')' \cdot (((C \cdot C)' \cdot D)' \cdot ((C \cdot C)' \cdot D)')')' \cdot (((A \cdot (B \cdot B)')' \cdot (A \cdot (B \cdot B)')')' \cdot C)')' \\ \cdot \\ ((((A \cdot B)' \cdot (A \cdot B)')' \cdot (((C \cdot C)' \cdot D)' \cdot ((C \cdot C)' \cdot D)')')' \cdot (((A \cdot (B \cdot B)')' \cdot (A \cdot (B \cdot B)')')' \cdot C)')')' \\)$$



Taula de veritat

										S1	S2	S3	S4	S5	S6
Α	В	С	D	Ε	A'	B'	C'	D'	E'	(A · B)'	S1 ⊕ C	(D · D)'	S3 + E'	(S4 + E)'	(S2 · S5)'
0	0	0	0	0	1	1	1	1	1	1	1	1	1	0	1
0	0	0	0	1	1	1	1	1	0	1	1	1	1	0	1
0	0	0	1	0	1	1	1	0	1	1	1	0	1	0	1
0	0	0	1	1	1	1	1	0	0	1	1	0	0	0	1
0	0	1	0	0	1	1	0	1	1	1	0	1	1	0	1
0	0	1	0	1	1	1	0	1	0	1	0	1	1	0	1
0	0	1	1	0	1	1	0	0	1	1	0	0	1	0	1
0	0	1	1	1	1	1	0	0	0	1	0	0	0	0	1
0	1	0	0	0	1	0	1	1	1	1	1	1	1	0	1
0	1	0	0	1	1	0	1	1	0	1	1	1	1	0	1
0	1	0	1	0	1	0	1	0	1	1	1	0	1	0	1
0	1	0	1	1	1	0	1	0	0	1	1	0	0	0	1
0	1	1	0	0	1	0	0	1	1	1	0	1	1	0	1
0	1	1	0	1	1	0	0	1	0	1	0	1	1	0	1
0	1	1	1	0	1	0	0	0	1	1	0	0	1	0	1
0	1	1	1	1	1	0	0	0	0	1	0	0	0	0	1
1	0	0	0	0	0	1	1	1	1	1	1	1	1	0	1
1	0	0	0	1	0	1	1	1	0	1	1	1	1	0	1
1	0	0	1	0	0	1	1	0	1	1	1	0	1	0	1
1	0	0	1	1	0	1	1	0	0	1	1	0	0	0	1
1	0	1	0	0	0	1	0	1	1	1	0	1	1	0	1
1	0	1	0	1	0	1	0	1	0	1	0	1	1	0	1
1	0	1	1	0	0	1	0	0	1	1	0	0	1	0	1
1	0	1	1	1	0	1	0	0	0	1	0	0	0	0	1
1	1	0	0	0	0	0	1	1	1	0	0	1	1	0	1
1	1	0	0	1	0	0	1	1	0	0	0	1	1	0	1
1	1	0	1	0	0	0	1	0	1	0	0	0	1	0	1
1	1	0	1	1	0	0	1	0	0	0	0	0	0	0	1
1	1	1	0	0	0	0	0	1	1	0	1	1	1	0	1
1	1	1	0	1	0	0	0	1	0	0	1	1	1	0	1
1	1	1	1	0	0	0	0	0	1	0	1	0	1	0	1
1	1	1	1	1	0	0	0	0	0	0	1	0	0	0	1

Funció del circuit

 $(((A \cdot B)' \oplus C) \cdot (((D \cdot D)' + E') + E)')'$