Name of the Institute: DYP DPU Dr. D.Y. Patil Institute of Technology Pimpri, Pune

Name of the Department: Computer Engineering Department

Class & Division: T.E. – A & B

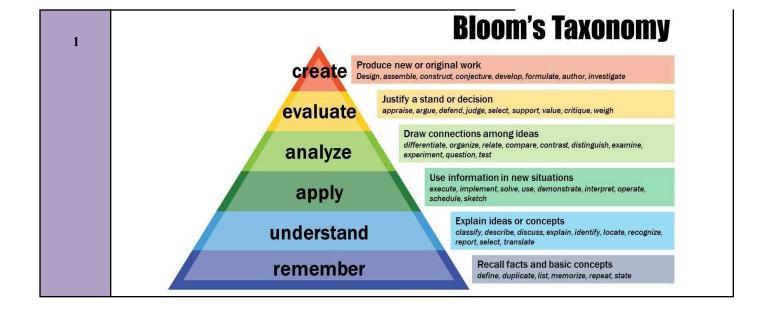
Subject: System Programming and Operating System

Name: Adsure Sharad Sampat, Amol Dhepe, Sunita Patil, Vidya Harkal
Assignment No: 02 Batch No: B1/B2/B3
A1: Q 1, 2,3,4,8,14 A2: Q 5,7,8,10,12,14 A3: Q 6,9,11,12,13,14

Maximum marks for Assignment: 8 Assignment Declaration Date: 28/7/2025

Assignment Submission Date: (8/8/2025) Assignment assessment declaration Date by faculty: (8/8/2025)

| Question NO | Question Statement | Level of mapping & NO. | | | Blooms Level | Marks |
|----------------|---|------------------------|------|-----|-----------------|-------|
| | | CO | РО | PSO | | |
| Q.1. | Explain MNT, MDT, MDTP and ALA with respect to macro processor Using example? | 2 | 1 | | 3 | 4 |
| Q.2 | Explain with suitable example term Macro expansion, nested macro call. | 2 | 1,10 | | 2 | 4 |
| Q.3 | Explain General loading scheme with advantages & Disadvantages | 2 | 1,3 | | 2 | 5 |
| Q.4 | Explain Advanced Macro facilities with example 1. Alteration of flow of control during macro expansion 2. Expansion time variable | 2 | 2,3 | | 2 | 6 |
| Q.5 | Explain the process of Macro expansion with relevant data Structure. | 2 | 1 | | 2 | 6 |
| Q.6 | Explain design of two pass Macro preprocessor in details. | 2 | 1,3 | | 2 | 6 |
| Q.7 | List & explain various code optimization techniques | 2 | 2 | | 2 | 4 |
| Q.8 | Define Macro. What are the advantages of macro facility? Explain difference between Macros and Function | 1 | 1 | | 2 | 8 |
| Q.9 | Explain in detail various phases of a compiler. | 3 | 1 | | 2 | 5 |
| Q.10 | Compare Compiler and Interpreter | 3 | 1 | | 1 | 4 |
| Q.11 | Explain various phases of the Compiler for the expression $x = I + R * 60$ where the data type of R is float. | 2 | 2 | | 3 | 4 |
| Q.12 | Define the terms Token, Lexeme, Pattern, String, Alphabets | 2 | 1 | | 1 | 5 |
| Q.13 | Explain argument passing mechanism in Macros with suitable examples? | 2 | 1 | | 1 | 6 |
| Q14. | Explain the concept of single pass Macro processor with examples. | 2 | 1,3 | | 2 | 6 |
| Blooms | Give an example for macro calls within the macro. | | | | | |
| level no | Blooms Taxonomy terms | | | | | |
| 6 | | | | | | |
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Subject In-charge HOD