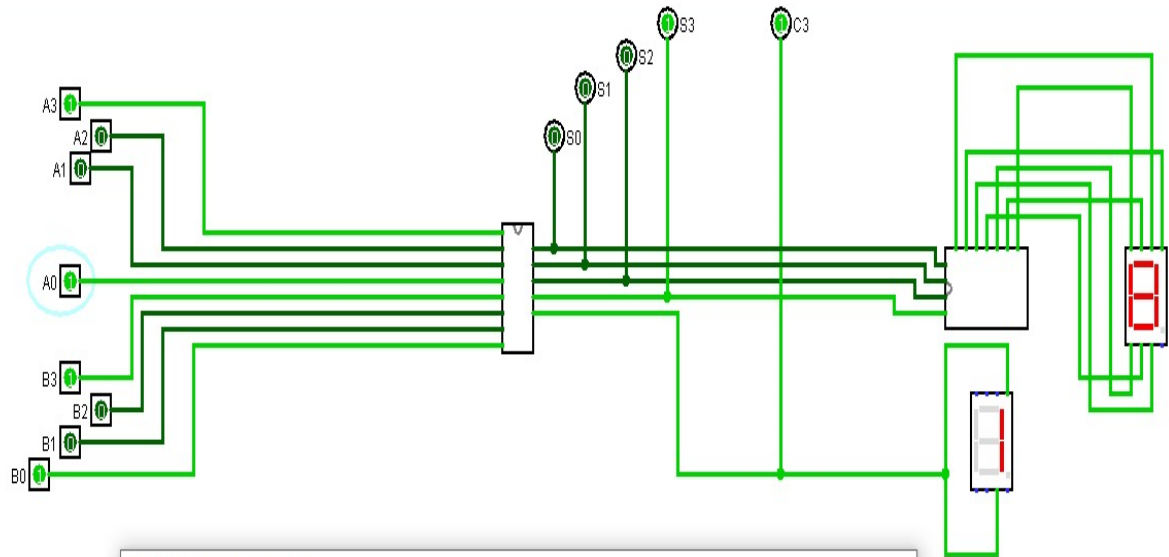


Assignment-3



Combinational Analysis

File Edit Project Simulate Window Help

Inputs Outputs Table Expression Minimized

A3	A2	A1	A0	B3	B2	B1	B0	S3	C3	S2	S1	S0
0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	1	0	0	0	0	1
0	0	0	0	0	0	1	0	0	0	0	1	0
0	0	0	0	0	0	1	1	0	0	0	1	1
0	0	0	0	0	1	0	0	0	0	1	0	0
0	0	0	0	0	1	0	1	0	0	1	0	1
0	0	0	0	0	1	1	0	0	0	1	1	0
0	0	0	0	0	1	1	1	0	0	1	1	1
0	0	0	0	1	0	0	0	1	0	0	0	0
0	0	0	0	1	0	0	1	1	0	0	0	1
0	0	0	0	1	0	1	0	0	1	0	0	0
0	0	0	0	1	0	1	1	0	1	0	0	1
0	0	0	0	1	1	0	0	0	1	0	1	0
0	0	0	0	1	1	0	1	0	1	0	1	1
0	0	0	0	1	1	1	0	0	1	1	0	0
0	0	0	0	1	1	1	1	0	1	1	0	1
0	0	0	1	0	0	0	0	0	0	0	0	1
0	0	0	1	0	0	0	1	0	0	0	1	0
0	0	0	1	0	0	1	0	0	0	0	1	1
0	0	0	1	0	0	1	1	0	0	1	0	0
0	0	0	1	0	1	0	0	0	0	1	0	1

Build Circuit

