Topological Subsystem Codes with High-Weight Stabilizers and Moderate Thresholds

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Introduction

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Distraction: Color Codes

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Conclusions



Multiple Two-Qubit Gate Implementations

PHYSICAL REVIEW A 75, 032329 (2007)

Quantum-information processing with circuit quantum electrodynamics

Alexandre Blais, ^{1,2} Jay Gambetta, ¹ A. Wallraff, ^{1,3} D. I. Schuster, ¹ S. M. Girvin, ¹ M. H. Devoret, ¹ and R. J. Schoelkopf ¹

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(Received 30 November 2006; published 22 March 2007)

We theoretically study single and two-qubit dynamics in the circuit QED architecture. We focus on the current experimental design [Wallraff et al., Nature (London) 431, 162 (2004); Schuster et al., ibid. 445, 515 (2007)] in which superconducting charge qubits are capacitively coupled to a single high-Q superconducting coplanar resonator. In this system, logical gates are realized by driving the resonator with microwave fields. Advantages of this architecture are that it allows for multiqubit gates between non-nearest qubits and for the realization of gates in parallel, opening the possibility of fault-tolerant quantum computation with superconduting circuits. In this paper, we focus on one- and two-qubit gates that do not require moving away from the charge-degeneracy "sweet spot." This is advantageous as it helps to increase the qubit dephasing time and does not require modification of the original circuit OED. However, these gates can, in some cases, be slower than

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importance of selection rules when working at the charge degeneracy point.

Multiple Two-Qubit Gate Implementations

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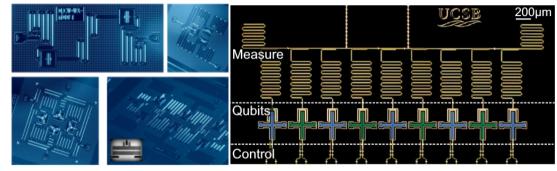
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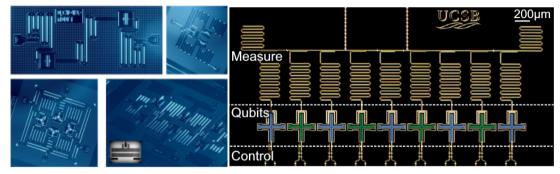
Fidelity Vs. Range



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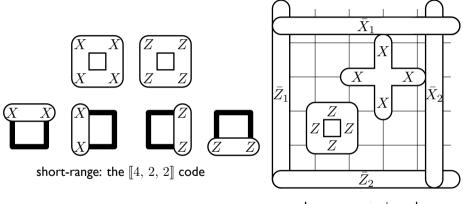
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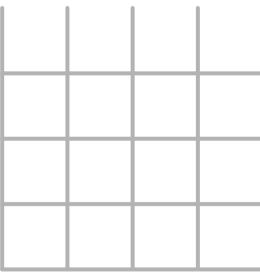
Can we use short- and long-range gates as part of the same layout?

The $[\![4,\,2,\,2]\!]$ -Toric Code

Introducing Short-Range Gates by Concatenation

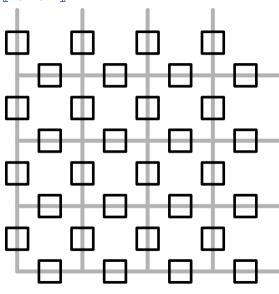
ArXiv:1604.04062, soon to appear in QIC.



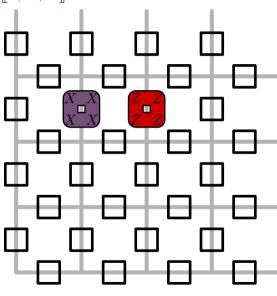


begin with a square lattice

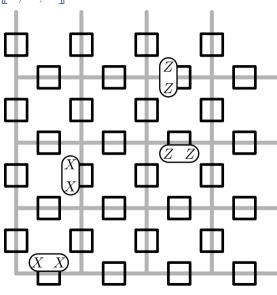
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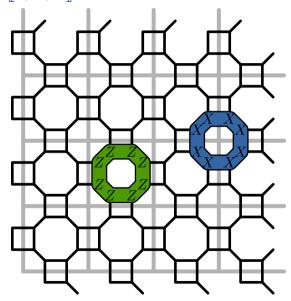
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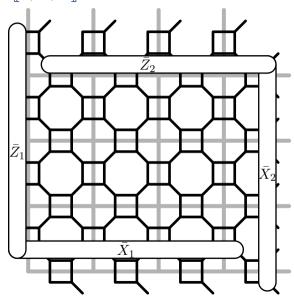
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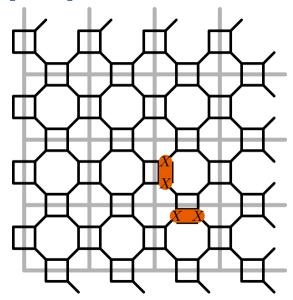
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- local logical operators



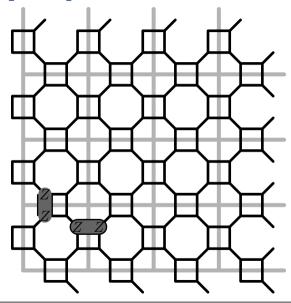
- begin with a square lattice
- replace physical qubits on edges with squares (physical qubits now on vertices, $n \to 4n$)
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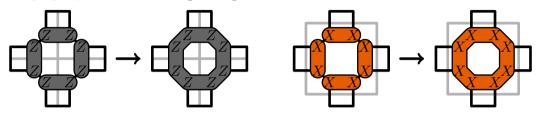


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- looks a lot like a color code ...

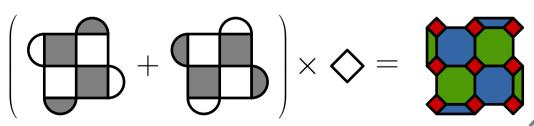
Distraction: Color Codes

Color Codes as Concatenated Codes

The gauge operators from the [4, 2, 2]-toric code can be encoded into a toric code:

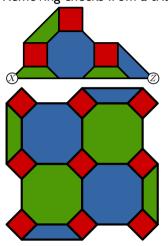


As long as every qubit is on a square, this is a color code:



Triangular Boundary Conditions/Logical Cliffords

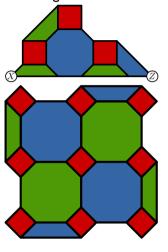
Removing checks from a triangular color code doesn't work:



Extra weight-I logical operators appear at corners.

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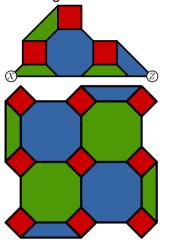


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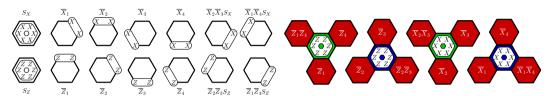
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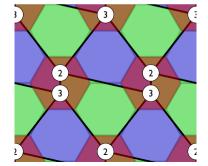


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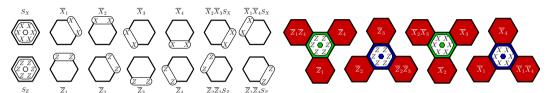
- lacksquare Concatenated code is not self-dual o no transversal logical H
- \blacksquare Concatenated code has even $n \to \operatorname{no}$ transversal logical P

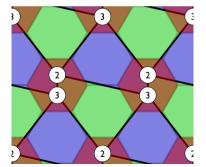
We can also concatenate the $[\![6, 4, 2]\!]$ code with two toric codes to get a color code:





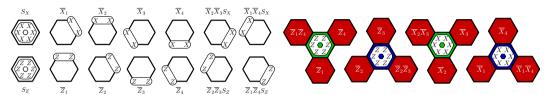
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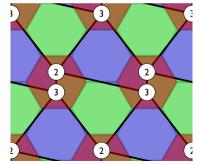




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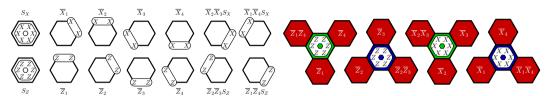
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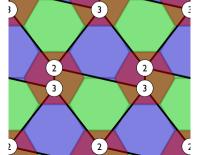




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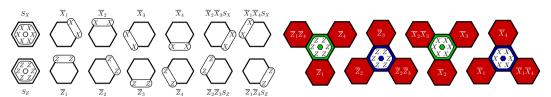
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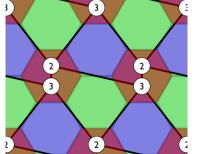




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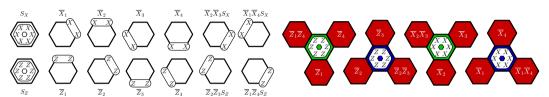
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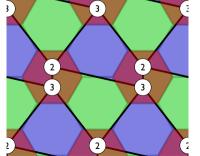




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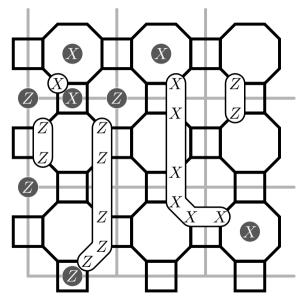
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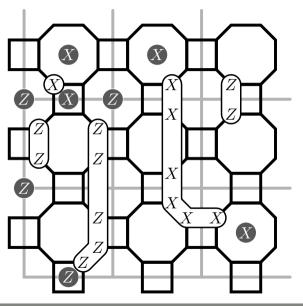




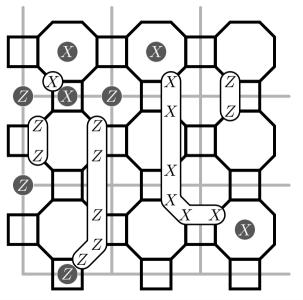
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- Maybe extends to higher dimensions
- Small chance it can help with decoding

Back To Work: Decoding the $[\![4,\,2,\,2]\!]$ -Toric Code

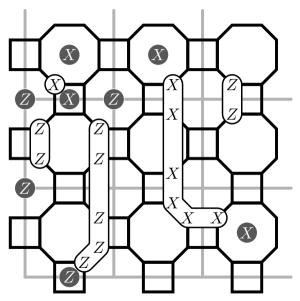




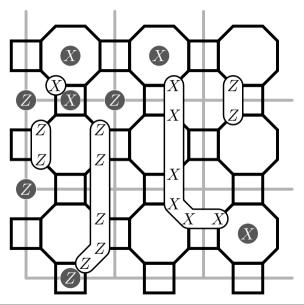
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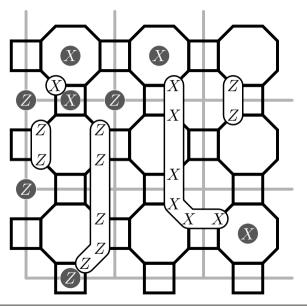
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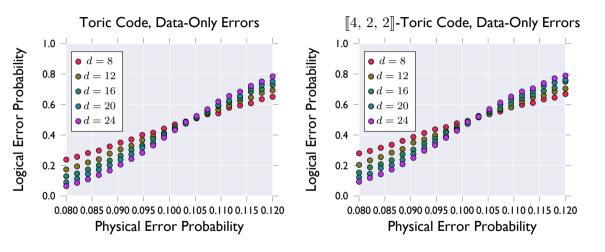
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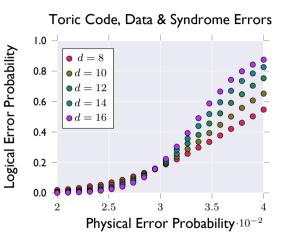
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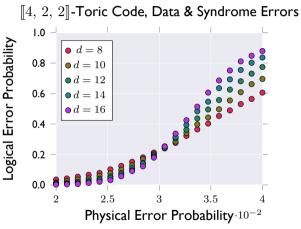


- [4, 2, 2]-toric code is CSS
- Syndromes appear at endpoints of a chain, as in the toric code
- Weight of an error still length-like:
 - chains between octagons use a Manhattan metric
 - square-to-octagon (square-to-square) chains use the minimum octagon-to-octagon length plus one(two)



IID X/Z/Stabiliser Errors





Aside: Gauge Generators Vs. Error Propagation

Errors which propagate through CNOT gates on squares can result in gauge operators:



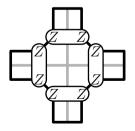


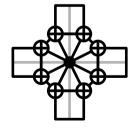




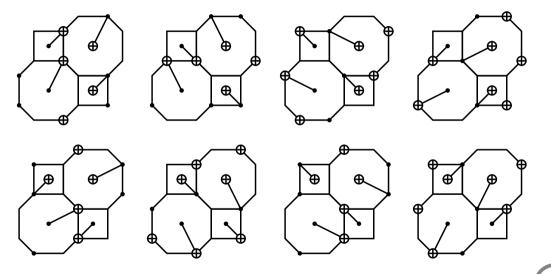


However, octagons whose CNOTs propagate X errors have Z gauge operators on the perimeter, and vice versa:

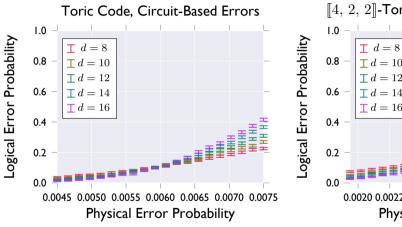


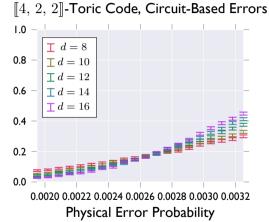


Circuit-Based Errors: Eight-Step Circuit



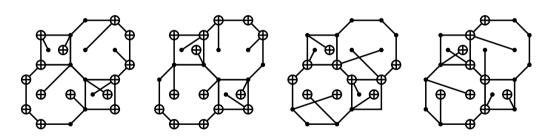
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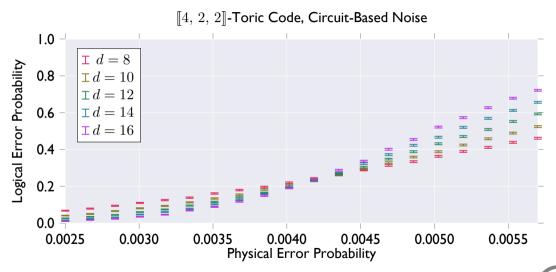
Aside: Bell Pair Ancillas

- There are data qubits sitting idle at every timestep in the eight-step circuit
- All ancilla qubits are being used at all times
- Solution: more ancilla qubits (prepare octagon ancillas in Bell states)

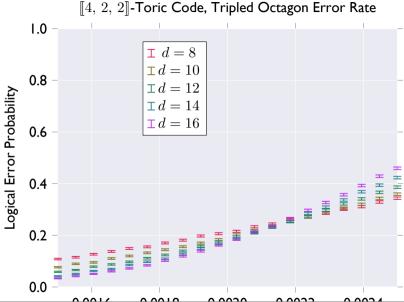


Note: This has the same number of ancilla qubits as the toric code.

Circuit-Based Errors: Four-Step Circuit



The Original Goal



Conclusions

What Have We Learned?

- We can use a level of concatenation to take advantage of high-fidelity short-range gate implementations.
- We can construct color codes by concatenation.
- Large stabilizers (weight > 4) don't destroy FT thresholds.
- Subsystem codes can have high-ish thresholds.
- Unverified high-weight (> 1) ancilla states can increase thresholds, especially where stabilizer weights differ.

What Should We Do?

- Adapt advanced decoding techniques ("diagonal edges") to decoding the $[\![4,\,2,\,2]\!]$ -toric code.
- Look for more places to insert short-range operations (ancilla preparation?).
- Account for differences in gate time.
- Use high-weight ancillas to extract syndromes from the surface code.
- Prove that all 2D (nD? gauge?) color codes can be constructed by concatenation.
- Find out if color codes can be decoded using schemes for concatenated codes.

