	Sanjivani Rural Educational Society's SANJIVANI COLLEGE OF ENGINEERING (An Autonomous Institution) Kopargaon – 423 603, Maharashtra.	ACAD-F-15 K
Academic Year: 2025-26	CIA CASE STUDY ACTIVITY ABSTRACT	ISO Revision : 00 ISO Dated: 11/10/2025
Department :	COMPUTER ENGINEERING	Date of Preparation:
Course Code & Name:	Digital Electronics & Logic Design – (MDCO221)	Year/Sem: SY(A) SEM-1 W'26

Group ID:14

Activity Title: Bus Systems in Digital Electronics.

Abstract:. Bus Systems in Digital Electronics are essential for communication between components such as the CPU, memory, and input/output devices. A bus serves as a common pathway to transfer data, addresses, and control signals efficiently. The main types include the **data bus**, **address bus**, and **control bus**, which together ensure proper coordination and performance of a digital system. Understanding bus architecture is vital for designing reliable, fast, and scalable digital circuits.

Expected I/P : Digital signals, address lines, control signals, clock pulses.
Expected O/P : Reliable data transfer, synchronized communication, and efficient system performance.
Submitted by:

Roll No	PRN No	Name of the Student	Mobile No.	Email	Signature
66	0124UCSM1066	Deokar Sai	8055107422	saideokar35@gmail.com	
67	0124UCSM1067	Nere Tejas	9272556195	tejasnere94@gmail.com	
68	0124UCSM1068	Palande Siddharth	9822619282	palandesiddharth011@gmail.com	
69	0124UCSF1069	Gidhad Arti	9359769877	artigidhad72@gmail.com	
70	0124UCSM1070	Rashinkar Sanket	9359984963	ranshinkarsanket7@gmail.com	

Prof. P.M. Dhanrao
Subject Teacher

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Subject Teacher