COMP22111 Exercise 2 - Demonstration Marking Rubric

	Excellent	Good	Limited	Poor
Understanding - ALU	I am able to discuss is detail the operation of the ALU and how the Verilog code I written delivers this functionality.	I can discuss some elements of the operation of the ALU and have some understanding of the code I have written. However, there is some uncertainty/ hesitation in my explanation.	My understanding of the operation of the ALU and the code I have written to provide the required functionality is somewhat vague. I don't have a full grasp of what the code I have written does.	I am unable to discuss what my Verilog code is doing or how it implements the functionality of the ALU. It is clear that I do not understand the code I have written. 0
Verilog Code Quality & Style	My Verilog code is formatted nicely and is efficient in its design. Assignments are performed correctly and I have used appropriate Verilog code constructs. The code is short and compact.	The formatting/efficiency of my Verilog code is on the whole ok. However, there are some minor issues, such as the way I performed assignments, or statements are incomplete. Elements of the code could have been more efficiently written. 6	There are a number of issues with the formatting of my Verilog code. My approach needs work as the code I have used could be significantly more efficient. I can have made better choices in the Verilog constructs I have used.	My Verilog code contains a significant number of style issues and I have produced code that is very inefficient and long winded.
Code Commenting	I have clearly commented my code with informative in-code comments and provided some useful information in the file header.	I have made some attempt at commenting my code. However, the comments, in places, are nor particularly informative. I have added some useful information to the file header.	I have made some (minor) attempt at commenting my code, or the majority of my comments are not very helpful in helping to understand the code I have written. I have not updated the file header.	I have made no attempt to comment my code.
Understanding - Testing	I am able to explain why my ALU design is functionally correct using the results produced by the test harness provided.	I can partly demonstrate that my ALU is working from the results provided by the test data. However, there is some hesitancy in y discussion.	I can discuss some aspects of the test results that demonstrate I am confident my ALU design is working, but my explanation is limited in scope.	I have no idea how to demonstrate that my ALU is functionally correct.