NES Emulator

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Introduction

The Nintendo Entertainment System, frequently called NES, is an home gaming console developed by the Japanese company called Nitendo Company, Limited. The hardware itself can be divided up to five big different part.

The console main chip was manufactured by Richo, which contains the CPU (Central Processing Unit) and the APU (Audio Processing Unit) [2]. The processor itself is an 8-bit MOS Technology 6502 with a little difference that the decimal mode is not presented.

The PPU (Pixel Processing Unit), which was also shipped by Richo, is technically a primitive graphics card which is used by the system to colour and render the graphics pixel by pixel to the Television screens.

The Cartrdige which meant to provide the necessary binary code of the games and also the graphics data for the system. Also gave an opportunity to the developers to implement their own cartridge builds and use it to extend the console's capabilities one great example for that is the first The legend of Zelda game. The game's cartridge also contains a battery powered RAM extension for players to save their game state [1].

Two 8 button, these buttons are up, down, left, right, select, start, A, B, controller provided the interface for user input to the system. The NES controller was the first controller which introduced the single button plus symbol shaped DPAD. Each Nintendo system were brought some revolutionary design idea to the world of gaming console controllers.

The RAM (Random Access Memory) is the central piece of the hardware which not only holds data but trough memory mapping it also connect all the other pieces to the CPU. Therefore the developers can control the full hardware behaviours trough with specific read and write operations to certain memory slots. In this project these core parts of the hardware will be emulated on an x86-64 machines. As a result, provide an application which is able to run those games which were developed for the original NES system.

1.1 History

Project Goal

Project Design

Specification and implementation

- 4.1 RAM
- 4.1.1 Specification
- 4.1.2 Implementation
- 4.2 Cartridge (ROM)
- 4.2.1 Specification
- 4.2.2 Implementation
- **4.3** CPU
- 4.3.1 Specification
- 4.3.2 Implementation
- 4.4 PPU
- 4.4.1 Specification
- 4.4.2 Implementation

Evaluation

- 5.1 Testing
- 5.2 Performance and Precision
- 5.3 Game Performance

Conclusions

Bibliography

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