CHAIR SPEAKER TI RECEIVER

MAJOR REVISION HISTORY:

PCB REV.	SCH. REV.	DESCRIPTION	DATE
0.2	0.2	Initial Draft	12-MAY-2021
	0.3	1.VCHRG_DET circuit is removed. 2.VBAT pin of CC8530 is connected to VBAT 3.Enable speaker enable pullups by default. (placed R33/R43, DNP R35/R45) 4.Removed Q4 & Q5 and sink LED current directly into CC8530 Marked LED2 and R22 as DNP. 5.Marked D4,D5, D6, D7,D10, D11 & U4 DNP 6.Test point added at I2C line 7.Connection of U4 is rearragned as per routing. 8.FB3 removed and make common AUGND and GND plane 9.RF Clip part number changed to S1001-46R. 10.Changed ON/OFF/Pair button connection to CS_N pin of CCS8530. 11.MICBIAS pin of DAC is additionally connected with SPKR_EN_LR using 0R link. 12.Marked the battery and charger circuits DNP by default. Added 0R resistor from VCHRG to VBAT_LDO and marked D8 and D9 as DNP. 13. Add R7 and change R2 and R7 part number as 1.5W 14. Changed LED resistor value from 100E to 220E.	
0.3	0.3	(1) Mark R6 mount (2) Mark J1 as DNP and added J5	01-June-2021
	0.4	Mark R20, C77, J2, J5 as DNP	11-July-2021
	0.5	Added U8 C2590 and it's components	17-July-2021
1.0 1.1	1.0	LED1 Connected to VCHARG LED2 position updated in layout	04-Sept-2021 20-April-2022

PAGE DESCRIPTION:

PAGE 01: COVER PAGE

PAGE 02: S1 POWER

PAGE 03 : S2_WirelessAudio_CC8530 PAGE 04: S3_CHAIR_SPKR-AMP+SPK

PCB MECHANICAL DETAILS:

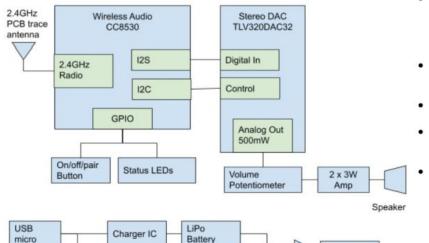
1. PCB SIZE: 74.93mm Dia. (Round)

2. NUMBER OF LAYERS: 2

3. IMPEDANCE CONTROL: YES

BLOCK DIAGRAM

Chairspeaker Receiver



- Wireless Audio CC8530
 - o I2S master to DAC
 - o PCB trace antenna
 - o Push-button input
 - 2 status LED outputs
- Stereo DAC TLV320DAC32
 - o I2S slave from CC8530
 - o I2C control from CC8530
- Speaker amplifier
 - o 2 x NS4150
- Dual-gang Potentiometer RV0971GS
 - o Control volume of output
 - o Switch gates power to the LDO to turn the board on or off
- Power section

3.3V LDO

- o USB micro 5V input
- Lipo charge and battery
- o Board can be powered by either USB or battery

NOTES:

