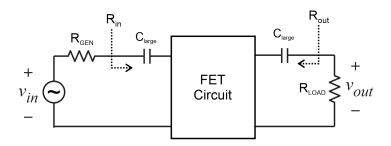
1) Small Signal Parameters: For the following FET amplifiers, you are going to obtain the small signal parameters,  $A_V$  (voltage gain),  $R_{in}$  and  $R_{out}$  using the circuits from homework assignment #3. The circuits from assignment #4 fit into the box shown here. The sinusoidal generator,  $v_{in}$ , has an internal resistance  $R_{GEN}$ . The amplifier output drives a resistive load,  $R_{LOAD}$ .



The FET parameters are  $V_{TN} = 2.6V$ ,  $k_N = 0.12 \, \text{A/V}^2$ ,  $V_A = large$ . For each circuit, the bias point (Q-point) is the same as found in homework assignment #5 with  $V_{GS} = 3.0 \, \text{V}$ ,  $I_{DQ} = 9.6 \, \text{mA}$  and  $V_{DS} = 10.4 \, \text{V}$ . For this assignment you do not need to repeat the calculations of the DC parameters (Step 1). In this case, the FET is "saturated" and we will assume that the FET is operating under the small signal condition having a magnitude of  $v_{in}$  that places  $v_{gS} \ll k_n (V_{GS} - V_{TN})$  (requirement for small signal).

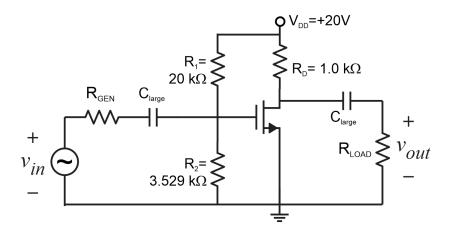
For the following circuits, use a generator resistance and load resistance of

$$R_{GEN} = 1 \text{ k}\Omega$$

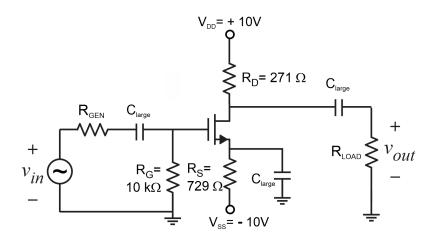
$$R_{IOAD} = 1 \text{ k}\Omega$$

Find  $A_V$  (voltage gain),  $R_{in}$  and  $R_{out}$  for each of the following circuits (A-D).

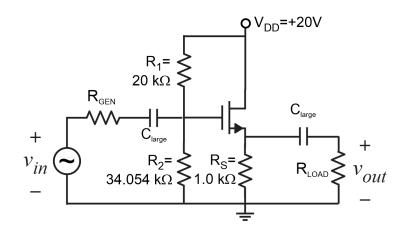
### A) Common Source Amplifier



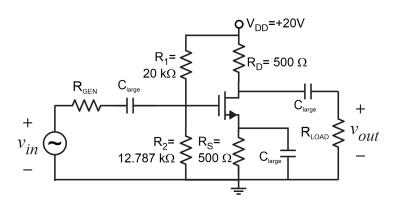
## B) Common Source Amplifier with bypassed source resistor



# C) Common Drain (a.k.a. Source Follower)



## D) Common Source Amplifier with bypassed source resistor



#### 2) Hand Calculation of Harmonic Distortion

<u>Background</u>: When the input signal to a non-linear network is a sinusoid ( $cos\omega t$ ) at frequency  $\omega$  (=2 $\pi f$ ). The output from this network may contain a DC term, a fundamental frequency ( $\omega$ ), and a set of harmonics (2 $\omega$ , 3 $\omega$ , 4 $\omega$ , ...).

Output = DC + 
$$A\cos\omega t + B\cos2\omega t + C\cos3\omega t + ...$$

The  $2^{nd}$  harmonic distortion (2HD) at the output is defined as the ratio of the <u>amplitude</u> in the  $2^{nd}$  harmonic to the <u>amplitude</u> of the fundamental, namely

$$2HD$$
 (%) =  $B/A \times 100\%$ 

### Questions:

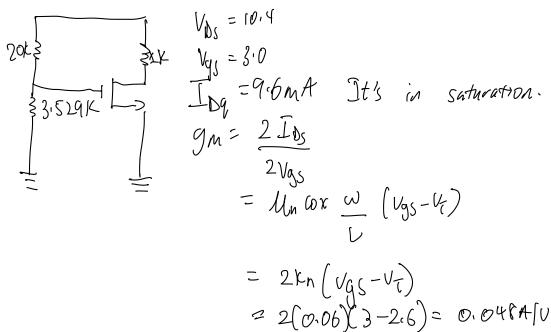
A) Find an expression for the  $2^{nd}$  harmonic distortion (2HD) for an n-channel MOSFET using the following expression for saturated drain current,  $i_D$ . The 2HD will be given in terms of A,  $V_{GS}$  and  $V_{TN}$ .

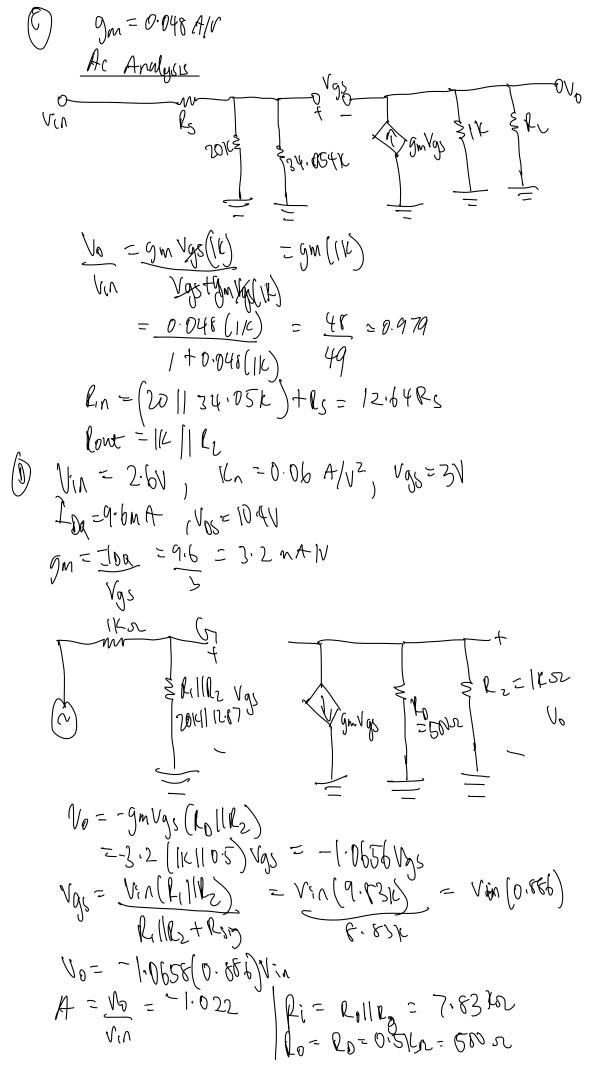
Assume a sinusoidal gate-source voltage,  $v_{qs}$  =  $A\cos\omega t$ 

Hint: 
$$\cos^2 \Theta = \frac{1}{2} + \frac{1}{2} \cos 2\Theta$$

$$i_{D} = \frac{k_{N}}{2} (V_{GS} - V_{TN})^{2} + k_{N} (V_{GS} - V_{TN}) v_{gS} + \frac{k_{N}}{2} v_{gS}^{2}$$

B) What is the value of A (amplitude of  $v_{gs}$  =  $Acos\omega t$ ) that produces a 1% 2HD? Assume that  $V_{GS}$  = 2.8V and  $V_{TN}$  = 2.6V.  $V_{GSO}-V_{TN}$ 





(2)
(a) 
$$i_D = K_n (v_{SSR} - v_{TN})^2 + 2k_n (v_{SSR} - v_{TN}) A \omega s(\omega t) + k_n A^2 \omega s^2(\omega t)$$

$$2Hb = \frac{|\ln A^2/2|}{2A \ln \left( \sqrt{9} \cdot \sqrt{9} - \sqrt{10} \right)} = \frac{A}{4 \left( \sqrt{9} \cdot \sqrt{9} - \sqrt{10} \right)}$$

So we got that the incremental voltage Vgs, that raids on top of quiescent voltage Vgs, need to have a amplitude less than 8mV, for the given Vgsq and Viw, to have 2HD less than 1%.