

Lab5 Report

一.实验内容

1.实现了 LB LH LW LBU LHU LWU SB SH SW mulw divw divuw remw remuw 指令

```
The image is ./ready-to-run/lab5/lab5-test.bin
Using simulated 256MB RAM
Using /home/wy/Document/cpu-design/ready-to-run/riscv64-nemu-interpretor-so for difftest
[src/device/io/mmio.c:19,add_mmio_map] Add mmio map 'clint' at [0x38000000, 0x3800ffff]
[src/device/io/mmio.c:19,add_mmio_map] Add mmio map 'uartlite' at [0x40600000, 0x4060000c]
[src/device/io/mmio.c:19,add_mmio_map] Add mmio map 'uartlite1' at [0x23333000, 0x2333300f]
dump wave 0-999999 to /home/wy/Document/cpu-design/build/1713016961.fst...
The first instruction of core 0 has committed. Difftest enabled.
[WARNING] difftest store queue overflow
Pass!
[src/cpu/cpu-exec.c:393,cpu_exec] nemu: HIT GOOD TRAP at pc = 0x000000008000fc58
[src/cpu/cpu-exec.c:394,cpu_exec] trap code:0
[src/cpu/cpu-exec.c:74,monitor_statistic] host time spent = 4727 us
[src/cpu/cpu-exec.c:76,monitor_statistic] total guest instructions = 16251
[src/cpu/cpu-exec.c:77,monitor_statistic] simulation frequency = 3437909 instr/s
Program execution has ended. To restart the program, exit NEMU and run again.
sh: 1: spike-dasm: not found

===== Commit Group Trace (Core 0) =====
```

2.在原有代码上添加判断有无符号，与是否是字指令的条件即可，乘除法时将字放在高 32 位即可。

二.展望

还不知道下个 lab 要怎么实现