

A

B

C

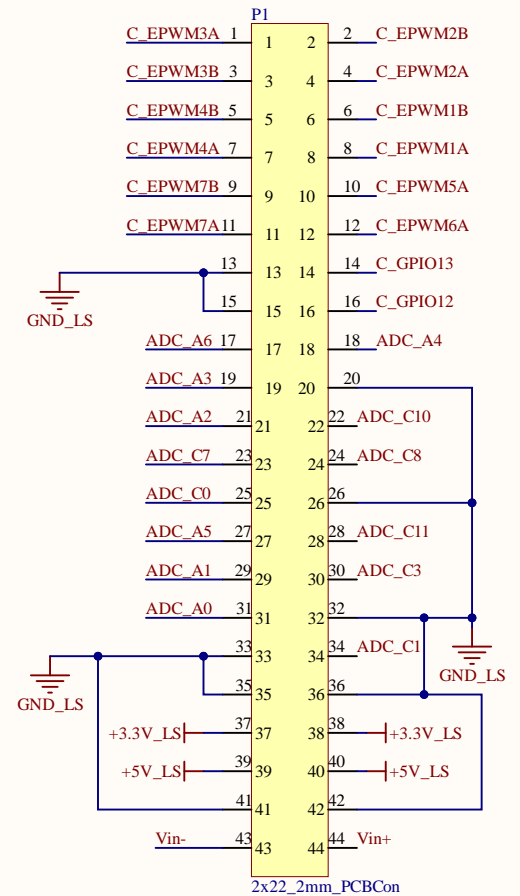
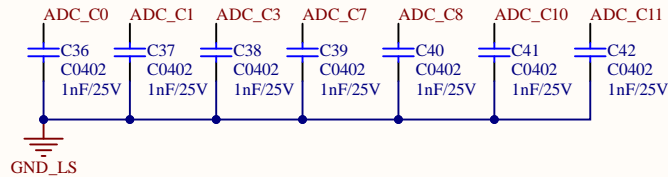
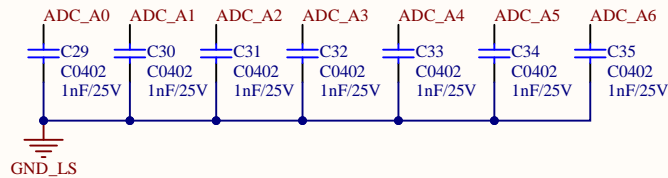
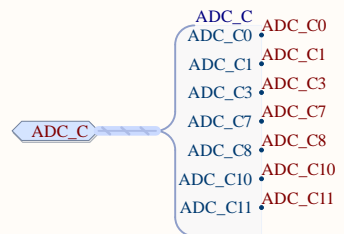
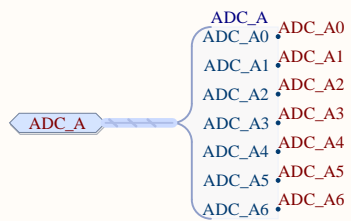
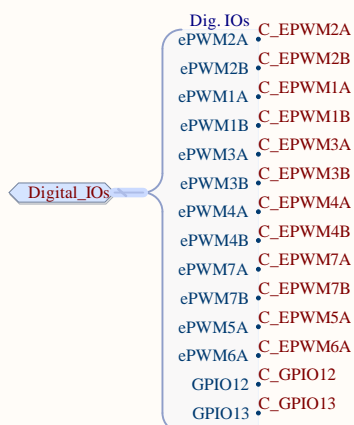
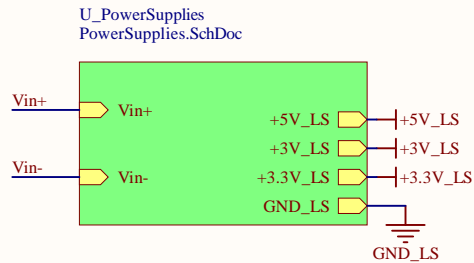
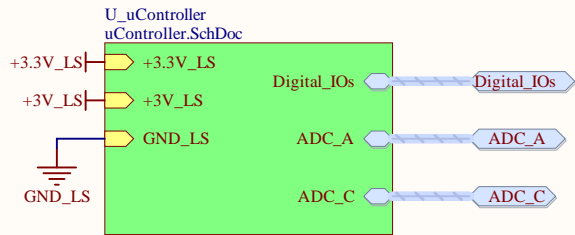
D

A

B

C

D



Title <b>2SWBB_Controller</b>			TeoMouse <a href="https://github.com/TeoMouse">https://github.com/TeoMouse</a>	
Size: <b>A4</b>	Project: <b>2SWBB Converter</b>	Revision: <b>1.0</b>		
Board: <b>F280025_Daughter_Board.PrjPcb</b>		Sheet <b>1</b> of <b>3</b>		
File: <b>Main.SchDoc</b>		Date: <b>23/3/2025</b>	Drawn By: <b>Theodoros Mouselinos</b>   Built on: <b>November 2024</b>	

A

B

C

D

A

B

C

D

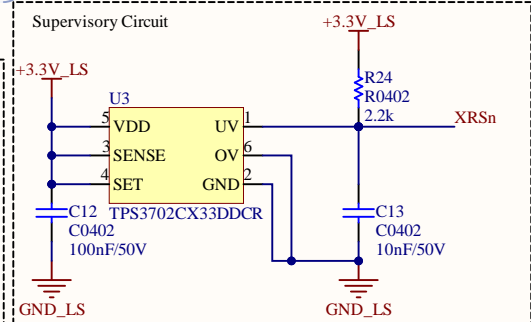
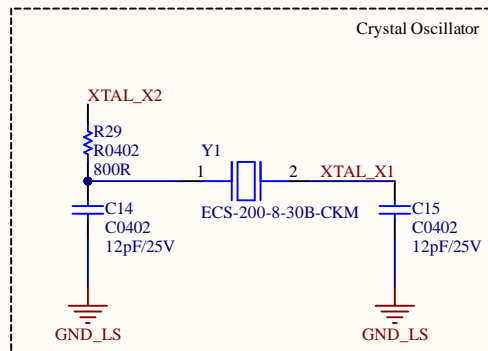
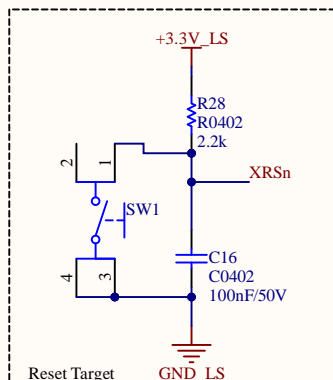
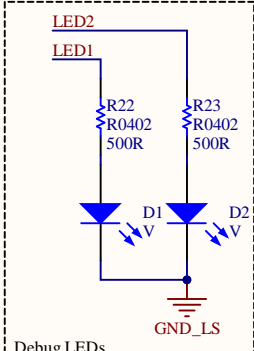
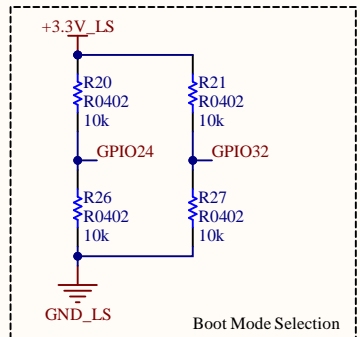
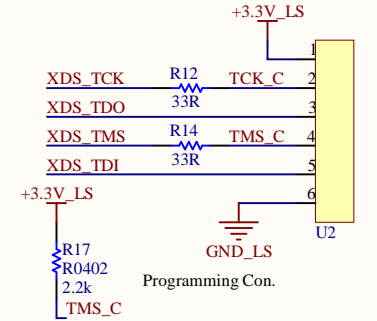
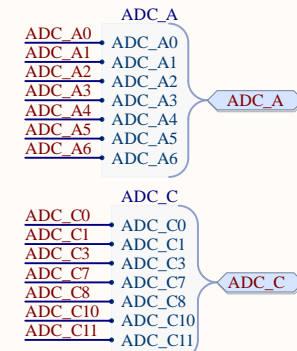
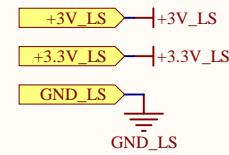
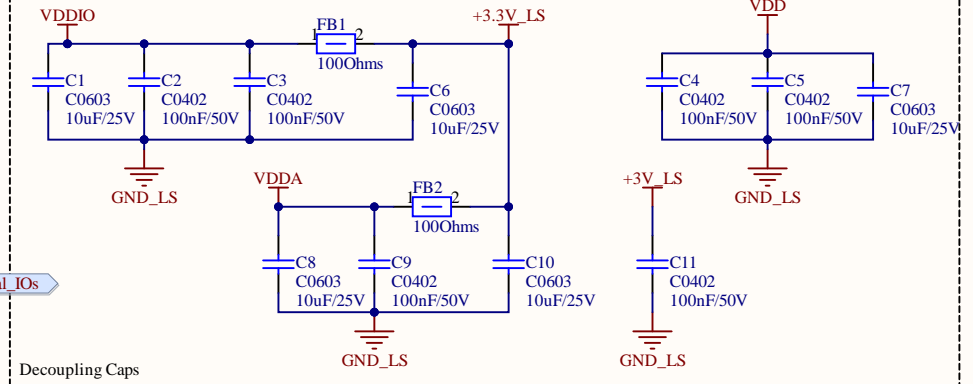
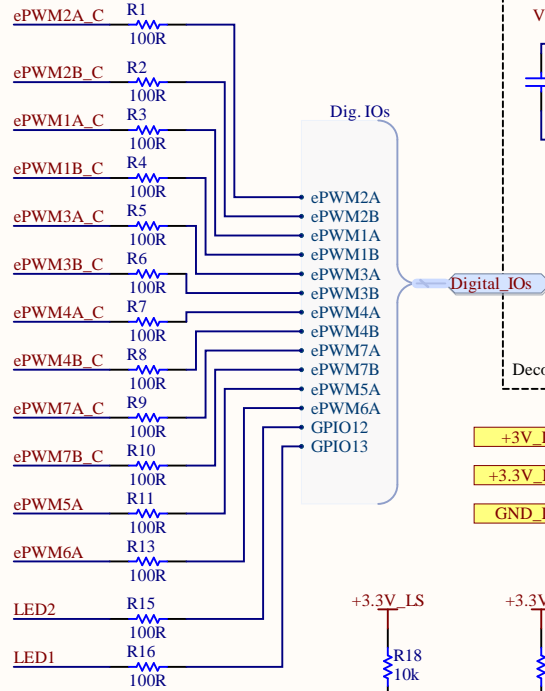
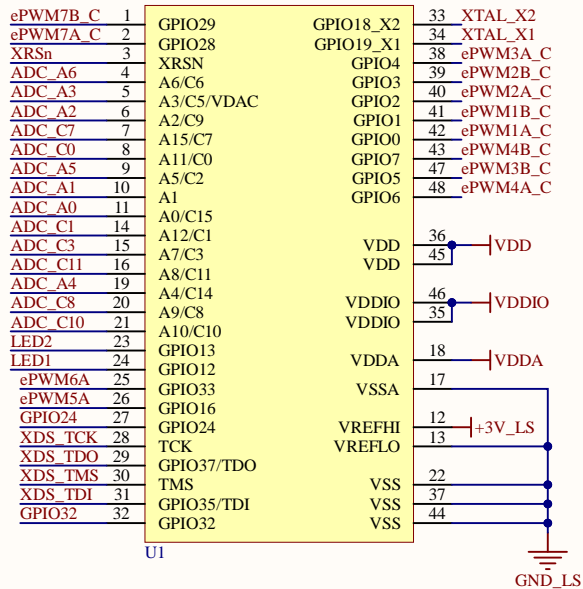
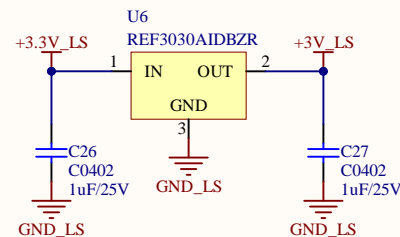
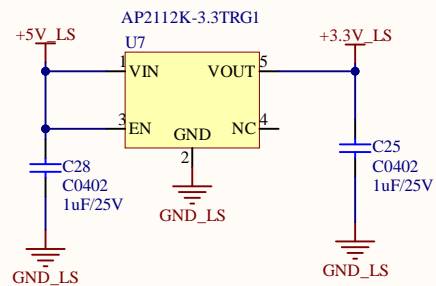
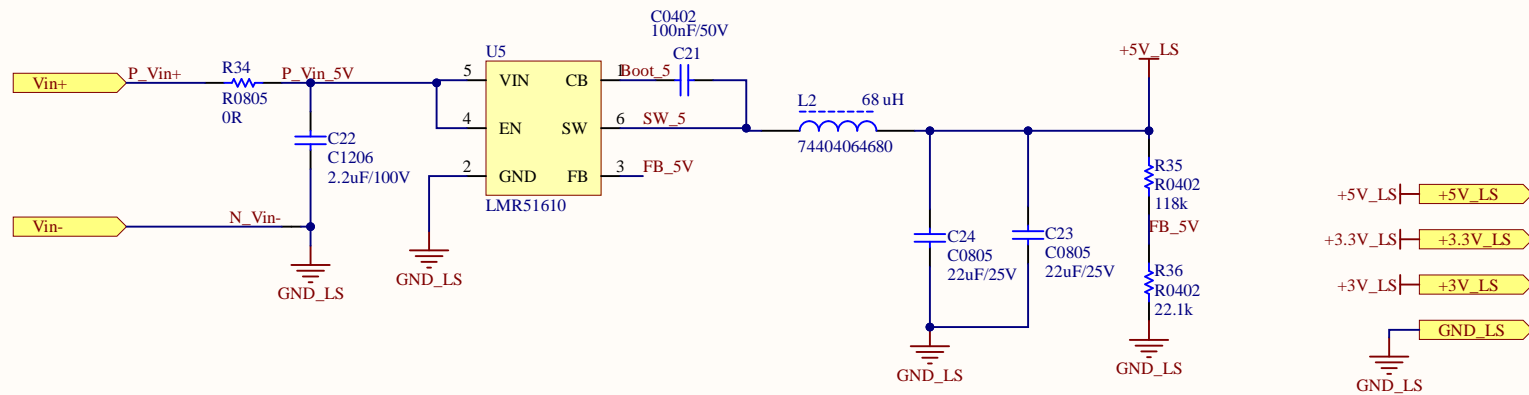


Table 8-7. Device Default Boot Modes

BOOT MODE	GPIO24 (DEFAULT BOOT MODE SELECT PIN 1)	GPIO32 (DEFAULT BOOT MODE SELECT PIN 0)
Parallel IO	0	0
SCI / Wait Boot <sup>(1)</sup>	0	1
CAN	1	0
Flash	1	1

(1) SCI boot mode can be used as a wait boot mode as long as SCI continues to wait for an 'A' or 'a' during the SCI autobaud lock process.



Title <b>2SWBB_Controller</b>			TeoMouse	
Size: A4	Project: 2SWBB Converter	Revision: 1.0	<a href="https://github.com/TeoMouse">https://github.com/TeoMouse</a>	
Board: F280025_Daughter_Board.PrjPcb	Sheet 3 of 3			
File: PowerSupplies.SchDoc	Date: 23/3/2025	Drawn By: Theodoros Mouselinos	Built on: November 2024	