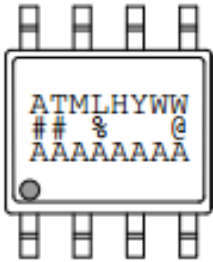
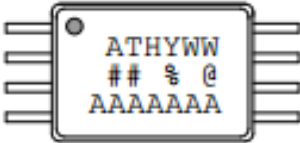
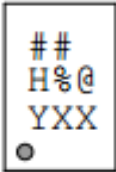


AT24CS64: Package Marking Information

8-lead SOIC	8-lead TSSOP	8-lead UDFN
		<div>2.0 x 3.0 mm Body</div> 

Note 1: ● designates pin 1
Note 2: Package drawings are not to scale

Catalog Number Truncation				
AT24CS64		Truncation Code ##: NC		
Date Codes				Voltages
Y = Year		M = Month	WW = Work Week of Assembly	Minimum Voltage = %
3: 2013	7: 2017	A: January	02: Week 2	M: 1.7V min
4: 2014	8: 2018	B: February	04: Week 4	
5: 2015	9: 2019	
6: 2016	0: 2020	L: December	52: Week 52	
Country of Assembly		Lot Number		Grade/Lead Finish Material
@ = Country of Assembly		AAA...A = Atmel Wafer Lot Number		H: Industrial/NiPdAu
Trace Code				Atmel Truncation
XX = Trace Code (Atmel Lot Number Compared to Code)				AT = Atmel