

# IRLML5203PbF

- Ultra Low On-Resistance
- P-Channel MOSFET
- Surface Mount
- Available in Tape & Reel
- Low Gate Charge
- Lead-Free
- RoHS Compliant, Halogen-Free

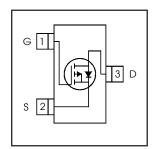
### Description

These P-channel MOSFETs from International Rectifier utilize advanced processing techniques to achieve the extremely low on-resistance per silicon area. This benefit provides the designer with an extremely efficient device for use in battery and load management applications.

A thermally enhanced large pad leadframe has been incorporated into the standard SOT-23 package to produce a HEXFET Power MOSFET with the industry's smallest footprint. This package, dubbed the Micro3<sup>TM</sup>, is ideal for applications where printed circuit board space is at a premium. The low profile (<1.1mm) of the Micro3 allows it to fit easily into extremely thin application environments such as portable electronics and PCMCIA cards. The thermal resistance and power dissipation are the best available.

### HEXFET® Power MOSFET

V <sub>DSS</sub>	$R_{DSS}$ $R_{DS(on)}$ max (m $\Omega$ )		
-30V	98@V <sub>GS</sub> = -10V	-3.0A	
	165@V <sub>GS</sub> = -4.5V	-2.6A	





Page Part Number	Dookses Type	Standard F	Pack	Orderable Part Number
Base Part Number	Package Type	Form	Quantity	
IRLML5203TRPbF	Micro3™ (SOT-23)	Tape and Reel	3000	IRLML5203TRPbF

### **Absolute Maximum Ratings**

	Parameter	Max.	Units
V <sub>DS</sub>	Drain- Source Voltage	-30	V
I <sub>D</sub> @ T <sub>A</sub> = 25°C	Continuous Drain Current, V <sub>GS</sub> @ -10V	-3.0	
I <sub>D</sub> @ T <sub>A</sub> = 70°C Continuous Drain Current, V <sub>GS</sub> @ -10V		-2.4	A
I <sub>DM</sub>	Pulsed Drain Current ①	-24	
P <sub>D</sub> @T <sub>A</sub> = 25°C	Power Dissipation	1.25	W
P <sub>D</sub> @T <sub>A</sub> = 70°C	Power Dissipation	0.80	VV
	Linear Derating Factor	10	mW/°C
$V_{GS}$	Gate-to-Source Voltage	± 20	V
T <sub>J</sub> , T <sub>STG</sub>	Junction and Storage Temperature Range	-55 to + 150	°C

### **Thermal Resistance**

	Parameter	Max.	Units
$R_{\theta JA}$	Maximum Junction-to-Ambient®	100	°C/W



# Electrical Characteristics @ $T_J = 25^{\circ}C$ (unless otherwise specified)

	Parameter	Min.	Тур.	Max.	Units	Conditions
V <sub>(BR)DSS</sub>	Drain-to-Source Breakdown Voltage	-30			V	$V_{GS} = 0V, I_D = -250\mu A$
$\Delta V_{(BR)DSS}/\Delta T_J$	Breakdown Voltage Temp. Coefficient		0.019		V/°C	Reference to 25°C, I <sub>D</sub> = -1mA
Book	Static Drain-to-Source On-Resistance			98	mΩ	V <sub>GS</sub> = -10V, I <sub>D</sub> = -3.0A ②
R <sub>DS(on)</sub>	Statio Brain to Godine On Hediotarioe			165	11152	V <sub>GS</sub> = -4.5V, I <sub>D</sub> = -2.6A ②
V <sub>GS(th)</sub>	Gate Threshold Voltage	-1.0		-2.5	V	$V_{DS} = V_{GS}$ , $I_D = -250\mu A$
9fs	Forward Transconductance	3.1			S	$V_{DS} = -10V, I_D = -3.0A$
lana	Drain-to-Source Leakage Current			-1.0		$V_{DS} = -24V, V_{GS} = 0V$
I <sub>DSS</sub>	Diali-to-Source Leakage Current			-5.0	μA	$V_{DS} = -24V, V_{GS} = 0V, T_{J} = 70^{\circ}C$
I <sub>GSS</sub>	Gate-to-Source Forward Leakage			-100	nA	V <sub>GS</sub> = -20V
IGSS	Gate-to-Source Reverse Leakage			100	IIA I	$V_{GS} = 20V$
Qg	Total Gate Charge		9.5	14		$I_D = -3.0A$
Q <sub>gs</sub>	Gate-to-Source Charge		2.3	3.5	nC	$V_{DS} = -24V$
$Q_{gd}$	Gate-to-Drain ("Miller") Charge		1.6	2.4		V <sub>GS</sub> = -10V ②
t <sub>d(on)</sub>	Turn-On Delay Time		12			V <sub>DD</sub> = -15V ②
t <sub>r</sub>	Rise Time		18		ns	$I_{D} = -1.0A$
t <sub>d(off)</sub>	Turn-Off Delay Time		88		115	$R_G = 6.0\Omega$
t <sub>f</sub>	Fall Time		52			$V_{GS} = -10V$
C <sub>iss</sub>	Input Capacitance		510			$V_{GS} = 0V$
Coss	Output Capacitance		71		pF	$V_{DS} = -25V$
C <sub>rss</sub>	Reverse Transfer Capacitance		43			f = 1.0MHz

### **Source-Drain Ratings and Characteristics**

	Parameter	Min.	Тур.	Max.	Units	Conditions
Is	Continuous Source Current			4.0		MOSFET symbol
	(Body Diode)		1.3	3 A	showing the	
I <sub>SM</sub>	Pulsed Source Current			24	^	integral reverse
	(Body Diode) ①				'	p-n junction diode.
$V_{SD}$	Diode Forward Voltage	I		-1.2	V	$T_J = 25^{\circ}C$ , $I_S = -1.3A$ , $V_{GS} = 0V$ ②
t <sub>rr</sub>	Reverse Recovery Time		17	26	ns	$T_J = 25^{\circ}C$ , $I_F = -1.3A$
Q <sub>rr</sub>	Reverse Recovery Charge	I	12	18	nC	di/dt = -100A/µs ②

#### Notes:

- ① Repetitive rating; pulse width limited by max. junction temperature.
- ② Pulse width  $\leq$  400 $\mu$ s; duty cycle  $\leq$  2%.



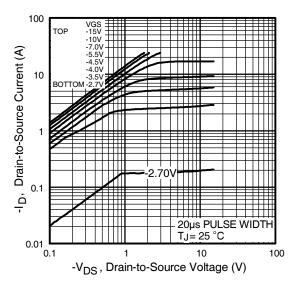


Fig 1. Typical Output Characteristics

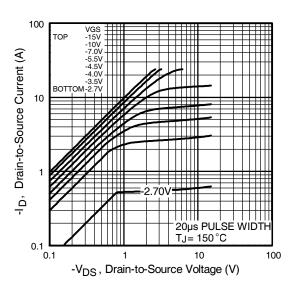


Fig 2. Typical Output Characteristics

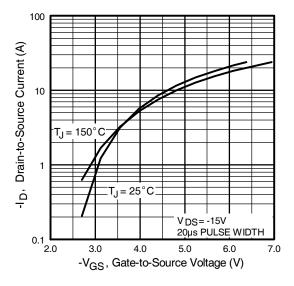
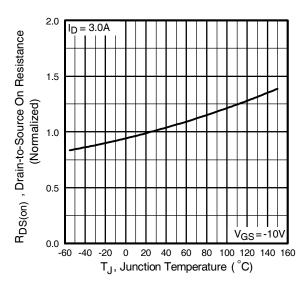
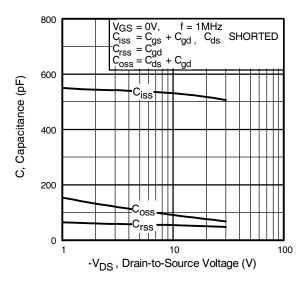


Fig 3. Typical Transfer Characteristics

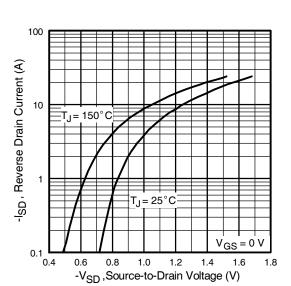


**Fig 4.** Normalized On-Resistance Vs. Temperature

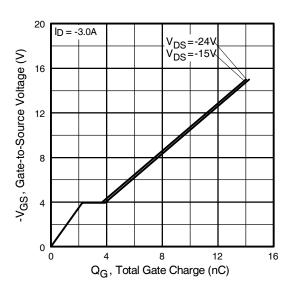




**Fig 5.** Typical Capacitance Vs. Drain-to-Source Voltage



**Fig 7.** Typical Source-Drain Diode Forward Voltage



**Fig 6.** Typical Gate Charge Vs. Gate-to-Source Voltage

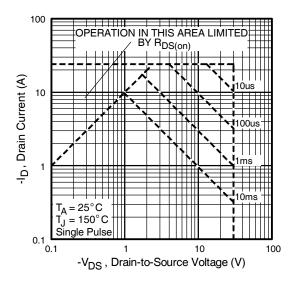
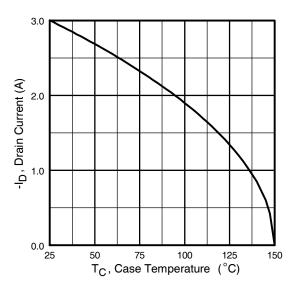


Fig 8. Maximum Safe Operating Area





**Fig 9.** Maximum Drain Current Vs. Case Temperature

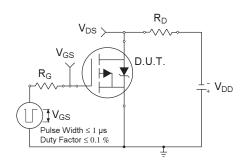


Fig 10a. Switching Time Test Circuit

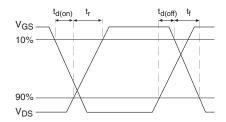


Fig 10b. Switching Time Waveforms

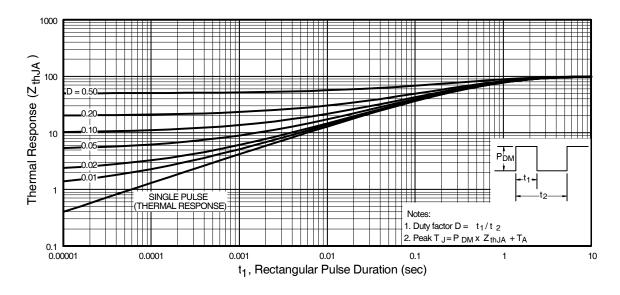
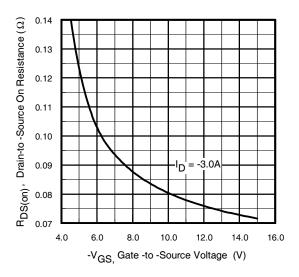
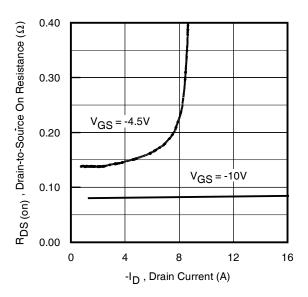


Fig 11. Maximum Effective Transient Thermal Impedance, Junction-to-Ambient







**Fig 11.** Typical On-Resistance Vs. Gate Voltage

**Fig 12.** Typical On-Resistance Vs. Drain Current

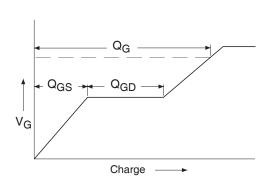


Fig 13a. Basic Gate Charge Waveform

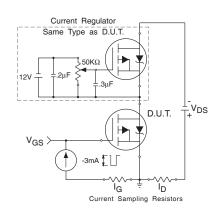


Fig 13b. Gate Charge Test Circuit



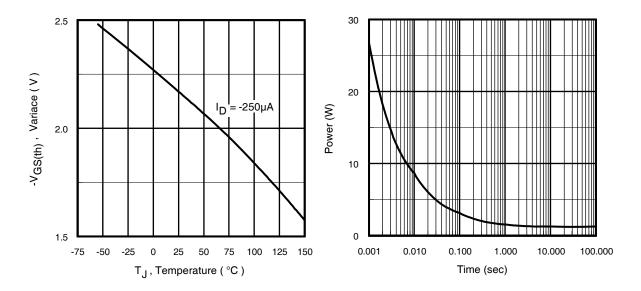


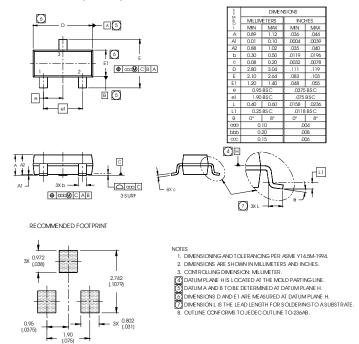
Fig 14. Threshold Voltage Vs. Temperature

Fig 15. Typical Power Vs. Time



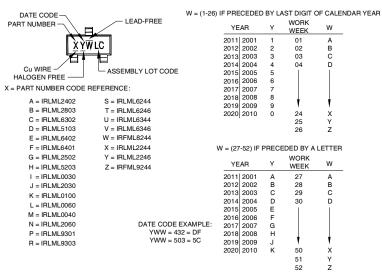
### Micro3 (SOT-23) (Lead-Free) Package Outline

Dimensions are shown in millimeters (inches)



## Micro3 (SOT-23 / TO-236AB) Part Marking Information

Notes: This part marking information applies to devices produced after 02/26/2001

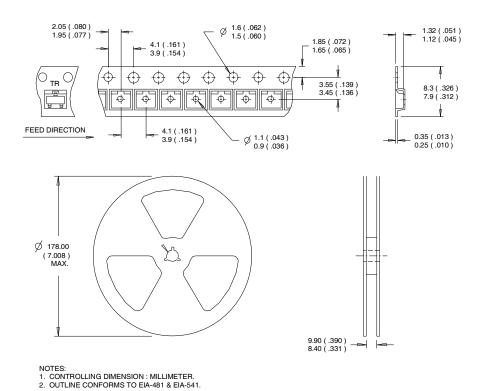


Note: For the most current drawing please refer to IR website at http://www.irf.com/package



# Micro3™ Tape & Reel Information

Dimensions are shown in millimeters (inches)



Note: For the most current drawing please refer to IR website at http://www.irf.com/package



Qualification information<sup>†</sup>

Qualification level	Consumer (per JEDEC JESD47F <sup>††</sup> guidelines)			
Moisture Sensitivity Level	Micro3 <sup>™</sup> (SOT-23)	MSL1 (per JEDEC J-STD-020D <sup>††</sup> )		
RoHS compliant	Yes			

- † Qualification standards can be found at International Rectifier's web site: http://www.irf.com/product-info/reliability
- †† Applicable version of JEDEC standard at the time of product release

### **Revision History**

Date	Comment			
	Updated data sheet with new IR corporate template.			
4/28/2014	Updated package outline & part marking on page 8.			
4/20/2014	Added Qualification table -Qual level "Consumer" on page 10.			
	Added bullet point in the Benefits "RoHS Compliant, Halogen -Free" on page 1.			



IR WORLD HEADQUARTERS: 101 N. Sepulveda Blvd., El Segundo, California 90245, USA To contact International Rectifier, please visit <a href="http://www.irf.com/whoto-call/">http://www.irf.com/whoto-call/</a>

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