



(0.635 mm) .025" PITCH • QMSS/QFSS SERIES

NRZ  
14  
Gbps



**QMSS**  
Board Mates:  
QFSS

**QFSS**  
Board Mates:  
QMSS

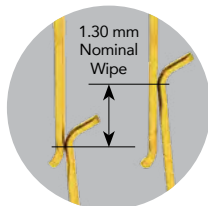
**QMSS/QFSS**  
Standoffs:  
SO

## SPECIFICATIONS

**Insulator Material:**  
Liquid Crystal Polymer  
**Terminal, Ground Plane & Shield Material:**  
Phosphor Bronze  
**Plating:**  
Au over 50  $\mu$ " (1.27  $\mu$ m) Ni  
(Tin on Ground Plane Tail)  
**Operating Temp Range:**  
-55 °C to +125 °C  
**Voltage Rating:**  
300 VAC when mated

## PROCESSING

**Lead-Free Solderable:**  
Yes  
**SMT Lead Coplanarity:**  
(0.10 mm) .004" max  
(026-078)  
**Board Stacking:**  
For applications requiring more than two connectors contact [ipg@samtec.com](mailto:ipg@samtec.com)



QMSS	PINS PER ROW NO. OF PAIRS	06.75	PLATING OPTION	TYPE	A	OTHER OPTION
------	------------------------------	-------	-------------------	------	---	-----------------

**-026, -052, -078**  
(52 total pins per bank / 40 signals  
+ 12 grounds to shield = -D)

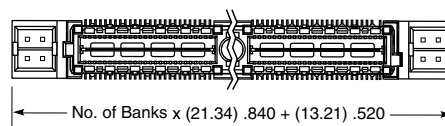
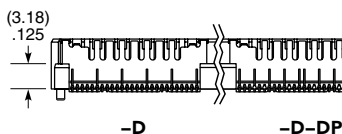
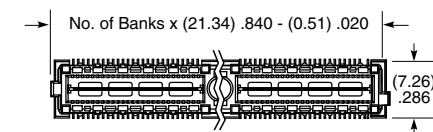
**-016, -032, -048**  
(16 pairs per bank = -D-DP)

**-L**  
= 10  $\mu$ " (0.25  $\mu$ m)  
gold on contact,  
Matte Tin on tail

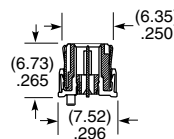
**-D**  
= Single-  
Ended  
**-D-DP**  
= Differential  
Pair

**-K**  
= (5.50 mm)  
.217" DIA  
Polyimide  
film Pick &  
Place Pad  
(N/A with -  
PC4)

**-PC4**  
= 4 Power  
Pins/End  
(N/A with -A)



**-PC4 OPTION**



## PACKAGING OPTION

Tape & Reel  
Options (-TR &  
-FR) available.  
Visit series web  
page and view  
engineering prints  
for complete  
specifications.

View complete specifications at: [samtec.com?QMSS](http://samtec.com?QMSS)

QFSS	PINS PER ROW NO. OF PAIRS	04.25	PLATING OPTION	TYPE	A	OTHER OPTION
------	------------------------------	-------	-------------------	------	---	-----------------

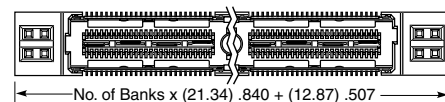
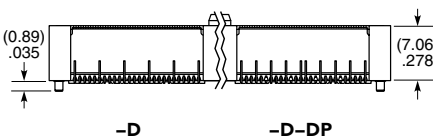
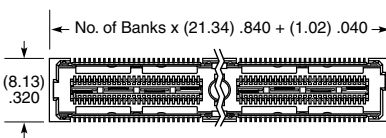
**-026, -052, -078**  
(52 total pins per bank / 40 signals  
+ 12 grounds to shield = -D)

**-016, -032, -048**  
(16 pairs per bank = -D-DP)

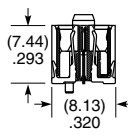
**-L**  
= 10  $\mu$ " (0.25  $\mu$ m)  
gold on contact,  
Matte Tin on tail

**-D**  
= Single-  
Ended  
**-D-DP**  
= Differential  
Pair

**-PC4**  
= 4 Power  
Pins/End  
(N/A with -A)



**-PC4 OPTION**



## PACKAGING OPTION

Tape & Reel  
Options (-TR &  
-FR) available.  
Visit series web  
page and view  
engineering prints  
for complete  
specifications.

**Note:**  
Some lengths, styles and  
options are non-standard,  
non-returnable.

View complete specifications at: [samtec.com?QFSS](http://samtec.com?QFSS)

# Mouser Electronics

Authorized Distributor

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

## Samtec:

[QMSS-026-06.75-H-D-A](#) [QMSS-016-06.75-L-D-DP-PC4](#) [QMSS-078-06.75-L-D-PC4](#) [QMSS-026-06.75-L-D-PC4](#)  
[QMSS-052-06.75-L-D-A](#) [QMSS-026-06.75-L-D-A](#) [QMSS-016-06.75-L-D-DP-A](#) [QMSS-032-06.75-L-D-DP-PC4](#) [QMSS-](#)  
[052-06.75-H-D-PC4](#) [QMSS-032-06.75-H-D-DP-A-GP](#) [QMSS-016-01-H-D-DP-EM2](#) [QMSS-104-01-L-D-EM2](#) [QMSS-](#)  
[016-01-L-D-DP-EM2](#) [QMSS-032-01-L-D-DP-EM2](#) [QMSS-078-01-L-D-EM2-PC4](#) [QMSS-048-01-H-D-DP-EM2-PC4](#)  
[QMSS-104-01-H-D-EM2](#) [QMSS-016-01-L-D-DP-EM2-PC4](#) [QMSS-048-01-L-D-DP-EM2](#) [QMSS-048-01-H-D-DP-EM2](#)  
[QMSS-026-01-L-D-EM2](#) [QMSS-026-01-L-D-EM2-PC4](#) [QMSS-052-01-L-D-EM2-PC8](#) [QMSS-016-01-H-D-DP-EM2-PC8](#)  
[QMSS-052-01-H-D-EM2](#) [QMSS-078-01-H-D-EM2](#) [QMSS-078-01-L-D-EM2](#) [QMSS-026-01-H-D-EM2](#) [QMSS-052-01-](#)  
[L-D-EM2](#) [QMSS-052-01-L-D-EM2-PC4](#) [QMSS-064-01-H-D-DP-EM2](#) [QMSS-016-01-H-D-DP-EM2-PC4](#) [QMSS-032-01-](#)  
[H-D-DP-EM2-PC4](#) [QMSS-048-01-L-D-DP-EM2-PC4](#) [QMSS-052-01-H-D-EM2-PC4](#) [QMSS-064-01-L-D-DP-EM2](#)  
[QMSS-078-01-H-D-EM2-PC4](#) [QMSS-032-01-H-D-DP-EM2-PC8](#) [QMSS-032-01-L-D-DP-EM2-PC4](#) [QMSS-032-01-L-D-](#)  
[DP-EM2-PC8](#) [QMSS-032-01-L-D-EM2-PC8](#) [QMSS-026-01-H-D-EM2-PC4](#) [QMSS-032-01-H-D-DP-EM2](#) [QMSS-016-](#)  
[06.75-L-D-DP](#) [QMSS-026-06.75-L-D-A-K-TR](#) [QMSS-026-06.75-L-D-A-TR](#) [QMSS-052-01-L-D-EM2-PC4-TR](#) [QMSS-](#)  
[078-01-H-D-EM2-PC8](#) [QMSS-052-06.75-L-D-A-K](#) [QMSS-032-06.75-H-D-DP-PC4](#) [QMSS-048-06.75-H-D-DP-A](#)  
[QMSS-052-06.75-H-D-A](#) [QMSS-052-06.75-H-D-A-K](#) [QMSS-078-06.75-L-D](#) [QMSS-016-06.75-H-D-DP-A](#) [QMSS-048-](#)  
[06.75-L-D-DP-A-K-TR](#) [QMSS-048-06.75-L-D-DP-PC4](#) [QMSS-064-06.75-L-D-DP-A](#) [QMSS-078-06.75-H-D-A](#) [QMSS-](#)  
[016-06.75-L-D-DP-A-K-TR](#) [QMSS-048-06.75-H-D-DP-PC4](#) [QMSS-052-06.75-H-D-A-GP](#) [QMSS-078-06.75-L-D-A](#)  
[QMSS-026-06.75-H-D-A-K-TR](#) [QMSS-032-06.75-L-D-DP-PT8](#) [QMSS-048-01-H-D-DP-EM2-PC8](#) [QMSS-052-06.75-H-](#)  
[D-A-TR](#) [QMSS-078-06.75-L-D-A-K](#) [QMSS-078-06.75-L-D-A-K-TR](#) [QMSS-016-06.75-L-D-DP-A-TR](#) [QMSS-016-06.75-](#)  
[L-D-DP-PC8](#) [QMSS-026-06.75-H-D-A-K](#) [QMSS-048-01-L-D-DP-EM2-PC8](#) [QMSS-052-06.75-L-D-A-GP](#) [QMSS-052-](#)  
[06.75-L-D-A-K-TR](#) [QMSS-078-06.75-L-D-PT4](#) [QMSS-016-06.75-L-D-DP-A-K](#) [QMSS-032-06.75-L-D-DP-A](#) [QMSS-064-](#)  
[06.75-H-D-DP-A](#) [QMSS-078-06.75-H-D-A-GP](#) [QMSS-016-06.75-H-D-DP-PC4](#) [QMSS-026-06.75-L-D-PC8](#) [QMSS-026-](#)  
[06.75-L-D-PT4](#) [QMSS-048-06.75-L-D-DP-A-K](#) [QMSS-078-01-L-D-EM2-PC8](#) [QMSS-026-06.75-L-D-A-GP](#) [QMSS-032-](#)  
[06.75-H-D-DP-A](#) [QMSS-052-06.75-L-D-PC4](#) [QMSS-052-06.75-L-D-PC8](#) [QMSS-104-06.75-L-D-A](#) [QMSS-026-06.75-L-](#)  
[D-A-K](#) [QMSS-048-06.75-L-D-DP](#) [QMSS-052-06.75-L-D-PC4-TR](#) [QMSS-104-06.75-L-D-A-K](#) [QMSS-026-06.75-H-D-](#)  
[PC4](#) [QMSS-032-06.75-L-D-DP](#) [QMSS-048-06.75-L-D-DP-A](#) [QMSS-052-06.75-H-D](#) [QMSS-078-06.75-H-D-PC4](#)  
[QMSS-016-06.75-L-D-DP-A-GP](#)