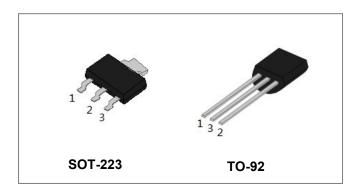
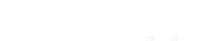






# SST131 Series 1A TRIACs





**Circuit Diagram** 

### **Description**

With low holding and latching current, SST131 series triacs are especially recommended for use on middle and small resistance type power load.

### **Maximum Ratings:**

Characteristics	Symbol	Condition	Max.	Units
Storage junction temperature range	T <sub>stg</sub>	-	-40 - 150	$^{\circ}$ C
Operating junction temperature range	Tj	-	-40 - 125	°C
Repetitive peak off-state voltage (T <sub>j</sub> =25℃)	$V_{DRM}$	-	600	V
Repetitive peak reverse voltage (Tj=25 $^{\circ}\text{C}$ )	$V_{RRM}$	-	600	V
Non repetitive surge peak off-state voltage	V <sub>DSM</sub>	-	V <sub>DRM</sub> +100	V
Non repetitive peak reverse voltage	V <sub>RSM</sub>	-	V <sub>RRM</sub> +100	V
RMS on-state current	I <sub>(TRMS)</sub>	TO-92(T <sub>C</sub> =50°C) SOT-223(T <sub>C</sub> =70°C)	1	А
Non repetitive surge peak on-state current (full cycle, F=50Hz)	I <sub>TSM</sub>	-	16	А
I <sup>2</sup> t value for fusing (tp=10ms)	l²t	-	1.28	A <sup>2</sup> s
Critical rate of rise of on-state current $(I_G = 2 \times I_{GT})$	dl/dt	-	20	A/us
Peak gate current	I <sub>GM</sub>	-	2	А
Average gate power dissipation	P <sub>GM</sub>	-	0.5	W
Peak gate power	P <sub>G(AV)</sub>	-	5	W

<sup>•</sup> http://www.smc-diodes.com - sales@ smc-diodes.com •







# **Electrical Characteristics**(Tj=25℃ unless otherwise specified)

Cumbal	Took Condition	Overdrent		Value		Unit	
Symbol	Test Condition	Quadrant		Т	D	Ollit	
1		I - II -III	MAX	5	5	mA	
I <sub>GT</sub> V <sub>I</sub>	V <sub>D</sub> =12V R <sub>L</sub> =33Ω	IV		5	10		
V <sub>GT</sub>		ALL	MAX	1.3		V	
$V_{GD}$	$V_D=V_{DRM}T_j=125^{\circ}C$ RL=3.3K $\Omega$ ALL		MIN	0.2		V	
		I -III	MAX	5	5	mA	
l L	I <sub>G</sub> =1.2I <sub>GT</sub>	II -IV	IVIAA	10	20		
I <sub>H</sub>	I <sub>T</sub> =200mA		MAX	5	7	mA	
dV/dt	V <sub>D</sub> =2/3V <sub>DRM</sub> Gate Open T <sub>j</sub> =125℃		MIN	15	50	V/µs	

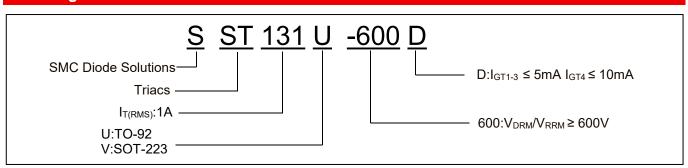
# Static Characteristics

Symbol	Condition	Max.	Units
$V_{TM}$	I <sub>T</sub> =1.4A tp=380μs,Tj=25℃	1.5	V
I <sub>DRM</sub>	V <sub>D</sub> =V <sub>DRM</sub> V <sub>R</sub> =V <sub>RRM</sub> , Tj=25℃	5	μA
I <sub>RRM</sub>	V <sub>D</sub> =V <sub>DRM</sub> V <sub>R</sub> =V <sub>RRM</sub> , Tj=125°C	500	μA

#### **Thermal Resistances**

Symbol	Condition		Value	Units	
Rth(j-c)	Junction to case(AC)	TO-92	60	°C/W	
		SOT-223	31	C/VV	

#### **Ordering Information**



Device	Package	Shipping
SST131U-600D	TO-92	2000pcs/ reel
SST131U-600DTR	TO-92	2000pcs/ reel
SST131V-600D	SOT-223	8000pcs/ reel
SST131V-600DTR	SOT-223	8000pcs/ reel

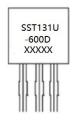
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## **Marking Diagram**





Where XXXXX is YYWWL

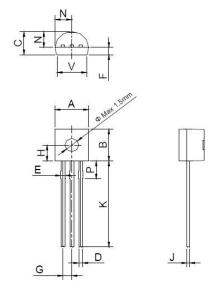
 SST131U/V-600D
 = Part name

 YY
 = Year

 WW
 = Week

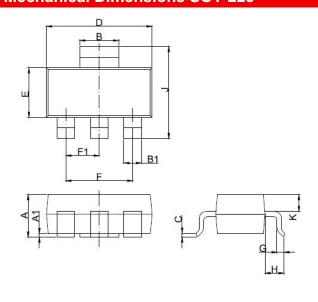
 L
 = Lot Number

### **Mechanical Dimensions TO-92**



SYMBO	Millimeters			Inches		
L	Min.	Тур.	Max.	Min.	Тур.	Max.
Α	4.45	-	5.20	0.175	-	0.205
В	4.32	-	5.33	0.170	-	0.210
С	3.18	-	4.19	0.125	-	0.165
D	0.407	-	0.533	0.016	-	0.021
E	0.60	-	0.80	0.024	-	0.031
F	-	1.1	-	-	0.043	-
G	-	1.27	-	-	0.050	-
Н	-	2.30	-	-	0.091	-
J	0.36	-	0.50	0.014	-	0.020
K	12.70	-	15.0	0.500	-	0.591
N	2.04	-	2.66	0.080	-	0.105
Р	1.86	-	2.06	0.073	-	0.081
V	-	-	4.3	-	-	0.169

#### **Mechanical Dimensions SOT-223**



SYMBO	Mil	limeters	<b>3</b>	Inches		
L	Min.	Тур.	Max.	Min.	Тур.	Max.
Α	1.5	1.6	1.8	0.059	0.063	0.071
A1	0.01	0.06	0.10	0.001	0.002	0.004
В	2.9	3.0	3.1	0.114	0.118	0.122
B1	0.6	0.7	0.8	0.024	0.028	0.031
С	0.22	0.26	0.32	0.009	0.010	0.013
D	6.3	6.5	6.7	0.248	0.256	0.264
E	3.3	3.5	3.7	0.130	0.138	0.146
F		4.6			0.181	
F1		2.3			0.091	
G	0.7	0.9	1.1	0.028	0.035	0.043
Н	1.50	1.5	2.0	0.059	0.069	0.079
J	6.7	7.0	7.3	0.264	0.276	0.287
K	0.8	0.9	1.0	0.031	0.035	0.039

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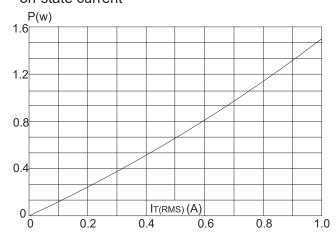




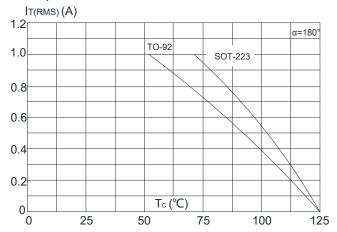


#### **Ratings and Characteristics Curves**

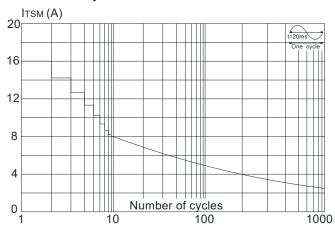
**FIG.1:** Maximum power dissipation versus RMS on-state current



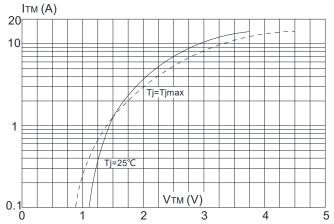
**FIG.2:** RMS on-state current versus case temperature



**FIG.3:** Surge peak on-state current versus number of cycles



**FIG.4:** On-state characteristics (maximum values)



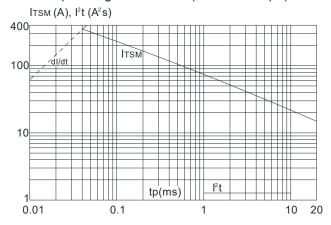
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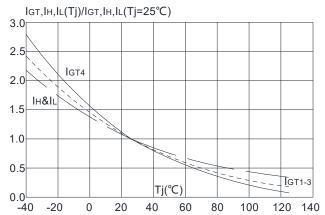




**FIG.5:** Non-repetitive surge peak on-state current for a sinusoidal pulse with width tp<20ms and corresponding value of lt (dl/dt < 20A/µs)



**FIG.6:** Relative variations of gate trigger current, holding current and latching current versus junction temperature









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