TLN PulseCap™ Series

High Capacitance Tantalum Solid Electrolytic Chip Capacitors
Undertab Series

FEATURES

- Large Case Size for Maximum Capacitance
- 3x Reflow 260°C Compatible
- 100% Surge Current Tested
- Low Profile Solution
- Consumer Applications
  (e.g. PCMCIA/USB Wireless Express Cards etc.)
- CV Range: 1000-3300µF / 4-10V
- 2 Case Sizes Available

APPLICATIONS

- Data Transfer Modems
- SSD Backup Circuits

LEAD-FREE
LEAD-FREE COMPATIBLE COMPONENT

RoHS COMPLIANT

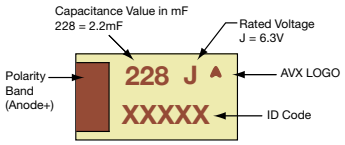
CASE DIMENSIONS:

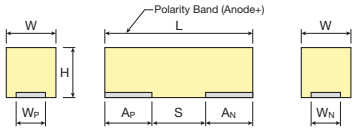
millimeters (inches)

Code	EIA Code	EIA Metric	L±0.20 (0.008)	W+0.20 (0.008) -0.10 (0.004)	H max.	W_P±0.10 (0.004)	W_N±0.10 (0.004)	A_P±0.10 (0.004)	A_N±0.10 (0.004)	S Min.
4	2924	7361-20	7.30 (0.287)	6.10 (0.240)	2.00 (0.079)	4.75 (0.187)	4.75 (0.187)	2.00 (0.079)	3.20 (0.126)	2.10 (0.083)
6	5831	14878-20	14.80 (0.583)	7.80 (0.307)	2.00 (0.079)	5.50 (0.217)	5.50 (0.217)	2.45 (0.096)	2.45 (0.096)	9.90 (0.390)

MARKING

4, 6 CASE





HOW TO ORDER

TLN	6	228	M	006	R	0055
Type	**Case Size** See table above	**Capacitance Code** pF code: 1st two digits represent significant figures, 3rd digit represents multiplier	**Tolerance** M = ±20%	**Rated DC Voltage** 004 = 4Vdc 006 = 6.3Vdc 010 = 10Vdc	**Packaging** R = Pure Tin 7" Reel	**ESR in mΩ**

TECHNICAL SPECIFICATIONS

Technical Data:		All technical data relate to an ambient temperature of +25°C			
Capacitance Range:		1000 µF to 3300 µF			
Capacitance Tolerance:		±20%			
Leakage Current DCL:		0.01CV			
Rated Voltage (V_R)	-55°C ≤ +40°C:	4	6.3	10	
Category Voltage (V_C)	at 85°C:	2	3.2	5	
Category Voltage (V_C)	at 125°C:	0.8	1.3	2	
Temperature Range:		-55°C to +125°C with category voltage			
Reliability:		0.2% per 1000 hours at 85°C, 0.5xV_R with 0.1Ω/V series impedance with 60% confidence level			

CAPACITANCE AND RATED VOLTAGE RANGE (LETTER DENOTES CASE SIZE)

Capacitance		Voltage Rating DC (VR) to 85°C		
µF	Code	4V (G)	6.3V (J)	10V (A)
680	687			
1000	108			4(100)/6(55)
1500	158		4(100)	6(55)
2200	228		6(55)	
3300	338	6(55)		

Released ratings (ESR ratings in mOhms in parentheses)
Note: Voltage ratings are minimum values. AVX reserves the right to supply higher voltage ratings in the same case size, to the same reliability standards.

RATINGS & PART NUMBER REFERENCE

AVX Part No.	Case Size	Capacitance (µF)	Rated Voltage (V)	Rated Temperature (°C)	Category Voltage (V)	Category Temperature (°C)	DCL Max. (µA)	ESR Max. @ 100kHz (mΩ)	100kHz RMS Current (mA)			MSL
									25°C	85°C	125°C	
4 Volt @ 40°C												
TLN6338M004#0055	6	3300	4	40	0.8	125	132	55	2045	1840	818	3
6.3 Volt @ 40°C												
TLN4158M006#0100	4	1500	6.3	40	1.3	125	90	100	1285	1156	514	3
TLN6228M006#0055	6	2200	6.3	40	1.3	125	132	55	2045	1840	818	3
10 Volt @ 40°C												
TLN4108M010#0100	4	1000	10	40	2	125	100	100	1285	1156	514	3
TLN6108M010#0055	6	1000	10	40	2	125	100	55	2045	1840	818	3
TLN6158M010#0055	6	1500	10	40	2	125	150	55	2045	1840	818	3

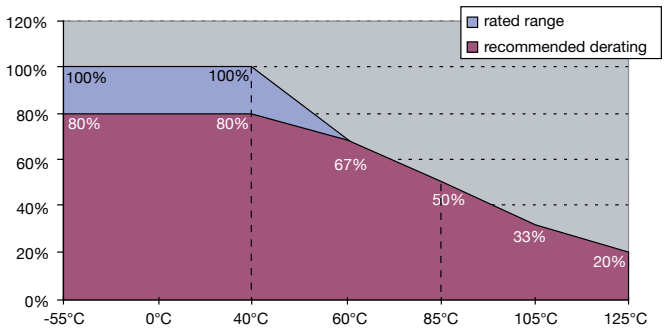
Moisture Sensitivity Level (MSL) is defined according to J-STD-020.
All technical data relates to an ambient temperature of +25°C. Capacitance and DF are measured at 120Hz, 0.5V RMS with a maximum DC bias of 2.2 volts.
DCL is measured at rated voltage after 5 minutes.
ESR allowed to move up to 1.25 times catalogue limit post mounting
DCL allowed to move up to 2.00 times catalogue limit post mounting
For typical weight and composition see page 259.
NOTE: AVX reserves the right to supply higher voltage ratings or tighter tolerance part in the same case size, to the same reliability standards.

QUALIFICATION TABLE

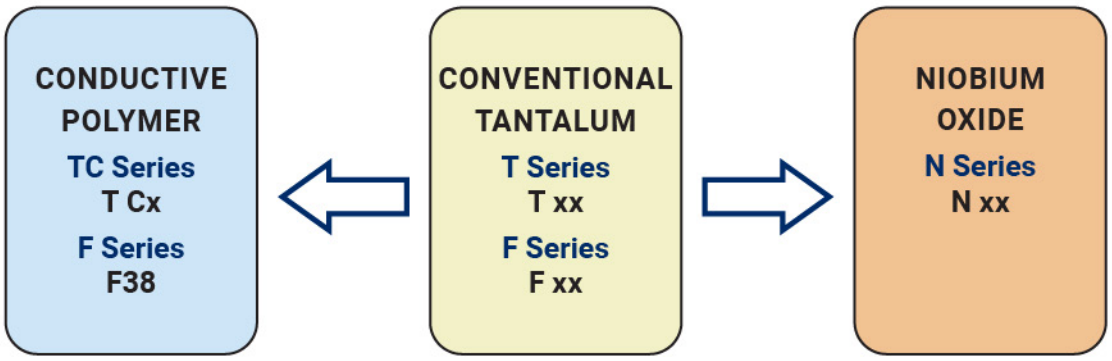
<table>
<tr><th rowspan="2">TEST</th><th colspan="8">TLN PulseCap™ series (Temperature range -55°C to +125°C)</th></tr>
<tr><th colspan="3">Condition</th><th colspan="5">Characteristics</th></tr>
<tr><td>Endurance</td><td colspan="3">Apply rated voltage (Ur) at 40°C and / or category voltage (Uc) at 85°C for 2000 hours through a circuit impedance of ≤0.1Ω/V. Stabilize at room temperature for 1-2 hours before measuring.</td><td>Visual examination</td><td colspan="4">no visible damage</td></tr>
<tr><td></td><td></td><td></td><td>DCL</td><td colspan="4">2 x initial limit</td></tr>
<tr><td></td><td></td><td></td><td>ΔC/C</td><td colspan="4">within +5/-30% of initial value</td></tr>
<tr><td></td><td></td><td></td><td>ESR</td><td colspan="4">1.25 x initial limit</td></tr>
<tr><td>Humidity</td><td colspan="3">Store at 65°C and 90-95% relative humidity for 500 hours, with no applied voltage. Stabilize at room temperature and humidity for 1-2 hours before measuring.</td><td>Visual examination</td><td colspan="4">no visible damage</td></tr>
<tr><td></td><td></td><td></td><td>DCL</td><td colspan="4">2 x initial limit</td></tr>
<tr><td></td><td></td><td></td><td>ΔC/C</td><td colspan="4">within ±10% of initial value</td></tr>
<tr><td></td><td></td><td></td><td>ESR</td><td colspan="4">1.25 x initial limit</td></tr>
<tr><td rowspan="8">Temperature Stability</td><td>Step</td><td>Temperature°C</td><td>Duration(min)</td><td></td><td>+20°C</td><td>-55°C</td><td>+20°C</td><td>+85°C</td><td>+125°C</td><td>+20°C</td></tr>
<tr><td>1</td><td>+20</td><td>15</td><td>DCL</td><td>2 x IL*</td><td>n/a</td><td>2 x IL*</td><td>20 x IL*</td><td>25 x IL*</td><td>2 x IL*</td></tr>
<tr><td>2</td><td>-55</td><td>15</td><td>ΔC/C</td><td>n/a</td><td>+5/-20%</td><td>±10%</td><td>+20/-0%</td><td>+25/-0%</td><td>±10%</td></tr>
<tr><td>3</td><td>+20</td><td>15</td><td>ESR</td><td>1.25xIL*</td><td>2.5 x IL*</td><td>1.25 x IL*</td><td>1.25 x IL*</td><td>1.25 x IL*</td><td>1.25xIL*</td></tr>
<tr><td>4</td><td>+85</td><td>15</td><td colspan="6"></td></tr>
<tr><td>5</td><td>+125</td><td>15</td><td colspan="6"></td></tr>
<tr><td>6</td><td>+20</td><td>15</td><td colspan="6"></td></tr>
<tr><td>Surge Voltage</td><td colspan="3">Apply 1.3x rated voltage (Ur) at 40°Cfor 1000 cycles of duration 6 min (30 sec charge, 5 min 30 sec discharge) through a charge / discharge resistance of 1000Ω</td><td>Visual examination</td><td colspan="4">no visible damage</td></tr>
<tr><td></td><td></td><td></td><td>DCL</td><td colspan="4">2 x initial limit</td></tr>
<tr><td></td><td></td><td></td><td>ΔC/C</td><td colspan="4">within ±5% of initial value</td></tr>
<tr><td></td><td></td><td></td><td>ESR</td><td colspan="4">1.25 x initial limit</td></tr>
<tr><td>Mechanical Shock</td><td colspan="3">MIL-STD-202, Method 213, Condition C</td><td>Visual examination</td><td colspan="4">no visible damage</td></tr>
<tr><td></td><td></td><td></td><td>DCL</td><td colspan="4">initial limit</td></tr>
<tr><td></td><td></td><td></td><td>ΔC/C</td><td colspan="4">within ±5% of initial value</td></tr>
<tr><td></td><td></td><td></td><td>DF</td><td colspan="4">initial limit</td></tr>
<tr><td></td><td></td><td></td><td>ESR</td><td colspan="4">initial limit</td></tr>
<tr><td>Vibration</td><td colspan="3">MIL-STD-202, Method 204, Condition D</td><td>Visual examination</td><td colspan="4">no visible damage</td></tr>
<tr><td></td><td></td><td></td><td>DCL</td><td colspan="4">initial limit</td></tr>
<tr><td></td><td></td><td></td><td>ΔC/C</td><td colspan="4">within ±5% of initial value</td></tr>
<tr><td></td><td></td><td></td><td>DF</td><td colspan="4">initial limit</td></tr>
<tr><td></td><td></td><td></td><td>ESR</td><td colspan="4">initial limit</td></tr>
</table>

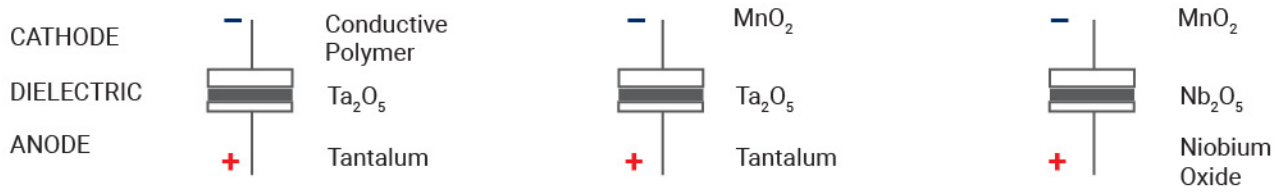
*Initial Limit

Voltage vs Temperature Rating

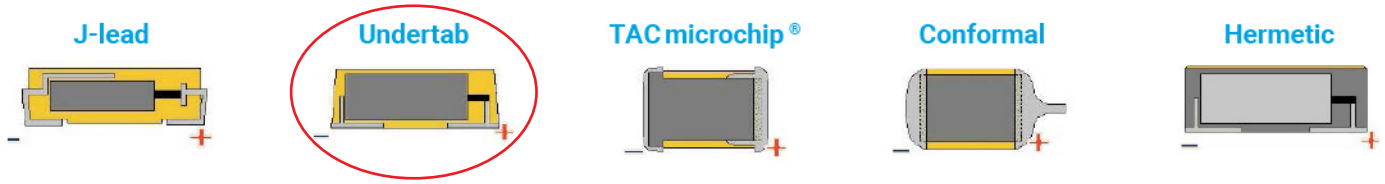


AVX SOLID ELECTROLYTIC CAPACITOR ROADMAP

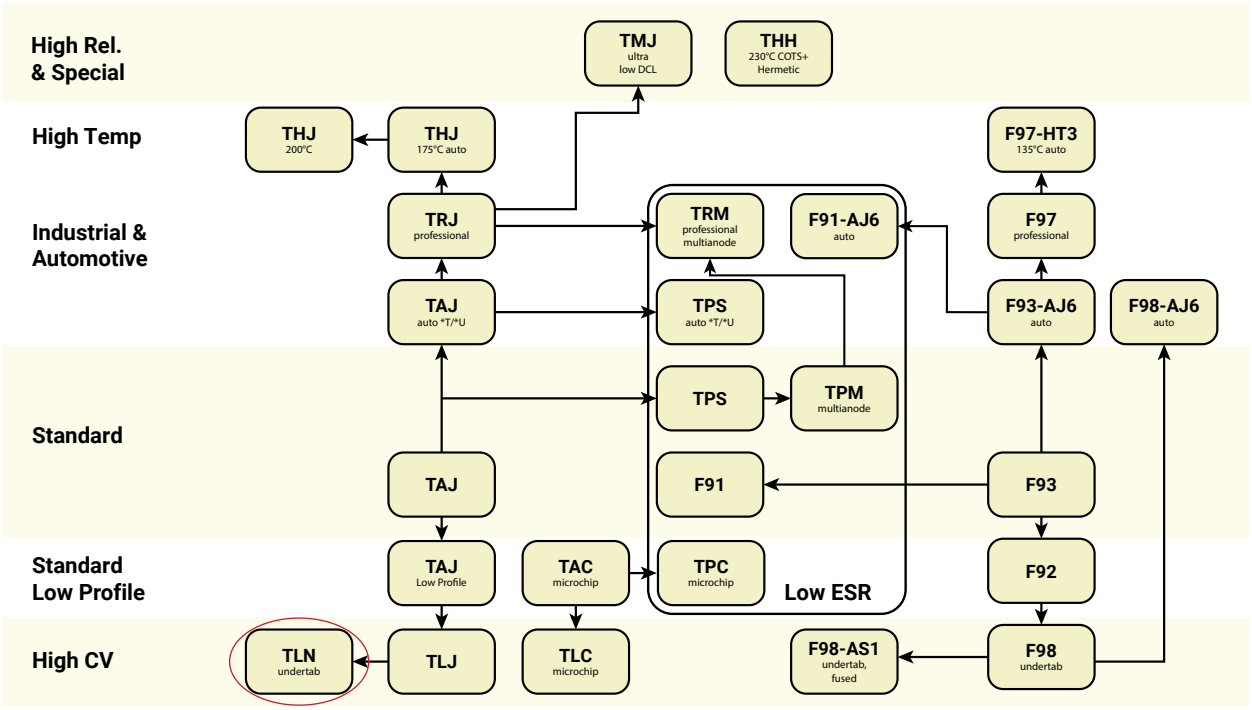




FIVE CAPACITOR CONSTRUCTION STYLES



SERIES LINE UP : CONVENTIONAL SMD MnO_2



Mouser Electronics

Authorized Distributor

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Kyocera AVX:

TLN4158M006R0100 TLN6228M006R0055 TLN6338M004R0055 TLN6108M010R0055 TLN6158M010R0055 TLN4108M010R0100