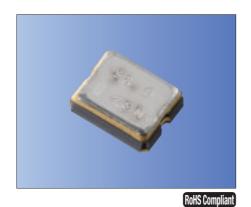


CMOS/ 1.8V/ 2.5×2.0mm



Features

- Miniature ceramic package 2.5 (L) ×2.0 (W) ×0.7 (H) mm (Typ.)
- High Stability Output Frequency $\pm 10 \times 10^{-6}$ (-10 to +70°C) $\pm 15 \times 10^{-6}$ (-40 to +85°C)
- CMOS output
- Supply voltage Vcc=1.8V

Applications

• Wi-Fi, Bluetooth® etc.

Table 1

Freq. Tol.		Operating Temperature	Note		
Code	× 10 ⁻⁶	Range (°C)	Note		
Υ	±10	-10 to +70	With only certain		
K	±20	-40 to +85	frequencies		
L	±15	-40 10 +65	Standard specifications		

How to Order

KC2520C 40.0000 C 1 L E 00 $\overline{}$ $\overline{}$ $\overline{}$ $\overline{}$ $\overline{}$ $\overline{}$ $\overline{}$ $\overline{}$ $\overline{}$ $\overline{}$

- ① Type (2.5×2.0mm SMD)
- 2 Output Frequency
- 3 Output Type (CMOS)
- 4 Supply Voltage (1.8V)
- (5) Frequency Tolerance (See Table 1)
- 6 Symmetry/ INH Function (45/55%, Stand-by)
- (7) Customer Special Model Suffix (STD Specification is "00")

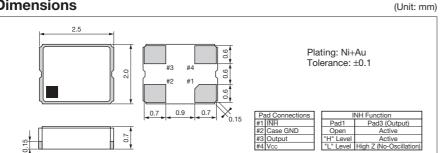
Packaging (Tape & Reel 2000 pcs./ reel)

Specifications

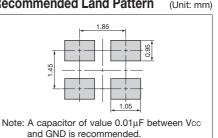
Specifications									
Item	Symbol	Conc	litions	Min.	Max.	Units			
Output Frequency Range	fo			1.5	54	MHz			
	f_tol	Initial tolerance, Operating temperatu	emperature range. Op. Temp.: -40 to +85°C -15 +15		+15	×10 ⁻⁶			
Frequency Tolerance		Rated power supply voltage change, Aging Op. Temp.: -40 to +85°C (1 year @25°C), Shock and vibration Op. Temp.: -10 to +70°C		-20	+20				
				-10	+10				
Storage Temperature Range	T_stg			-55	+125	°C			
Operating Temperature Range	T_use			-10	+70	°C			
Operating remperature hange		-40		+85					
Max. Supply Voltage	_			-0.3	+4.0	V			
Supply Voltage	Vcc			+1.6	+2.0	V			
	Icc	No-load	1.5≤fo<24MHz	_	3	mA			
Current Consumption		No-load	24≤fo≤54MHz	_	3.5				
(Maximum Loaded)		CL≤15pF	1.5≤fo<24MHz	_	4				
			24≤fo≤54MHz	_	4.5				
Stand-by Current	I_std			_	5	μΑ			
Symmetry	SYM	@50% Vcc		45	55	%			
Rise/ Fall Time (10% Vcc to 90% Vcc Maximum Loaded)	tr/ tf			_	4	ns			
Low Level Output Voltage	Vol	IoL=4mA		_	10% Vcc	V			
High Level Output Voltage	igh Level Output Voltage Voн Ioн=-4mA		90% Vcc	_	V				
MOS Load L_CMOS CMOS Output		_	15	pF					
Input Voltage Range	VIN			0	Vcc	V			
Low Level Input Voltage	VIL			_	30% Vcc	V			
High Level Input Voltage	VIH			70% Vcc	_	V			
Disable Time	t_dis			_	100	ns			
Enable Time	t_ena			_	5	ms			
Start-up Time	t_str	@Minimum operating voltage to be 0 sec.		_	10	ms			
1 Sigma Jitter	J Sigma	Measured with Wavecrest SIA-3000			8	ps			
Peak to Peak Jitter	JPK-PK			_	80	ps			

Note: All electrical characteristics are defined at the maximum load and operating temperature range. Please contact us for inquiry about operating temperature range, available frequencies and other conditions.

Dimensions



Recommended Land Pattern



Mouser Electronics

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