

SIPMOS® Small-Signal-Transistor

Features

- N-channel
- Depletion mode
- dv/dt rated
- \bullet Available with $V_{\rm GS(th)}$ indicator on reel
- Pb-free lead-plating; RoHS compliant
- ° Halogen free according to IEC61249-2-21
- ° Qualified according to AEC Q101



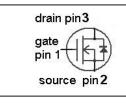




Product Summary

V _{DS}	250	V
R _{DS(on),max}	30	Ω
I _{DSS,min}	0.03	Α

PG-SOT-23





Туре	Package	Tape and Reel Information	Marking	Pb-free
BSS139	PG-SOT-23	H6327: 3000 pcs/ree	STs	Yes
BSS139	PG-SOT-23	H6906: 3000 pcs/reel sorted in V _{S(th)} bands ¹⁾	STs	Yes

Maximum ratings, at T_i =25 °C, unless otherwise specified

Parameter	Symbol	Conditions	Value	Unit
Continuous drain current	I _D	T _A =25 °C	0.10	А
_		T _A =70 °C	0.08	
Pulsed drain current	I _{D,pulse}	T _A =25 °C	0.4	
Reverse diode dv/dt	dv/dt	$I_{\rm D}$ =0.1 A, $V_{\rm DS}$ =200 V, d <i>i</i> /d <i>t</i> =200 A/ μ s, $T_{\rm j,max}$ =150 °C	6	kV/µs
Gate source voltage	V_{GS}		±20	V
ESD class (JESD22-A114-HBM)			0 (<250V)	
Power dissipation	P _{tot}	<i>Т</i> Д=25 °С	0.36	W
Operating and storage temperature	$T_{\rm j}$, $T_{\rm stg}$		-55 150	°C
IEC climatic category; DIN IEC 68-1			55/150/56	

¹⁾ see table on next page and diagram 11





						SS13
Parameter	Symbol	Conditions	Values			Unit
			min.	typ.	max.	
Thermal characteristics						
Thermal resistance, junction - ambient	$R_{ m thJA}$	minimal footprint	-	-	350	K/W
Electrical characteristics, at T_j =25	5 °C, unless	otherwise specified				
Static characteristics						
Drain-source breakdown voltage	V _{(BR)DSS}	V _{GS} =-3 V, I _D =250 μA	250	-	-	V
Gate threshold voltage	V _{GS(th)}	V _{DS} =3 V, I _D =56 μA	-2.1	-1.4	-1	
Drain-source cutoff current	I _{D(off)}	V _{DS} =250 V, V _{GS} =-3 V, T _j =25 °C	-	-	0.1	μA
		V _{DS} =250 V, V _{GS} =-3 V, T _j =125 °C	-	-	10	
Gate-source leakage current	I _{GSS}	V _{GS} =20 V, V _{DS} =0 V	-	-	10	nA
On-state drain current	I _{DSS}	V _{GS} =0 V, V _{DS} =10 V	30	-	-	mA
Drain-source on-state resistance	R _{DS(on)}	V _{GS} =0 V, I _D =15 mA	-	12.5	30	Ω
		V _{GS} =10 V,I _D =0.1 mA	-	7.8	14	
Transconductance	g fs	$ V_{\rm DS} > 2 I_{\rm D} R_{\rm DS(on)max},$ $I_{\rm D} = 0.08 \text{ A}$	0.060	0.13	-	S
Threshold voltage $V_{\rm GS(th)}$ sorted i	n bands ²⁾					•
J	$V_{GS(th)}$	V _{DS} =3 V, I _D =56 μA	-1.2	-	-1	V
К			-1.35	-	-1.15	
L			-1.5	-	-1.3	
M			-1.65	-	-1.45	
N			-1.8	-	-1.6	1

²⁾ Each reel contains transistors out of one band whose identifying letter is printed on the reel label. A specific band cannot be ordered separately.



Parameter	Symbol	ol Conditions	Values			Unit
			min.	typ.	max.	
Dynamic characteristics						
Input capacitance	C iss	V _{GS} =-3 V, V _{DS} =25 V, f=1 MHz	-	60	76	pF
Output capacitance	C oss		-	6.7	8.4	
Reverse transfer capacitance	C _{rss}		-	2.6	3.3	
Turn-on delay time	t _{d(on)}	$V_{\rm DD}$ =125 V, $V_{\rm GS}$ =-35 V, $I_{\rm D}$ =0.04 A, $R_{\rm G}$ =6 Ω	-	5.8	8.7	ns
Rise time	t _r		-	5.4	8.1	
Turn-off delay time	t _{d(off)}		-	29	43	
Fall time	t _f		-	182	273	
Gate Charge Characteristics	_		Ī	T	T	
Gate to source charge	Q _{gs}	$V_{\rm DD}$ =200 V, $I_{\rm D}$ =0.04 A, $V_{\rm GS}$ =-3 to 5 V	-	0.14	0.21	nC
Gate to drain charge	Q_{gd}		-	1.3	2.0	
Gate charge total	Q _g		-	2.3	3.5	
Gate plateau voltage	V _{plateau}		-	-0.28	-	V
Reverse Diode						
Diode continous forward current	Is	−7 _A =25 °C	-	-	0.10	Α
Diode pulse current	I _{S,pulse}		-	-	0.4	1
Diode forward voltage	V _{SD}	V _{GS} =-3 V, I _F =0.1 A, T _j =25 °C	-	0.81	1.2	V
Reverse recovery time	t rr	V_R =50 V, I_F =0.04 A, di_F/dt =100 A/ μ s	-	8.6	12.9	ns
Reverse recovery charge	Q _{rr}		-	2.1	3.1	nC

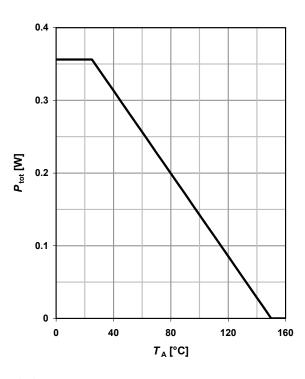


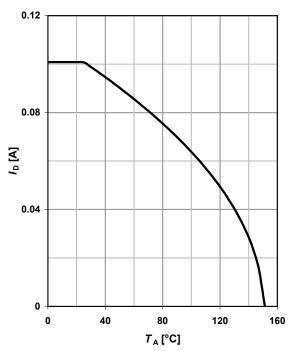
1 Power dissipation

$$P_{\text{tot}}$$
=f(T_{A})

2 Drain current

$$I_D = f(T_A); V_{GS} \ge 10 \text{ V}$$

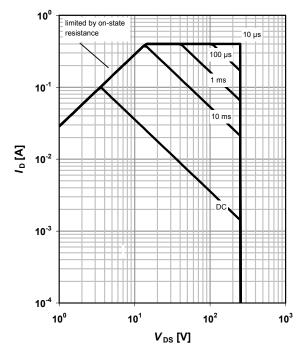




3 Safe operating area

$$I_D = f(V_{DS}); T_A = 25 °C; D = 0$$

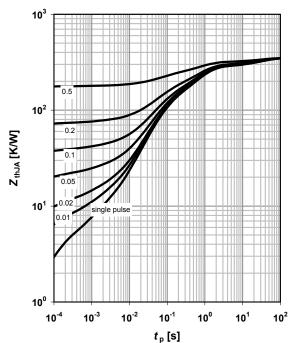
parameter: $t_{\rm p}$



4 Max. transient thermal impedance

$$Z_{thJA}$$
=f(t_p)

parameter: $D = t_p/T$

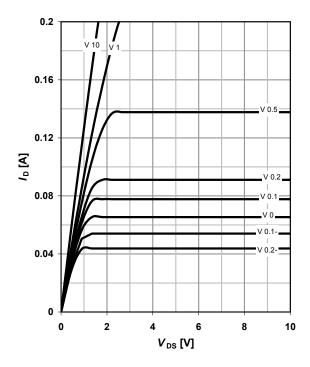




5 Typ. output characteristics

 $I_D = f(V_{DS}); T_j = 25 °C$

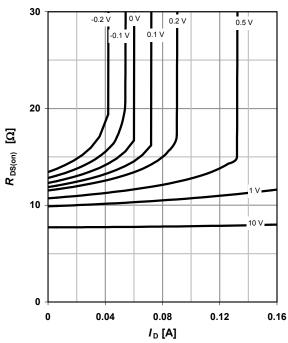
parameter: $V_{\rm GS}$



6 Typ. drain-source on resistance

 $R_{DS(on)}$ =f(I_D); T_j =25 °C

parameter: V_{GS}

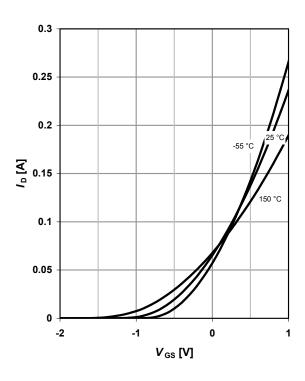


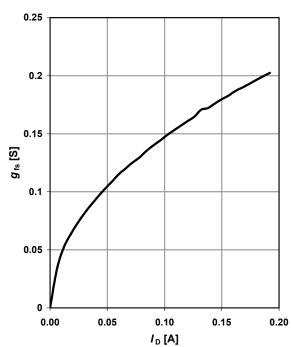
7 Typ. transfer characteristics

 $I_D = f(V_{GS}); |V_{DS}| > 2|I_D|R_{DS(on)max}$



$$g_{fs}$$
=f(I_D); T_j =25 °C

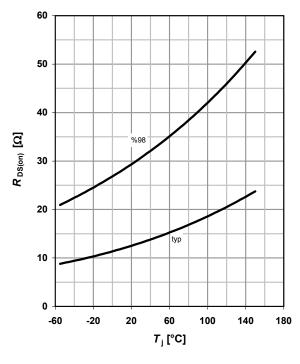






9 Drain-source on-state resistance

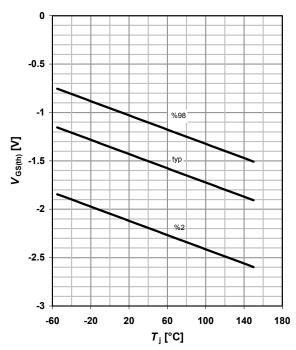
$$R_{DS(on)}$$
=f(T_{j}); I_{D} =0.015 A; V_{GS} =0 V



10 Typ. gate threshold voltage

$$V_{GS(th)}$$
=f(T_j); V_{DS} =3 V; I_D =56 μ A

parameter: I_D

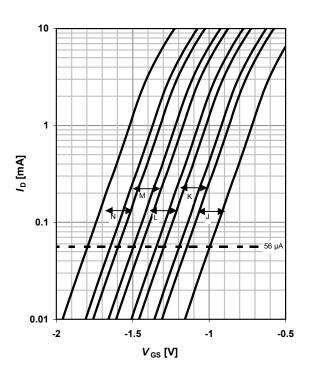


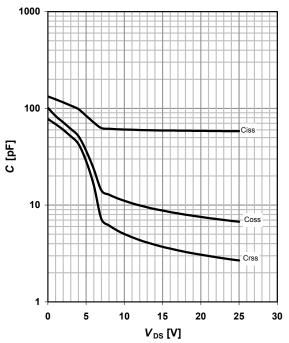
11 Threshold voltage bands

$$I_D$$
=f(V_{GS}); V_{DS} =3 V; T_j =25 °C

12 Typ. capacitances

$$C = f(V_{DS}); V_{GS} = -3 \text{ V}; f = 1 \text{ MHz}$$



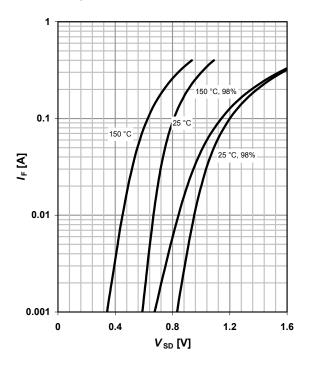




13 Forward characteristics of reverse diode

$I_{\mathsf{F}} = \mathsf{f}(V_{\mathsf{SD}})$

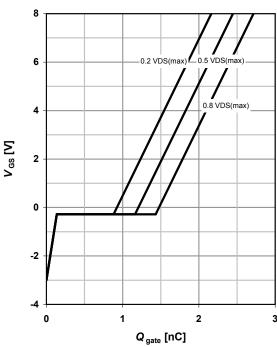
parameter: T_j



15 Typ. gate charge

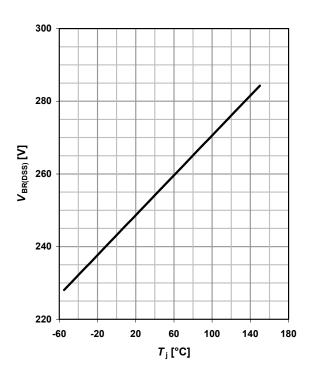
 $V_{\rm GS}$ =f($Q_{\rm gate}$); $I_{\rm D}$ =0.1 A pulsed

parameter: $V_{\rm DD}$



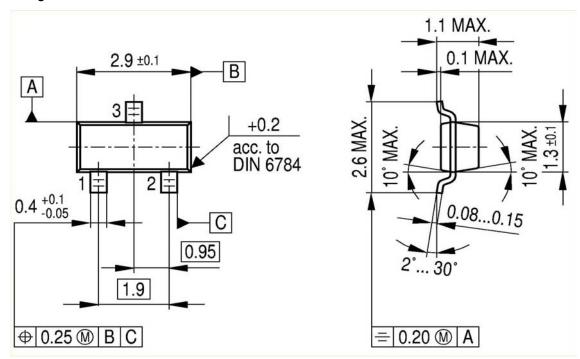
16 Drain-source breakdown voltage

 $V_{BR(DSS)}$ =f(T_j); I_D =250 μ A

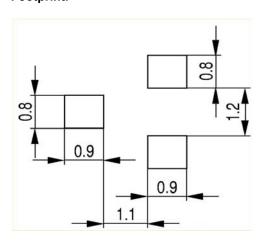




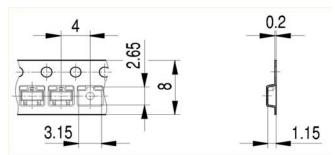
Package Outline:



Footprint:



Packaging:



Dimensions in mm



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