

# **OptiMOS**<sup>™</sup>3 Power-Transistor

### **Features**

- N-channel, normal level
- Excellent gate charge x R DS(on) product (FOM)
- Very low on-resistance R<sub>DS(on)</sub>
- 175 °C operating temperature
- Pb-free lead plating; RoHS compliant
- Qualified according to JEDEC<sup>1)</sup> for target application
- Ideal for high-frequency switching and synchronous rectification
- Halogen-free according to IEC61249-2-21

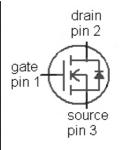
## **Product Summary**

V <sub>DS</sub>	150	٧
R <sub>DS(on),max (TO263)</sub>	7.2	mΩ
I <sub>D</sub>	100	А





Туре	IPB072N15N3 G	IPP075N15N3 G	IPI075N15N3 G
	1 3 2 (tab)	123	1223
Package	PG-TO263-3	PG-TO220-3	PG-TO262-3
Marking	072N15N	075N15N	075N15N



### **Maximum ratings,** at $T_j$ =25 °C, unless otherwise specified

Parameter	Symbol	Conditions	Value	Unit
Continuous drain current	I <sub>D</sub>	T <sub>C</sub> =25 °C	100	А
		T <sub>C</sub> =100 °C	93	
Pulsed drain current <sup>2)</sup>	I <sub>D,pulse</sub>	T <sub>C</sub> =25 °C	400	
Avalanche energy, single pulse	E <sub>AS</sub>	$I_{\rm D}$ =100 A, $R_{\rm GS}$ =25 $\Omega$	780	mJ
Reverse diode dv/dt	dv/dt	$I_{\rm D}$ =100 A, $V_{\rm DS}$ =120 V, d <i>i</i> /d <i>t</i> =100 A/ $\mu$ s, $T_{\rm j,max}$ =175 °C	6	kV/μs
Gate source voltage	$V_{\rm GS}$		±20	V
Power dissipation	$P_{\text{tot}}$	T <sub>C</sub> =25 °C	300	W
Operating and storage temperature	$T_{\rm j},T_{\rm stg}$		-55 175	°C
IEC climatic category; DIN IEC 68-1			55/175/56	

<sup>1)</sup>J-STD20 and JESD22

<sup>&</sup>lt;sup>2)</sup> See figure 3



# IPB072N15N3 G IPP075N15N3 G IPI075N15N3 G

Parameter	Symbol	Conditions	Values		Unit	
			min.	typ.	max.	
Thermal characteristics						
Thermal resistance, junction - case	$R_{\mathrm{thJC}}$		-	-	0.5	K/W
Thermal resistance, junction -	$R_{\mathrm{thJA}}$	minimal footprint	-	-	62	
ambient		6 cm2 cooling area <sup>3)</sup>	-	-	40	

# **Electrical characteristics,** at $T_j$ =25 °C, unless otherwise specified

### Static characteristics

Drain-source breakdown voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> =0 V, I <sub>D</sub> =1 mA	150	-	-	V
Gate threshold voltage	$V_{\rm GS(th)}$	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =270 μA	2	3	4	
Zero gate voltage drain current	I <sub>DSS</sub>	V <sub>DS</sub> =120 V, V <sub>GS</sub> =0 V, T <sub>j</sub> =25 °C	-	0.1	1	μΑ
		V <sub>DS</sub> =120 V, V <sub>GS</sub> =0 V, T <sub>j</sub> =125 °C	-	10	100	
Gate-source leakage current	I <sub>GSS</sub>	V <sub>GS</sub> =20 V, V <sub>DS</sub> =0 V	-	1	100	nA
Drain-source on-state resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =10 V, I <sub>D</sub> =100 A, (TO220, TO262)	-	6.2	7.5	mΩ
		V <sub>GS</sub> =10 V, I <sub>D</sub> =100 A, (TO263)	-	5.8	7.2	
		V <sub>GS</sub> =8 V, I <sub>D</sub> =50 A, (TO220; TO262)	-	6.4	7.7	
		V <sub>GS</sub> =8 V, I <sub>D</sub> =50 A, (TO263)	-	6.0	7.4	
Gate resistance	R <sub>G</sub>		ı	2.3	-	Ω
Transconductance	$g_{fs}$	V <sub>DS</sub>  >2 I <sub>D</sub>  R <sub>DS(on)max</sub> , I <sub>D</sub> =100 A	65	130	-	s

 $<sup>^{3)}</sup>$  Device on 40 mm x 40 mm x 1.5 mm epoxy PCB FR4 with 6 cm $^2$  (one layer, 70  $\mu$ m thick) copper area for drain connection. PCB is vertical in still air.



# IPB072N15N3 G IPP075N15N3 G IPI075N15N3 G

Parameter	Symbol	Conditions		Values		Unit
			min.	typ.	max.	
Dynamic characteristics						
Input capacitance	Ciss		-	5470	-	pF
Output capacitance	Coss	V <sub>GS</sub> =0 V, V <sub>DS</sub> =75 V, f=1 MHz	-	638	-	
Reverse transfer capacitance	C <sub>rss</sub>		-	10	-	
Turn-on delay time	$t_{d(on)}$		-	25	38	ns
Rise time	t <sub>r</sub>	V <sub>DD</sub> =75 V, V <sub>GS</sub> =10 V,	-	35	52	1
Turn-off delay time	$t_{d(off)}$	$I_{\rm D}$ =100 A, $R_{\rm G}$ =1.6 Ω	-	46	69	1
Fall time	t <sub>f</sub>	1	-	14	21	1
Gate Charge Characteristics <sup>4)</sup>						
Gate to source charge	Q <sub>gs</sub>		-	30	40	nC
Gate to drain charge	Q <sub>gd</sub>		ı	11	17	
Switching charge	Q sw	V <sub>DD</sub> =75 V, I <sub>D</sub> =100 A, V <sub>GS</sub> =0 to 10 V	-	25	35	
Gate charge total	Qg		-	70	93	
Gate plateau voltage	V <sub>plateau</sub>		-	5.5	-	V
Output charge	Q <sub>oss</sub>	V <sub>DD</sub> =75 V, V <sub>GS</sub> =0 V	-	179	239	nC
Reverse Diode						
Diode continous forward current	Is	T -25 °C	-	-	100	А
Diode pulse current	I <sub>S,pulse</sub>	T <sub>C</sub> =25 °C	-	-	400	
Diode forward voltage	V <sub>SD</sub>	V <sub>GS</sub> =0 V, I <sub>F</sub> =100 A, T <sub>j</sub> =25 °C	-	1	1.2	V
Reverse recovery time	t <sub>rr</sub>	V <sub>R</sub> =75 V, I <sub>F</sub> =I <sub>S</sub> ,	-	146	-	ns
Reverse recovery charge	Q <sub>rr</sub>	d <i>i<sub>F</sub></i> /d <i>t</i> =100 A/μs	-	478	-	nC

<sup>&</sup>lt;sup>4)</sup> See figure 16 for gate charge parameter definition



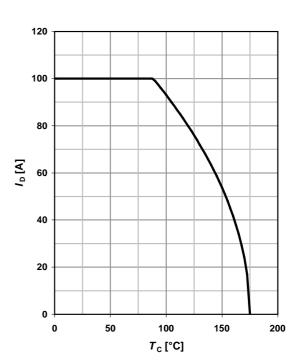
### 1 Power dissipation

### $P_{\text{tot}}$ =f( $T_{\text{C}}$ )

# 280 240 200 120 120 80 40 0 0 0 100 150 200 T<sub>C</sub> [°C]

### 2 Drain current

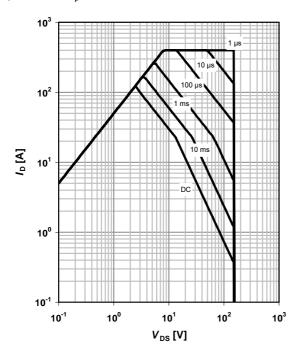
$$I_D = f(T_C); V_{GS} \ge 10 \text{ V}$$



## 3 Safe operating area

$$I_D$$
=f( $V_{DS}$ );  $T_C$ =25 °C;  $D$ =0

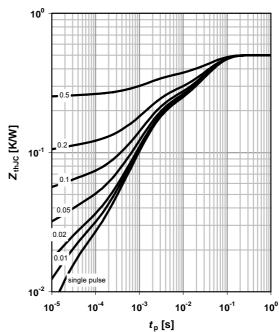
parameter:  $t_p$ 



### 4 Max. transient thermal impedance

$$Z_{\rm thJC}$$
=f( $t_{\rm p}$ )

parameter:  $D=t_p/T$ 

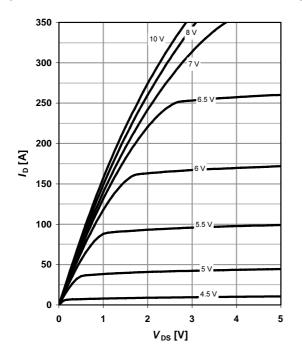




### 5 Typ. output characteristics

 $I_D=f(V_{DS}); T_j=25 °C$ 

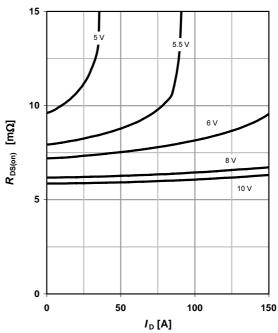
parameter:  $V_{\rm GS}$ 



### 6 Typ. drain-source on resistance

 $R_{DS(on)}$ =f( $I_D$ );  $T_j$ =25 °C

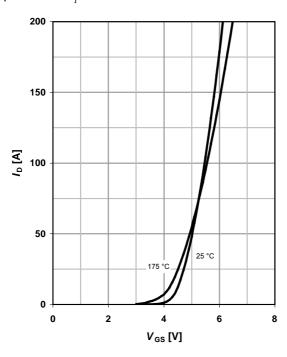
parameter:  $V_{\rm GS}$ 



## 7 Typ. transfer characteristics

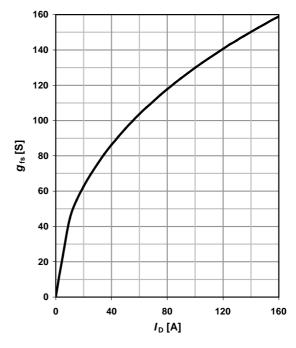
 $I_{D}$ =f( $V_{GS}$ );  $|V_{DS}|$ >2 $|I_{D}|R_{DS(on)max}$ 

parameter:  $T_j$ 



## 8 Typ. forward transconductance

 $g_{fs}$ =f( $I_D$ );  $T_j$ =25 °C





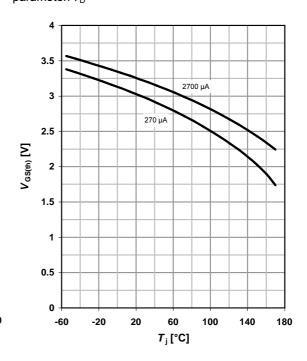
### 9 Drain-source on-state resistance

 $R_{DS(on)} = f(T_j); I_D = 100 A; V_{GS} = 10 V$ 

# 20 15 10 98% 5 0 -60 -20 20 60 100 140 180 T<sub>j</sub> [°C]

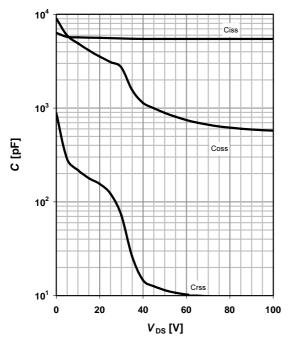
### 10 Typ. gate threshold voltage

 $V_{\text{GS(th)}}$ =f( $T_{\text{j}}$ );  $V_{\text{GS}}$ = $V_{\text{DS}}$ parameter:  $I_{\text{D}}$ 



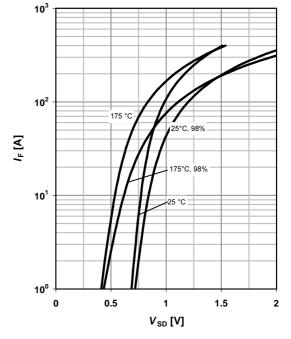
### 11 Typ. capacitances

 $C=f(V_{DS}); V_{GS}=0 V; f=1 MHz$ 



### 12 Forward characteristics of reverse diode

 $I_{\text{F}} = f(V_{\text{SD}})$ parameter:  $T_{\text{j}}$ 

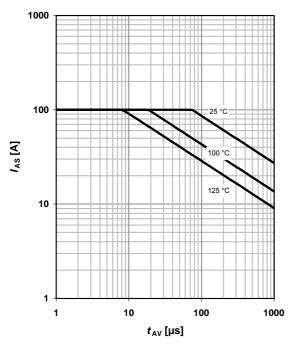




#### 13 Avalanche characteristics

 $I_{AS}$ =f( $t_{AV}$ );  $R_{GS}$ =25  $\Omega$ 

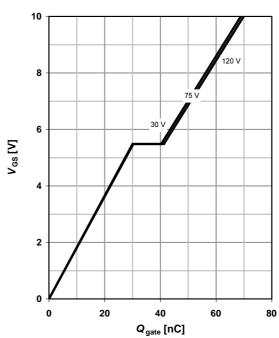
parameter:  $T_{j(start)}$ 



### 14 Typ. gate charge

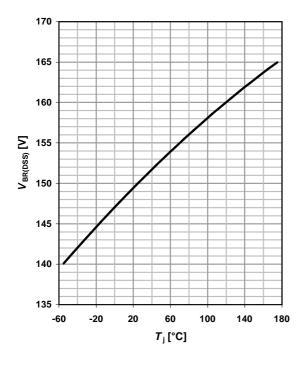
 $V_{\rm GS}$ =f(Q<sub>gate</sub>);  $I_{\rm D}$ =100 A pulsed

parameter:  $V_{\rm DD}$ 

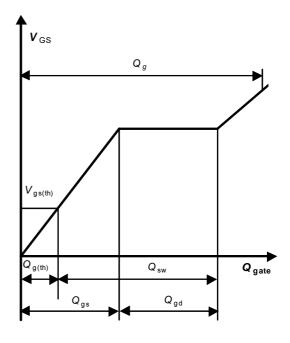


### 15 Drain-source breakdown voltage

 $V_{BR(DSS)}$ =f( $T_j$ );  $I_D$ =1 mA

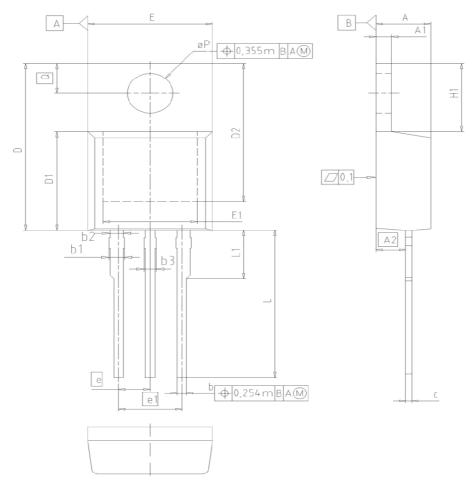


### 16 Gate charge waveforms





### PG-TO220-3: Outline

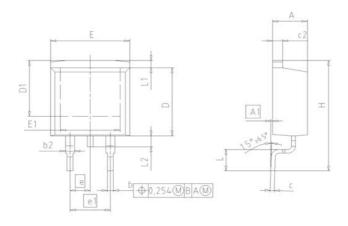


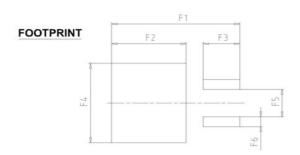
DIM	MILLI	METERS	INC	HES
DIN	MIN	MAX	MIN	MAX
Α	4.30	4.57	0.169	0.180
A1	1.17	1.40	0.046	0.055
A2	2.15	2.72	0.085	0.107
b	0.65	0.86	0.026	0.034
<b>b</b> 1	0.95	1.40	0.037	0.055
b2	0.95	1.15	0.037	0.045
b3	0.65	1.15	0.026	0.045
С	0.33	0.60	0.013	0.024
D	14.81	15.95	0.583	0.628
D1	8.51	9.45	0.335	0.372
D2	12.19	13.10	0.480	0.516
E	9.70	10.36	0.382	0.408
E1	6.50	8.60	0.256	0.339
e	2	2.54		00
e1	5	5.08	0.2	200
N		3		3
H1	5.90	6.90	0.232	0.272
L	13.00	14.00	0.512	0.551
L1	-	4.80	-	0.189
øΡ	3.60	3.89	0.142	0.153
Q	2.60	3.00	0.102	0.118

DOCUMEN Z8B0000	
SCALE	0
0 2.5	2.5 5mm
EUROPEAN P	ROJECTION
ISSUE E 23-08-2	
REVIS	ION



### PG-TO263-3: Outline



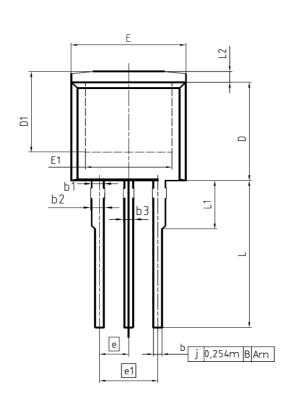


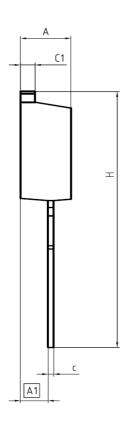
DIM	MILLIN	IETERS	INCI	HES
DIM	MIN	MAX	MIN	MAX
A	4.30	4.57	0.169	0.180
A1	0.00	0.25	0.000	0.010
b	0.65	0.85	0.026	0.033
b2	0.95	1.15	0.037	0.045
С	0.33	0.65	0.013	0.026
c2	1.17	1.40	0.046	0.055
D	8.51	9.45	0.335	0.372
D1	7.10	7.90	0.280	0.311
E	9.80	10.31	0.386	0.406
E1	6.50	8.60	0.256	0.339
е	2.54		0.100	
e1	5.08		0.200	
N		2	2	
н	14.61	15.88	0.575	0.625
L	2.29	3.00	0.090	0.118
L1	0.70	1.60	0.028	0.063
L2	1.00	1.78	0.039	0.070
F1	16.05	16.25	0.632	0.640
F2	9.30	9.50	0.366	0.374
F3	4.50	4.70	0.177	0.185
F4	10.70	10.90	0.421	0.429
F5	3.65	3.85	0.144	0.152
F6	1.25	1.45	0.049	0.057





### PG-TO262-3: Outline





DIM	MILLIM	MILLIMETERS		IES	
DIM	MIN	MAX	MIN	MAX	
Α	4.300	4,572	0.169	0.180	
A1	2.150	2.718	0.085	0.107	
b	0.650	0.864	0.026	0.034	
b1	0.950	1.093	0.037	0.043	
b2	0.950	1,400	0.037	0.055	
ь3	0.650	1.118	0.026	0.044	
С	0.330	0.600	0.013	0.024	
c1	1.170	1.400	0.046	0.055	
D	8.509	9,450	0.335	0.372	
D1	6.900	-	0.272	-	
E	9.700	10.363	0.382	0.408	
E1	6.500	8.600	0.256	0.339	
е	2.5	540	0,100		
e1	5.080		0.200		
N		3	3		
L	13.000	14.000	0.512	0.551	
L1	-	4,800	-	0.189	
L2	-	1.727	-	0.068	

REFERENCE JEDEC TO262
JEDEC 10262
SCALE 0
2.5- 0 2.5 L5mm
EUROPEAN PROJECTION
ISSUE DATE 05-05-2006
FILE TO262_1



Published by Infineon Technologies AG 81726 Munich, Germany © 2010 Infineon Technologies AG All Rights Reserved.

### **Legal Disclaimer**

The information given in this document shall in no event be regarded as a guarantee of conditions or characteristics. With respect to any examples or hints given herein, any typical values stated herein and/or any information regarding the application of the device, Infineon Technologies hereby disclaims any and all warranties and liabilities of any kind, including without limitation, warranties of non-infringement of intellectual property rights of any third party.

#### Information

For further information on technology, delivery terms and conditions and prices, please contact the nearest Infineon Technologies Office (www.infineon.com).

### **Warnings**

Due to technical requirements, components may contain dangerous substances. For information on the types in question, please contact the nearest Infineon Technologies Office. The Infineon Technologies component described in this Data Sheet may be used in life-support devices or systems and/or automotive, aviation and aerospace applications or systems only with the express written approval of Infineon Technologies, if a failure of such components can reasonably be expected to cause the failure of that life-support, automotive, aviation and aerospace device or system or to affect the safety or effectiveness of that device or system. Life support devices or systems are intended to be implanted in the human body or to support and/or maintain and sustain and/or protect human life. If they fail, it is reasonable to assume that the health of the user or other persons may be endangered.

# **Mouser Electronics**

**Authorized Distributor** 

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

# Infineon:

IPI075N15N3 G IPP075N15N3 G IPI075N15N3GXKSA1 IPP075N15N3GXKSA1