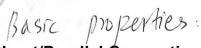
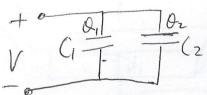
apacitor. a) Sack of Change -> thought expressment. In reality, we have a Capacitor that can store changes In modern electronis, Capactor is very Impartant, it is the Busic element in a train sistor structure, or flush memory (gate /floor gate) Basic structure. plate diebetic in sulating 1. Charge (both positive & negative) can be stored on the plates 2. No charge/current can pass through the dielectric layer let's assure initially If it is a open-circuit, we know that there will be no current flow But Capacitor will still have mittal convent. insulator (hunge. Conservation Charge neutrality Since the dielectric is an insulator, no current flow through the agaston So Q - will rauch to a saturation permittivity Q= (.V -> Capactitunce. (Farads)



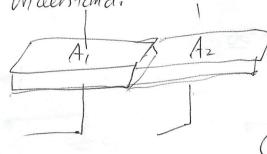


$$\Rightarrow \bigvee_{V} \int_{C_{ij}} C_{ij}$$

$$Q = Q_1 + Q_2 \implies G_1 \cdot V = G_1 \cdot V + C_2 \cdot V$$

$$\Rightarrow G_1 = G_1 + (2.$$

How to inderstand.



total area is A. +Az

$$G = \frac{\varepsilon A}{d} = \frac{\varepsilon (A_1 + A_2)}{d} = \frac{\varepsilon A_1}{d} + \frac{\varepsilon A_2}{d}$$

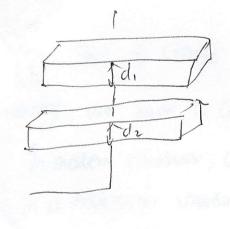
$$G = G_{i}$$

$$\Rightarrow \frac{1}{1}G_{i}$$

## 2. Sen'es Connection.

$$\frac{V_1 + V_2 = V}{Q_1 + Q_2} = \frac{Q}{Q_2} \Rightarrow \frac{1}{Q_2} = \frac{1}{Q_2} + \frac{1}{Q_2}$$

How to understand.



$$C_{eq} = \frac{\epsilon A}{d_1 + d_2}$$

$$= \frac{d_1}{\epsilon A} + \frac{d_2}{\epsilon A} = \frac{1}{\epsilon A}$$

$$= \frac{1}{C_1} + \frac{1}{C_2}$$

In general.

$$\frac{d^2 f_{(t)}}{dt} = \frac{d^2 f_{(t)}}{dt} = \frac{d^2$$

Linear dependence on voltage

Suppose.

C. Vo

from 
$$(0, 2s)$$
  $V_{(t)} = \frac{V_0}{z} \cdot t$   
from  $(2, +\infty)$   $V = V_0$