

[illegible]

EEPROM

The diagram illustrates the connection of an EEPROM (BR24G32FVT-3GE2) to an I2C bus. The chip is powered by 3V3_ALW through a 0R resistor (R16) and a 0.1uF capacitor (C1). The chip's pins are connected as follows:

- Address Pins (A0, A1, A2):** Connected to GND through 0R resistors (R18, R17, R19).
- SDA (A0):** Connected to the I2C_SDA line through a 4.7K resistor (R1).
- SCL (A1):** Connected to the I2C_SCL line through a 4.7K resistor (R2).
- WP (A2):** Connected to GND through a 100K resistor (R3).
- VCC:** Connected to 3V3_EEPROM.
- GND:** Connected to GND.
- EEPROM_WP:** Connected to GND through a 0R resistor (R20).

Choose Slave Address
Connect all to GND by default

The diagram shows the internal pull-up resistors (R1, R2) and the external pull-down resistor (R3) for the address pins. The chip is labeled BR24G32FVT-3GE2.

FANS

The diagram illustrates the wiring for two fans, Fan0 (J2) and Fan1 (J3), connected to a Raspberry Pi. The connections are as follows:

- GND:** Connected to pins 1 and 3 of both Fan0 and Fan1.
- 12V_FAN:** Connected to pins 2 and 4 of both Fan0 and Fan1.
- Fan0 (J2):**
 - FAN0_PWM:** Connected to pin 4.
 - FAN0_SPD:** Connected to pin 2.
- Fan1 (J3):**
 - FAN1_PWM:** Connected to pin 4.
 - FAN1_SPD:** Connected to pin 2.

Both fan headers are marked with "MP".

Nano-Pitch OCuLink

Pinout diagram for Nano-Pitch OCuLink connector J4 (173162-0131). The diagram shows the connections for various signals and ground pins.

Pinout details:

- Pin 1:** RX0+ (GND)
- Pin 2:** RX0- (GND)
- Pin 3:** RX1+ (GND)
- Pin 4:** RX1- (GND)
- Pin 5:** RX2+ (GND)
- Pin 6:** RX2- (GND)
- Pin 7:** RX3+ (GND)
- Pin 8:** RX3- (GND)
- Pin 9:** RX4+ (GND)
- Pin 10:** RX4- (GND)
- Pin 11:** RX5+ (GND)
- Pin 12:** RX5- (GND)
- Pin 13:** TX0+ (GND)
- Pin 14:** TX1+ (GND)
- Pin 15:** TX2+ (GND)
- Pin 16:** TX3+ (GND)
- Pin 17:** TX4+ (GND)
- Pin 18:** TX5+ (GND)
- Pin 19:** TX6+ (GND)
- Pin 20:** TX7+ (GND)
- Pin 21:** TX0- (GND)
- Pin 22:** TX1- (GND)
- Pin 23:** TX2- (GND)
- Pin 24:** TX3- (GND)
- Pin 25:** TX4- (GND)
- Pin 26:** TX5- (GND)
- Pin 27:** TX6- (GND)
- Pin 28:** TX7- (GND)
- Pin 29:** PERSTA (GND)
- Pin 30:** CPRSNTA (GND)
- Pin 31:** PERSTB (GND)
- Pin 32:** CPRSNTB (GND)
- Pin 33:** MP1 (GND)
- Pin 34:** MP2 (GND)
- Pin 35:** MP3 (GND)
- Pin 36:** MP4 (GND)
- Pin 37:** REFCLKA+ (GND)
- Pin 38:** REFCLKA- (GND)
- Pin 39:** REFCLKB+ (GND)
- Pin 40:** REFCLKB- (GND)

Additional connections:

- REFCLKA+:** Connected to REFCLKB+ via R12 OR.
- REFCLKA-:** Connected to REFCLKB- via R13 OR.
- PERSTA:** Connected to PERSTB via R14 OR.
- CPRSNTA:** Connected to CPRSNTB via R15 OR.
- MP1:** Connected to MP2 via R16 OR.
- MP2:** Connected to MP3 via R17 OR.
- MP3:** Connected to MP4 via R18 OR.

TODO: Add CPRSNT logic

CPRSNTA OR R19 3V3_ALW

TODO: Add CPRSNT logic

CPRSNTA OR 3V3_ALW
R19

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