Ethernet on the Zynq ZC706

18-545 Advanced Digital Design



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Chapter 1

Introduction

This paper is a guide to start building ethernet on the Zynq ZC706 board. It was originally written as the final project for a 18-545 project which didn't complete because they struggled to build an ethernet adapter in programmable logic and have it properly communicate with the Processing System. The intention of this paper is to aid future groups in completing an ethernet adapter, as well as providing the necessary background and deterring groups from fruitless avenues. This guide will expect a very minimal understanding of Vivado because most students in 18-545 have had very limited exposure to Vivado. We will attempt to provide the pertinent references as needed.

That being said, going through Lab 2 in Vivado Design Suite Tutorial [4] will probably be the fastest way to understand the work flow. Also, chapter 2 and chapter 4 of UltraFast Design Methodology Guide for the Vivado Design Suite [3] will be superbly helpful in learning to use Vivado, especially for using IP in Vivado. Finally, if you still want more details on using IP, you can refer to the Vivado guide on Designing with IP [2] and Designing IP Subsystems Using IP Integrator [1].

Chapter 2 Ethernet Background

Chapter 3 Basic Approaches

Chapter 4 Ethernet PL Guide

Chapter 5 PetaLinux Networking

Chapter 6 Alternate Approaches

Chapter 7

More on PL

Chapter 8

Miscellaneous

Bibliography

- [1] Xilinx. Designing IP Subsystems Using IP Integrator. 2015. URL: http://www.xilinx.com/support/documentation/sw_manuals/xilinx2015_4/ug994-vivado-ip-subsystems.pdf.
- [2] Xilinx. Designing with IP. 2015. URL: http://www.xilinx.com/support/documentation/sw_manuals/xilinx2015_4/ug896-vivado-ip.pdf.
- [3] Xilinx. UltraFast Design Methodology Guide for the Vivado Design Suite. 2015. URL: http://www.xilinx.com/support/documentation/sw_manuals/ug949-vivado-design-methodology.pdf.
- [4] Xilinx. Vivado Design Suite Tutorial. 2015. URL: http://www.xilinx.com/support/documentation/sw_manuals/xilinx2015_4/ug888-vivado-design-flows-overview-tutorial.pdf.

Appendix A Appendix