



- Lowering the PGM\_Mode causes the CPU board to become disabled.
- BOE needs to go low in order for the Pogramming board to take over the CPU boards
- Page\_Register is a special CPLD Command to use theA0-A3 lines to set the page in the Page

Title: AVR Programmer	
Project: Peripheral_Processor_Board_V2.PrjPcb	
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