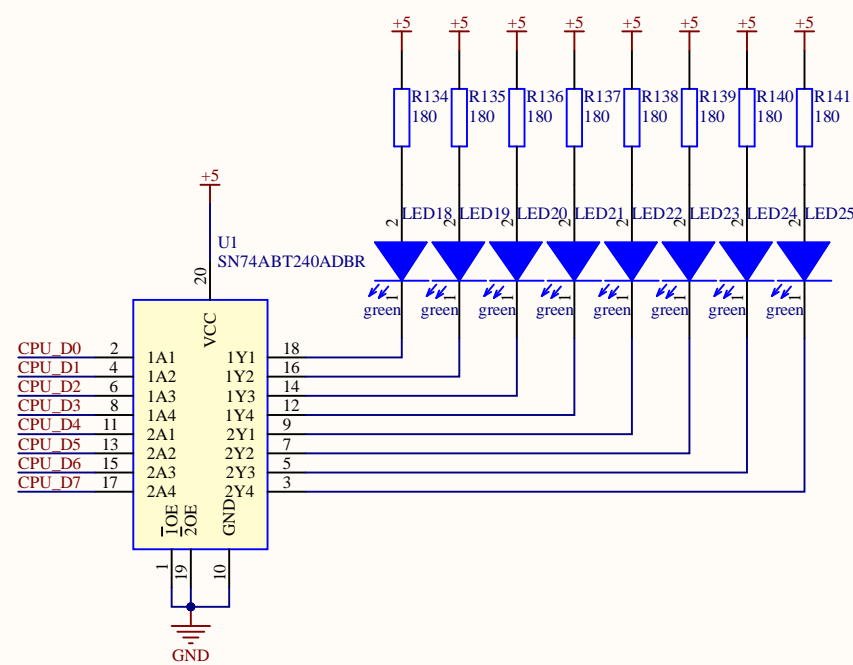
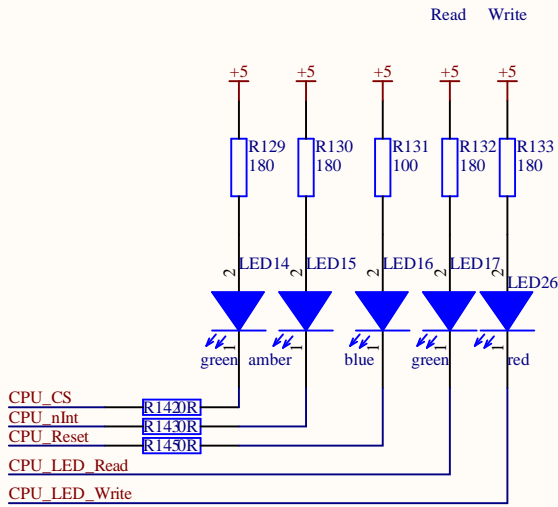


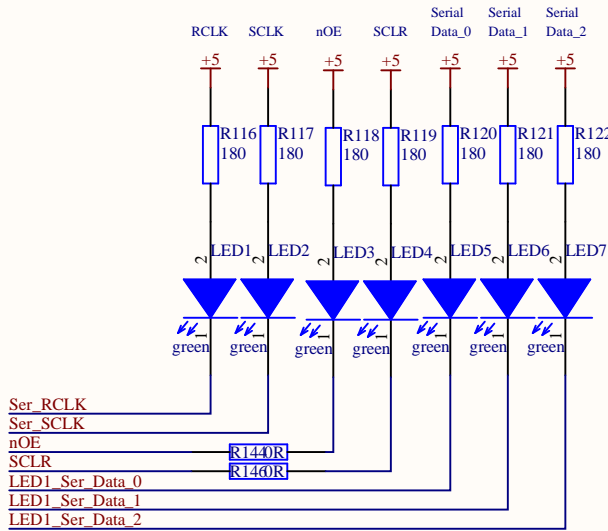
CPU Data Bus



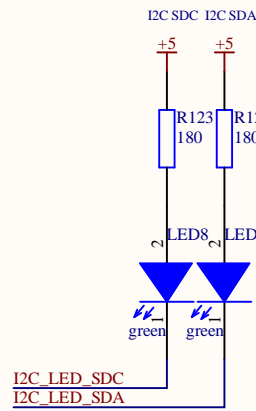
CPU Control



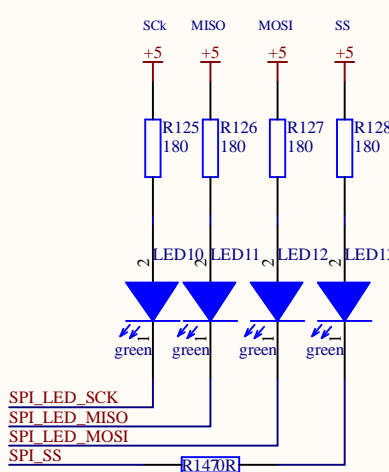
Shift Register



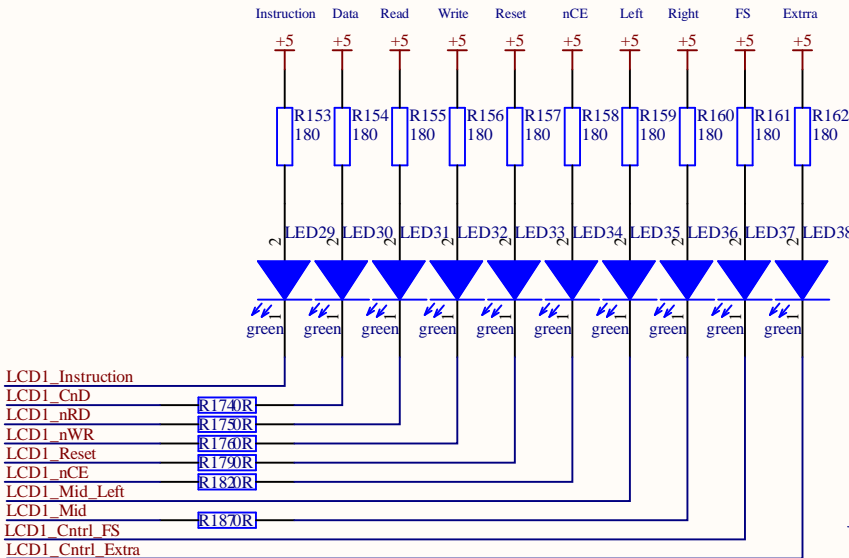
I2C



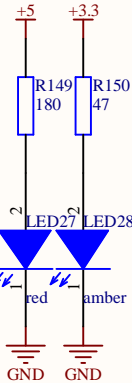
SPI



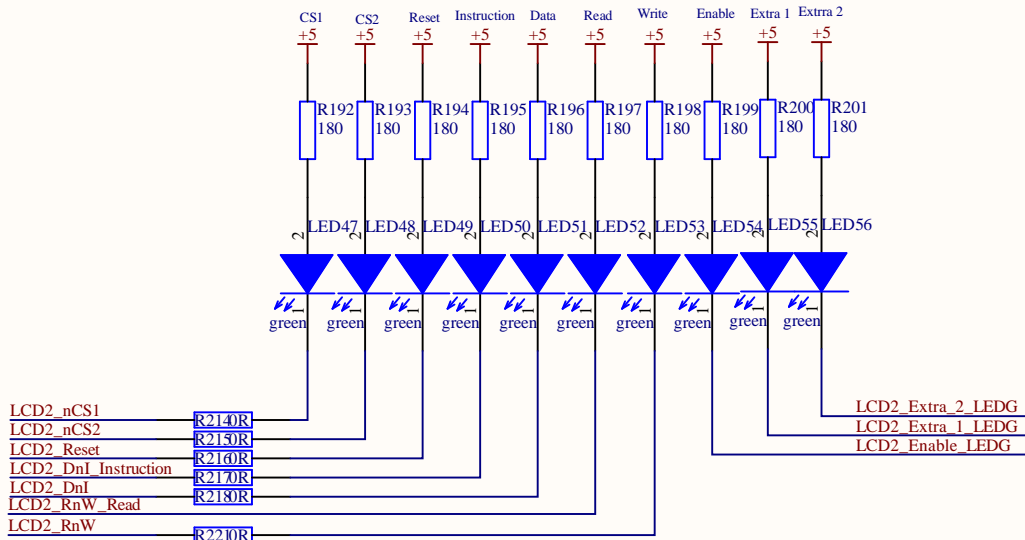
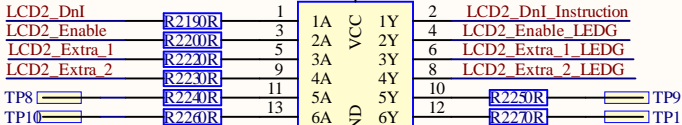
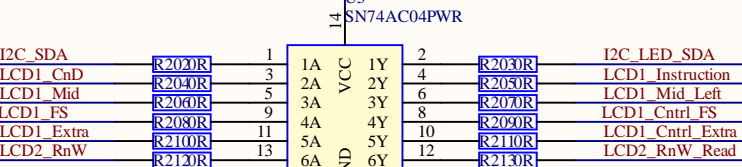
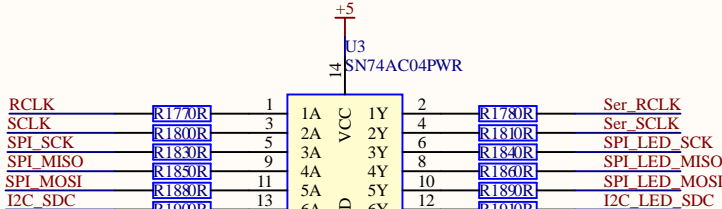
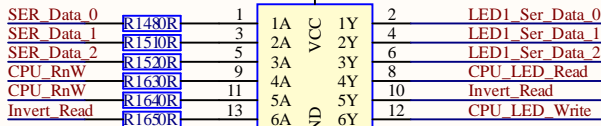
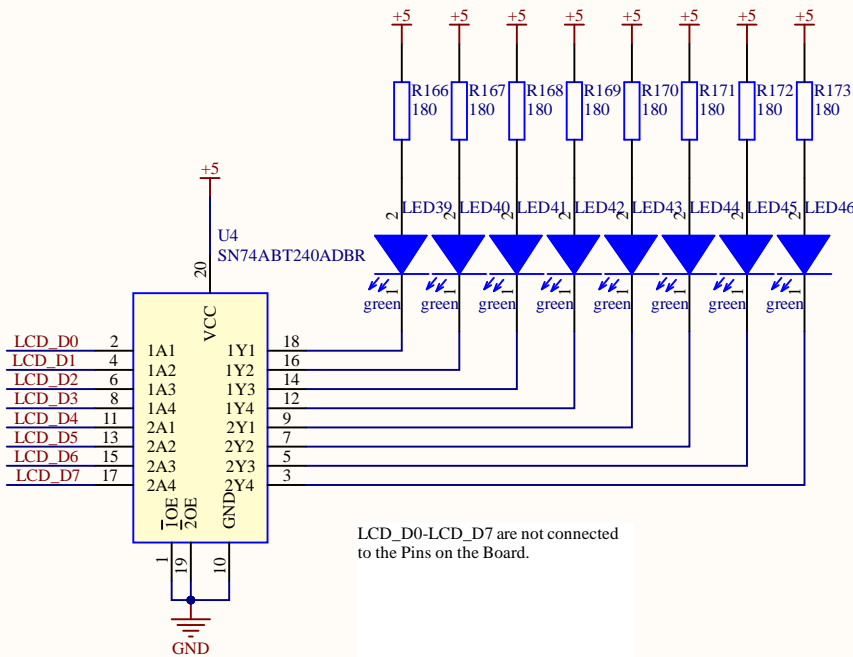
LCD 1 Control



Power



LCD Data Bus



LCD 2 Control

Title: Display LED Monitor

Project: Display_V1.1.PjPcb

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Revision: 1.0

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