

MCAL_AM261_10.00.00 Release Notes

MCAL Release Notes

Table of Contents

- [Introduction](#)
- [Licensing](#)
- [Getting Started](#)
- [Datasheet](#)
- [Documentation](#)
- [Dependencies](#)
- [What's New](#)
- [Device Support](#)
- [Tools and Compilers](#)
- [Compatibility](#)
- [Validation](#)
- [Fixed Defects](#)
- [Open Defects](#)
- [Known Limitations](#)
- [Support](#)
- [Versioning](#)

Introduction

This is the release notes for **MCAL_AM261_10.00.00** release, done on 2024-10-15.

The MCAL package consists of MCAL Driver & Applications for **AM261x family of devices**.
The MCAL modules are compliant to AUTOSAR specification versioned **4.3.1**.

Licensing

Refer to AM261x manifest at top level for MCAL_AM261_10.00.00_manifest.html

Getting Started

The AM261x [[HTML](#)] User Guide provides the documentation and references necessary to begin development on TI's platforms using AM261x.

Datasheet

The AM261x [[HTML](#)] User Guide provides the Memory footprint details.

Documentation

The AM261x [\[HTML\]](#) User Guide provides the documentation and references necessary to begin development on TI's platforms using AM261x.

This document details about supported driver, installation, dependencies, build instructions, steps to run example applications. This release package also includes module specific User Guides, test reports, configurator User Guide and others.

Dependencies

NA

What's New

1. New in this Release:
 - Support AM261 ZCZQ1 Package
 - Added BSWMD Arxml files for all the module configurator Plugins.
 - CCS Based Project for MCAL Examples
 - DataSheet (Memory Foot-Print)
 - User Guide
1. Example testing is performed for all the MCAL drivers mentioned in below table.
2. Drivers Supported:

Drivers	Modules
Micro controller Drivers	GPT, WDG, MCU
Memory Driver	FLS
Communication Driver	SPI, CAN
I/O Drivers	ADC, DIO, PORT, PWM, ICU
CDD	IPC, UART, I2C, DMA

Device Support

SoC	HOST (OS)	Target (OS)	Supported CPU	Test Platform
AM261x	Windows / Linux, EB configurator only on Windows	Baremetal	R5F	am261x

Tools and Compilers

1. Code Composer Studio: 12.8.0 or later. (**Note:** Please refer `CCS_Setup` present as part of `getting_started`)
2. TI ARM CLANG Compiler Version: 4.0.0
3. Elektrobit Tresos Studio: 24.0 (**EB_Tresos_ACG8.5.0_Installer.zip**). Please use link to request access to EB Tresos Studio and License: [Click Here](#)

Compatibility

*Internal Files are organized in V0, V1, V2 and V3 folders. The below table lists the associated V0/V1/V2/V3 files to be used as per device.

#	Modules	AM263x	AM273	AM263Px	AM261x
1.	ADC	V0	V1	V0	V0
2.	CAN	V0	V0	V0	V0
3.	CDD I2C	V0	V0	V0	V0
4.	CDD IPC	V0	V1	V0	V2
5.	CDD UART	V0	V1	V0	V0
6.	CDD DMA	V0	V1	V0	V0
7.	CDD PWM	V0	NA	V0	NA
8.	DIO	V0	V1	V0	V0
9.	ETH	V0	V1	V0	NA
10.	ETHTRCV	V0	V1	V0	NA
11.	FLS	V0	V1	V2	V2

12.	FSI	V0	NA	V0	NA
13.	GPT	V0	V0	V0	V0
14.	ICU	V0	V1	V0	V0
15.	LIN	V0	NA	V0	NA
16.	MCU	V0	V1	V2	V3
17.	PORT	V0	V1	V0	V0
18.	PWM	V0	V1	V0	V0
19.	SPI	V0	V1	V0	V0
20.	WDG	V0	V0	V0	V0

Validation

#	Module	Validation Scope	Remarks (Refer to <i>Open Defects</i> for details)
1.	ADC	Example Functional Testing	None
2.	CAN	Example Functional Testing	None
3.	CDD I2C	Example Functional Testing	None
4.	CDD IPC	Example Functional Testing	Internally tested with AM261x-LP Release Installer but can be tested with MCUPPLUS SDK 10.00.01 Release of AM261x-SOM (available by mid of nov)
5.	CDD UART	Example Functional Testing	None
6.	DIO	Example Functional Testing	None
7.	DMA	Example Functional Testing	None
8.	FLS	Example Functional Testing	None

9.	GPT	Example Functional Testing	None
10.	ICU	Example Functional Testing	None
11.	MCU	Example Functional Testing	Reset Reason tested only for Power on Reset, SW Warm Reset and Watchdog reset
12.	PORT	Example Functional Testing	None
13.	PWM	Example Functional Testing	None
14.	SPI	Example Functional Testing	None
15.	WDG	Example Functional Testing	None

Fixed Defects

None

Open Defects

ID	Summary	Workaround
MCAL-27009	[AM261]: I2C: Temperature sensor testing issue	None
MCAL-27008	[AM261]: MCU: MCU PLL is not supported	MCU_NO_PLL should be enable
MCAL-27007	[AM261]: UART(DMA) : Uart read/write with DMA enabled is not working	Interrupt mode can be used instead
MCAL-25969	Connecting Multiple Interrupt sources to single Interrupt Xbar line is not supported	None
MCAL-13434	[Spi]Dma mode is not working with Cache Writeback enabled	Enable Cachewrite through mode
MCAL-11609	[Spi] Data Receive mismatch if datawidth is 16bit and number of words not aligned to fifodepth 16	User has to disable cache or align the number of words to 16

Known Limitations

ID	Description	Workaround	Comments
NA	NA	NA	NA

Support

For technical support and additional assistance, visit [E2E](#) or contact local TI Field Application Engineer.

Versioning

Each package version is composed of 4 period-delimited numbers - represented here by the letters M, m, p and b [**M.m.p.b**]. The table below provides a descriptive reference regarding package version numbering.

Digit	Meaning	Description
1 (M =Major)	Major revision	Incremented when the new version is substantially different from the previous for example, a new module added or an existing modules algorithm significantly altered.
2 (m =minor)	Minor revision	Incremented when the new version has changed but not in a major way. For example, some minor changes in the API or feature set.
3 (p =patch)	Patch number	Incremented for all other source code changes. This includes any packaging support code.
4 (b =build)	Build number	Incremented for each release delivery to CM. Reset for any change to M, m or p

TI is a global semiconductor design and manufacturing company. Innovate with 100,000+ analog ICs and embedded processors, along with software, tools and the industry's largest sales/support staff.

© Copyright 1995-2023, Texas Instruments Incorporated. All rights reserved.
[Trademarks](#) | [Privacy policy](#) | [Terms of use](#) | [Terms of sale](#)