



# AM26xx MCAL 10.02.01 Release Notes

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# 1 Introduction

This is the release notes for **MCAL\_AM26xx\_10.02.01** release, done on  17 Jun 2025

The MCAL package consists of MCAL Driver & Applications for **AM26xx family of devices**. The MCAL modules are compliant to AUTOSAR specification versioned **4.3.1**.

**Note: This is a patch release to support new features and bug fixes. Only limited testing is performed based on regression plan, Customer should migrate to next regular release (11.00.00) when available.**

## 1.1 Licensing

Refer to AM263x/AM263Px/AM261x manifest at top level for mcal\_manifest\_am263x.html/  
mcal\_manifest\_am263px.html/mcal\_manifest\_am2631.html

## 1.2 Getting Started

The AM263x [[HTML](#)] /AM263Px [[HTML](#)] /AM261x [[HTML](#)] User Guide provides the documentation and references necessary to begin development on TI's platforms using AM263x/AM263Px/AM261x.

## 1.3 Datasheet

User Guide provides the Memory footprint details.

## 1.4 Documentation

The AM263x [[HTML](#)] /AM263Px [[HTML](#)] /AM261x [[HTML](#)] User Guide provides the documentation and references necessary to begin development on TI's platforms using AM263x/AM263Px/AM261x.

This document details about supported driver, installation, dependencies, build instructions, steps to run example applications. This release package also includes module specific User Guides, test reports, configurator User Guide and others.

## 1.5 Dependencies

NA

## 1.6 Tools and Compilers

1. Code Composer Studio: 12.5.0 or later.
2. TI ARM CLANG Compiler Version: 4.0.0
3. Elektrobit Tresos Studio: 29.3 (**[EB\\_Tresos\\_ACG8.8.10\\_Installer.zip](#)**). Please use link to request access to EB Tresos Studio and License: [Click Here](#)

## 2 What's New

### 2.1 New in this Release

<b>Features</b>	<b>Module</b>	<b>Supported Platforms</b>
ADC-R instance support	ADC	AM263Px
ADC Periodic HW Trigger from PWM with DMA Transfer Example	ADC	AM263Px
WDG compile time option to skip device reset when 0 timeout value is passed to Wdg_SetTriggerCondition API	WDG	AM263x, AM263Px, AM261x
<p>Code formatting through clang linting tool to enforce coding guidelines.</p> <p>This resulted in multiple whitespace changes in all source code but with no change in functionality.</p> <p>User need to ignore whitespace changes when comparing the package with previous release packages.</p>	All	AM263x, AM263Px, AM261x
Bug Fixes	All	Please refer <b>Fixed Defects</b> section for more details.

\*Regression testing performed for all the MCAL drivers based on regression test plan

### 2.2 Compatibility

<b>Driver</b>	<b>Compatibility Information</b>	<b>Comments</b>	<b>Recommended update for Customer Application</b>
None			

\*Internal Files are organized in V0, V1, V2 and V3 folders. The below table lists the associated V0/V1/V2/V3 files to be used as per device.



#	Modules	AM263x	AM263Px	AM261x
1.	ADC	V0	V0	V0
2.	CAN	V0	V0	V0
3.	CDD I2C	V0	V0	V0
4.	CDD IPC	V0	V0	V2
5.	CDD UART	V0	V0	V0
6.	CDD DMA	V0	V0	V0
7.	CDD PWM	V0	V0	NA
8.	DIO	V0	V0	V0
9.	ETH	V0	V0	NA
10.	ETHTRCV	V0	V0	NA
11.	FLS	V0	V2	V2
12.	FSI	V0	V0	NA
13.	GPT	V0	V0	V0
14.	ICU	V0	V0	V0
15.	LIN	V0	V0	NA
16.	MCU	V0	V2	V3
17.	PORT	V0	V0	V0



#	Modules	AM263x	AM263Px	AM261x
18.	PWM	V0	V0	V0
19.	SPI	V0	V0	V0
20.	WDG	V0	V0	V0

## 2.3 Drivers Supported

Drivers	Modules
Micro controller Drivers	GPT, WDG, MCU (with PWM XBAR, Output XBAR)
Memory Driver	FLS
Communication Driver	CAN, ETH, ETHTRCV, LIN, SPI
I/O Drivers	ADC, DIO, ICU, PORT, PWM
CDD	CMPSS, DMA, FLC, FSI, I2C, IPC, PWM, UART

## 2.4 Platforms Supported

SoC	Test Platform	HOST (OS)	Target (OS)	Supported CPU
AM263x	AM263x (am263x-cc)	Build: Windows / Linux EB Configurator: Only on Windows	Baremetal	R5F
AM263Px	AM263Px ControlCard (am263px-cc)	Build: Windows / Linux EB Configurator: Only on Windows		
	AM263Px Launchpad (am263px-lp)			



SoC	Test Platform	HOST (OS)	Target (OS)	Supported CPU
	AM263Px SIP Package			
AM261x	AM261x (am261x-som)			

### 3 Fixed Defects

<b>ID</b>	<b>Summary</b>	<b>Module</b>	<b>Platform</b>
MCAL-30 664	[OSPI] CSL_OSPI_CONFIG_REG_SEL_CLK_PHASE_FLD_MASK macro in MCAL_AM263Px	FLS	AM263Px
MCAL-30 658	Multiple FLS reads fails when DMA enabled	FLS	AM263Px
MCAL-30 634	Wrong path of MCU_E_MODE_FAILURE in the mcu_plugin	MCU	AM263x, AM263Px, AM261x
MCAL-30 736	EB configurator allows setting of Port_PinInitialMode as GPIO, without the need for pin mode being GPIO	PORT	AM263x, AM263Px, AM261x
MCAL-30 737	Port_GetGPIOPortAddr() return address not checked for NULL	PORT	AM263x, AM263Px, AM261x
MCAL-30 611	Wdg Set Mode api reset the Wdg reaction and Window size register value	WDG	AM263x, AM263Px, AM261x
MCAL-31 454	TOP RCM register for WARM RESET CONFIG accessed without unlocking the MMR	WDG	AM263x, AM263Px, AM261x
MCAL-29 488	AM263Px: SPI -DMA interrupt Handling is wrong when both TX and RX are selected	SPI	AM263x, AM263Px, AM261x

## 4 Open Defects

ID	Summary	Module	Platform	Workaround
MCAL-29157	AdcApp_GroupEndNotification not working for polling mode	ADC	AM263x, AM263Px, AM261x	Customer to use ADC in Interrupt Mode
MCAL-26385	Can Tx/Rx not working for BitRate=125 KBPS	CAN	AM263Px, A M261x	Customer to use BitRate other than 125KBPS
MCAL-21653	Fls Example App is not working in SBL OSPI mode	FLS	AM263Px, A M261x	No-Boot mode can be used.
MCAL-31448	Multichannel I2C transaction failure	I2C	AM263x, AM263Px, AM261x	None
MCAL-29057	LinChannelEcuMWakeupSource should not be mandatory parameter as per ASR 4.3.1	LIN	AM263x, AM263Px, AM261x	Customer should configure at least one wakeup source in EcuM which can be linked.
MCAL-25969	Connecting Multiple Interrupt sources to single Interrupt Xbar line is not supported	MCU	AM263x, AM263Px, AM261x	Use single interrupt source to a XBar
MCAL-13434	DMA mode is not working with Cache Writeback enabled	SPI	AM263x, AM263Px, AM261x	Customer to use Cache Write through in case if they are using SPI+DMA
MCAL-27007	UART read/write with DMA enabled is not working	UART	AM261x	Customer to use UART without DMA



ID	Summary	Module	Platform	Workaround
MCAL-26721	[AM263Px] WDG Example App is not working in SBL OSPI mode	WDG	AM263Px, AM261x	No-Boot mode can be used.
MCAL-30398	[Wdg] Wdg_SetTriggerCondition API does not follow AUTOSAR specification	WDG	AM263x, AM263Px, AM261x	Timeout value provided as part of initial config for both fast and slow mode will only be taken as part of SetTriggerCondition. So customer need to configure accordingly.



## 5 Known Limitations

Summary	Description	Module	Workaround	Comments
ADC-R specific checks in configurator	<p>ADC-R instance in AM263Px supports only 4 channels (other instances supports 6 channels). Also this ADC instance doesn't support HW triggers.</p> <p>These checks are not performed in the EB configurator. User need to ensure they select the right configuration based on the AM263Px TRM</p>	ADC	NA	NA

## 6 Validation

Examples are validated with SBL Prebuilt binary of MCU+SDK 10.02.00 available at [ti.com](http://ti.com)

#	Module	Validation Scope	Remarks (Refer to <i>Open Defects</i> for details)
1	ADC	Unit Functional Testing	None
2	CAN	Unit Functional Testing	None
3	CDD CMPSS	Unit Functional Testing	None
4	CDD EPWM	Unit Functional Testing	None
5	CDD FLC	Unit Functional Testing	None
6	CDD FSI TX/RX	Unit Functional Testing	None
7	CDD I2C	Unit Functional Testing	None
8	CDD IPC	Unit Functional Testing	None
9	CDD UART	Unit Functional Testing	None
10	DIO	Unit Functional Testing	None
11	DMA	Unit Functional Testing	None
12	ETH	Unit Functional Testing	ETH is not tested on RMII mode because of the limitation in current ETHTRCV, as it won't support RMII mode.
13	ETHTRCV	Unit Functional Testing	None



#	Module	Validation Scope	Remarks (Refer to <i>Open Defects</i> for details)
14	FLS	Unit Functional Testing	None
15	GPT	Unit Functional Testing	None
16	ICU	Unit Functional Testing	None
17	LIN	Unit Functional Testing	<p>Tested:</p> <ol style="list-style-type: none"><li>Internal sleep wakeup functionality.</li><li>Transmission types tested by connecting two simulated slaves on network:<ul style="list-style-type: none"><li>Master Response</li><li>Slave Response</li><li>Slave to Slave</li></ul></li></ol> <p>Not Tested:</p> <ol style="list-style-type: none"><li>Network level sleep wakeup functionality</li></ol>
18	MCU	Unit Functional Testing	Reset Reason tested only for Power on Reset, SW Warm Reset and Watchdog reset
19	PORT	Unit Functional Testing	None
20	PWM	Unit Functional Testing	None
21	SPI	Unit Functional Testing	None
22	WDG	Unit Functional Testing	None



## 7 Support

For technical support and additional assistance, visit [E2E](#) or contact local TI Field Application Engineer.

## 8 Versioning

Each package version is composed of 4 period-delimited numbers - represented here by the letters M, m, p and b [**M.m.p.b**]. The table below provides a descriptive reference regarding package version numbering.

<b>Digit</b>	<b>Meaning</b>	<b>Description</b>
1 ( <b>M</b> =Major)	Major revision	Incremented when the new version is substantially different from the previous for example, a new module added or an existing modules algorithm significantly altered.
2 ( <b>m</b> =minor)	Minor revision	Incremented when the new version has changed but not in a major way. For example, some minor changes in the API or feature set.
3 ( <b>p</b> =patch)	Patch number	Incremented for all other source code changes. This includes any packaging support code.
4 ( <b>b</b> =build)	Build number	Incremented for each release delivery to CM. Reset for any change to M, m or p

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