

VHDL Design and Implementation of an Instruction Set Architecture Z. Navabi

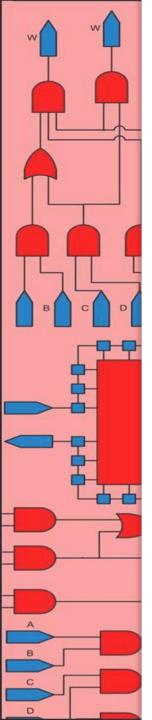
# Course on Details of Hardware of Processors (Processors)

# VHDL Design and Implementation of an Instruction Set Architecture









VHDL Design and Implementation of an Instruction Set Architecture Z. Navabi

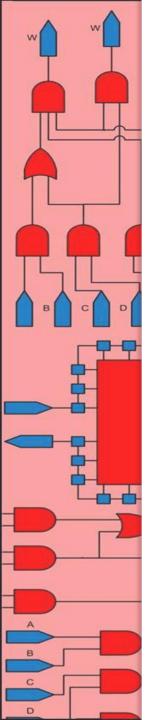
# Topic 1

# Introduction To Processing Systems

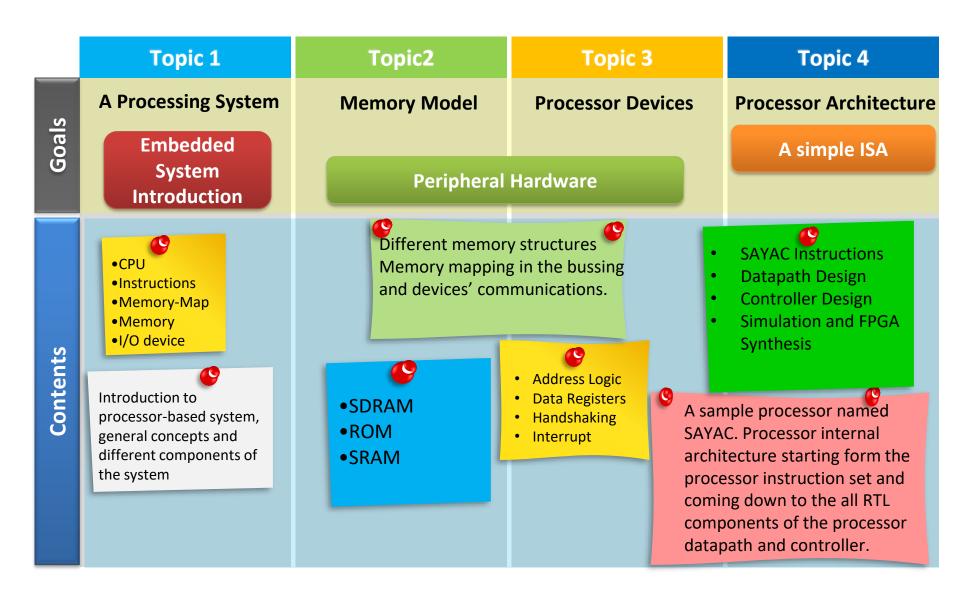
Zain Navabi

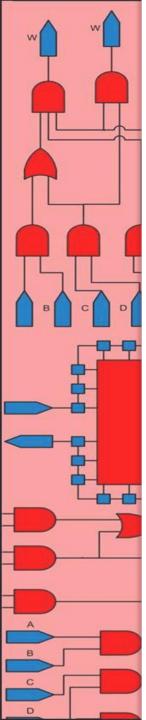






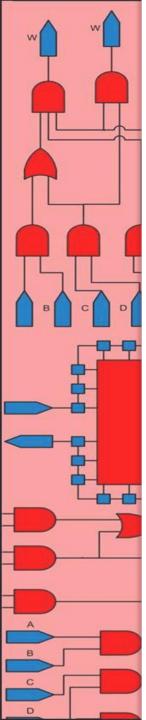
#### Course Roadmap





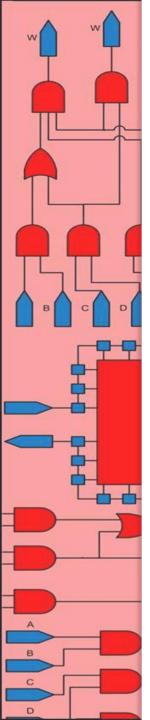
#### **Learning Outcomes:**

- Learn the general concepts of embedded systems
- Learn components of an embedded design
- Learn communication interfaces



#### **Outline:**

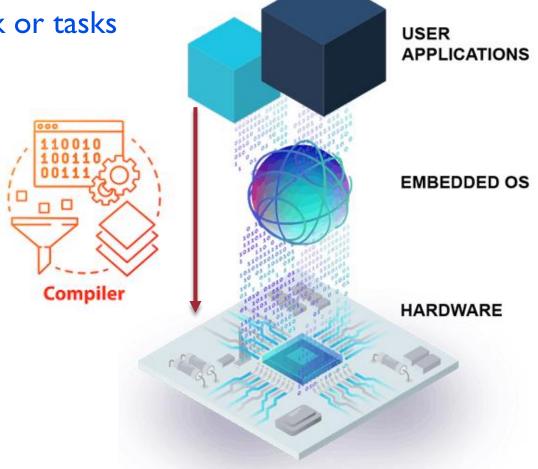
- Embedded System Layers
- Hardware Components
- Processor Execution Model
- Processor External Communication

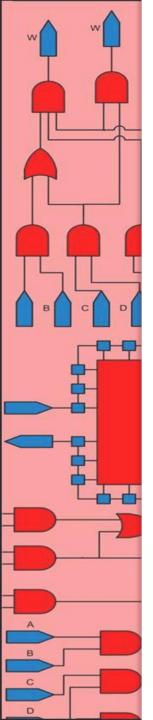


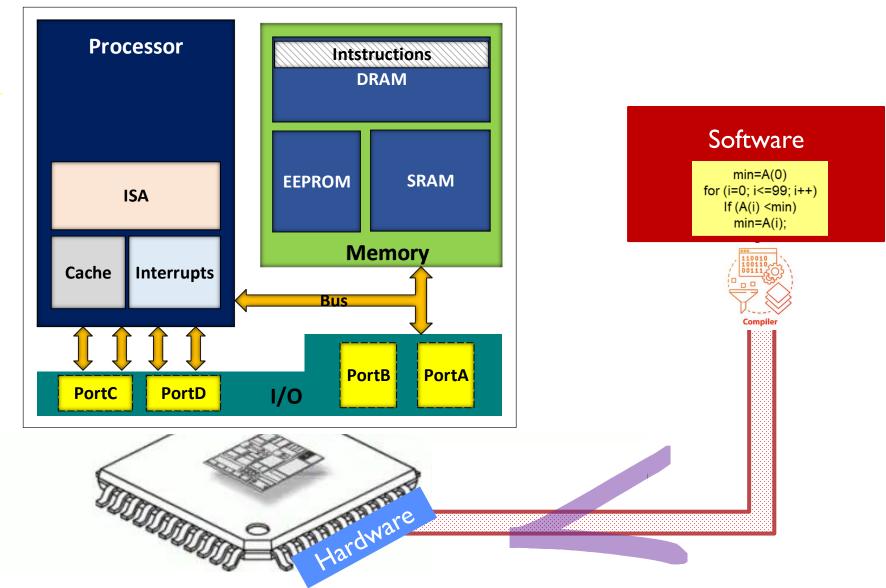
#### **Embedded System Layers**

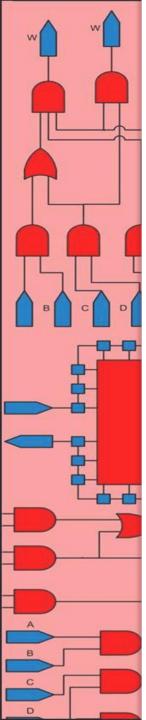
 A combination of hardware and software that is designed to carry out a certain task or tasks

- Software
- Operating System
- Hardware

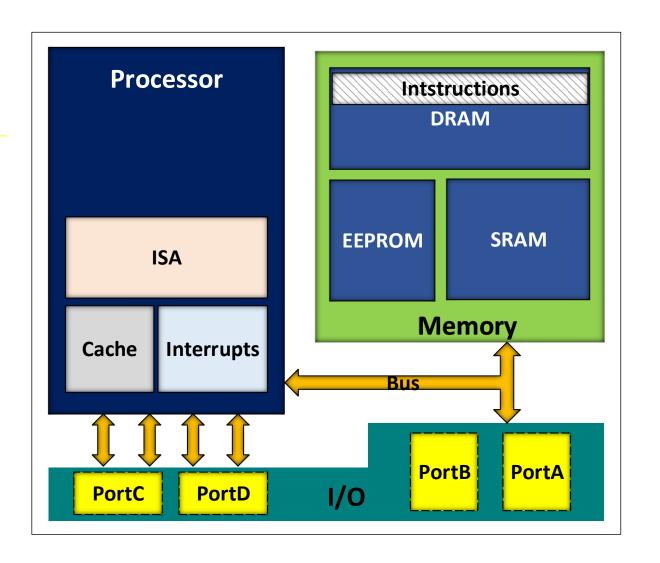


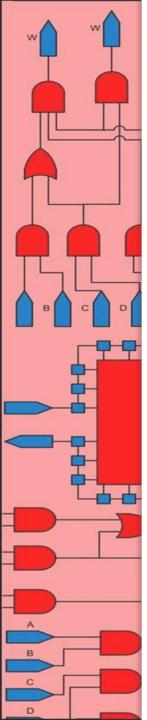






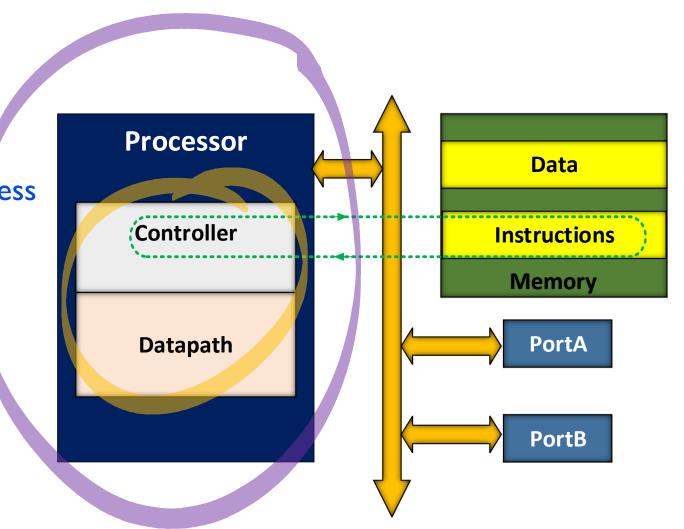
- Memory V
- Processor
- I/O Devices
- Bus |

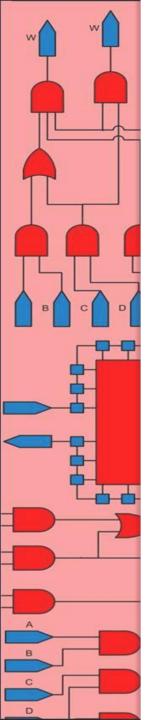


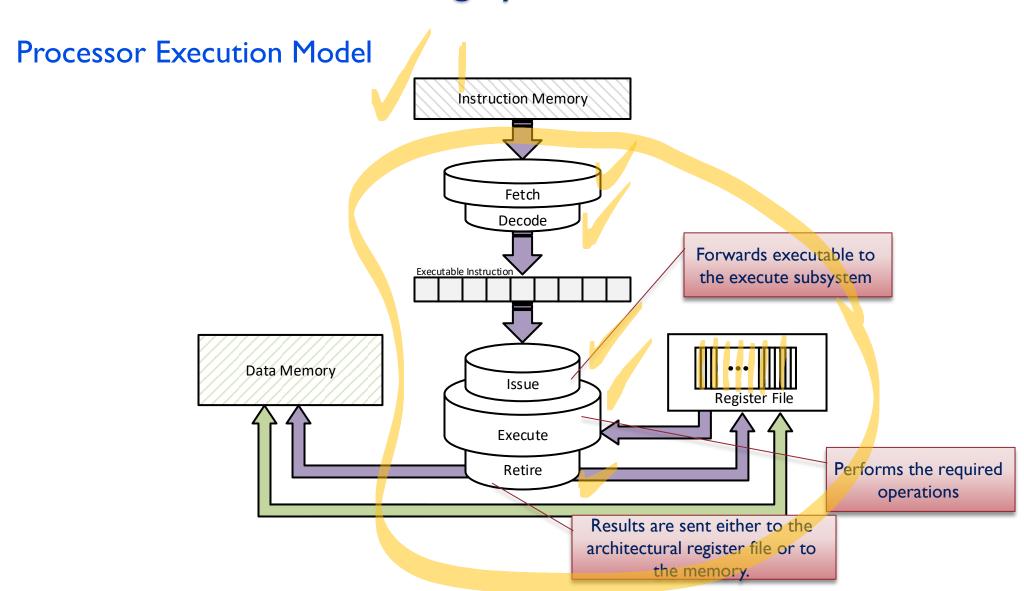


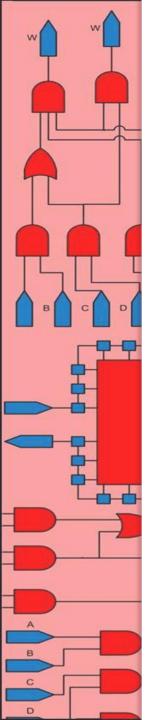
#### How processor works:

- Fetches instructions
- Executes instructions
  - Internal tasks
  - External memory access









#### Conclusion

In this topic we covered:

- General concepts in embedded systems
- Software-hardware layers
- Hardware components
- Processor execution model