D/A CONVERTER

2-Channel Serial & Binary input Floating D/A Converter

YM3016 DACKED

■ OUTLINE

The YM3016: DAC-GS is a Floating D/A converter (referred to as DAC hereafter) with the 2-channel serial and 16-bit binary input or 2's complement input. It can produce analog output (16-bit dynamic range) which has 10-bit mantissa and 7-step exponent characteristic for the input digital signal.

■ FEATURES

- 16-bit input format can select either binary or 2's complement (due to built-in floating converter logic).
- Analog output can be obtained easily by adding a buffer operational amplifier, etc.
- 16-bit wide dynamic range.
- Capable of processing PCM sound source up to 2 channels.
- Equipped with a built-in analog switch for sample hold.
- Lower noise and less harmonic distortion and outstanding temperature characteristics.
- Made by the monolithic process of highly accurate thin film resistor and CMOS.
- Package type: 16 pin plastic SOP: YM3016F

DIP: YM3016D

• +5V single power supply.

■ Electrical characteristics

1 Absolute Maximum Ratings

Item	Rating	Unit
Supply voltage	-0.3 ~ +15.0	V
High-level input voltage	$V_{\rm DD}$ $+0.3$	\mathbf{V}
Low-level input voltage	Vss - 0.3	V
Operating ambient temperature	0 - 70	°C
Storing temperature	$-50 \sim +125$	°C

2 Recommended Operating Conditions

Item	Symbol	Min		Max	Unit
Supply voltage	VDD	4.75*	5.0	5.25	v
	Vss	0	0	0	v
Input signal voltage	CLOCK				
	SD	0		VDD	v
	SMP1, 2			ļ	
	ICL				
Operating ambient temperature	Та	0	_	70	°C

③ DC Characteristics

Item	Symbol	Measuring Conditions	Min	Тур	Max	Unit
High-level input voltage	Vih		0.66VDD	_	_	V
Low-level input voltage	\mathbf{v}_{n}		_		0.30VDD	V
Input current	IIN	$V_{DD} = 5.0V$	_	_	10-3	μA
Analog output voltage	Vout			0.50V DD	_	$\mathbf{V}\mathbf{p}-\mathbf{p}$
Supply current	Idd	$V_{DD} = 5.0V$	_		6	mA

■ OUTLINE DIMENSIONS YM3016-F YM3016-D 7.8 ^{± 0.4} MARK AREA MARK AREA 2.54 TYP 19.35 TYP 10.0 TYP I 0.47 TYP **D**o 6.3 TYP 0.3 MIN 5.1 MAX 5.3 TYP 2.5 MIN 7.62 TYP 6.9 TYP UNIT : MN 0.25 UNIT : MM 0.05 MIN 0.3 MIN ■ BLOCK DIAGRAM **CLOCK** SMPI SMP2 SDo SHIFT REGISTER **TIMING** LATCH MSB **FORM** 15 FORM SELECT PRIORITY DIGITAL DETECT SHIFT MSB 91 ANALOG **MANTISSA** ICL o SHIFT-EXP 10 BIT DAC RB BIAS-R **ANALOG** ĊН I SW BC FEEDBACK-R **ANALOG** CH2 SW MP TO BUFF COM