Thaleia Dimitra Doudali

470-985-4045 • thdoudali [at] gatech.edu • www.cc.gatech.edu/~tdoudali/

EDUCATION

2015-present **Ph.D. Computer Science**
School of Computer Science, College of Computing
Georgia Institute of Technology, Atlanta, GA
Current GPA: 4.0/4.0
Expected Graduation: May 2020
Advisor: Ada Gavrilovska
Research focus: Design and deployment of memory management solutions, that optimize application performance in systems with heterogeneous memory components.

2010-2015 **Diploma**
School of Electrical and Computer Engineering
National Technical University of Athens, Greece
GPA 8.7/10.0
Advisor: Nectarios Koziris
Diploma Thesis at Computing Systems Lab NTUA.
Design and implementation of Spaten, a configurable generator of spatio-temporal and textual social media data.

PUBLICATIONS

Thaleia Dimitra Doudali and Ada Gavrilovska. 2017. CoMerge: Toward Efficient Data Placement in Shared Heterogeneous Memory Systems. In Proceedings of MEMSYS 2017, Alexandria, VA, USA, October 2–5, 2017, 11 pages. https://doi.org/10.1145/3132402.3132418

EXPERIENCE

Summer 2017 *Internship at VMware.*
Part of the CPBU Palo Alto - Distributed Resource Scheduler Team.
Worked on the design of a two-stage scheduler that enables efficient resource management of kubernetes containers deployed across virtual machines.
Mentor: Zhelong Pan, Manager: Lan Gao

Spring 2017 *Teaching Assistant.*
Helped design and grade projects for the Advanced Operating Systems class (CS6210), as well as interact with students during office hours.

Summer 2016 *Internship at Dell EMC.*
Part of the Data Domain File System - Garbage Collection team.
Worked on developing techniques that group similar files, implementing clustering algorithms and similarity estimations on a deduplication dataset.
Mentor: Abhinav Duggal, Manager: Aseem Vaid

SKILLS

Programming experience in**:** C/C++, Python, Matplotlib, kubernetes, Java, OpenMP/MPI, R, Memcached, Hadoop, HBase, SQL.

COURSE PROJECTS

- [Fall 2017] Graduate Level Computability & Algorithms (CS 6505)
- [Spring 2017] Various modifications of a DRAM memory simulator and extension to simulate hybrid KNL for the Advanced Topics in Memory Systems class (ECE 7103)
- [Fall 2016] Various modifications of a MIPS ISA simulator ("secs") for the High Performance Computer Architecture class (CS 6290)
- [Spring 2016] Comparative analysis of Apache Spark against other competing frameworks for the Advanced Internet Computing class (CS 6675)
- [Fall 2015] Class projects for Advanced Operating Systems (CS 6210)
    - Implementation of GTThreads --a preemptive user-level thread package with an API similar to pthreads.
    - Implementation of spin barriers using OpenMP and MPI.
    - Implementation of an RPC-Based proxy server using Apache Thrift.
    - Implementation of a recoverable virtual memory system using log files and transaction semantics.
- [2015] Implementation of lectical, syntax and semantics analysis, developed in C, for the language Pazcal (which is a subset C) as part of the course "Compilers".
- [2015] Software design and development of an auction server, implemented in Java for the course "Advanced Topics in Database Systems".
- [2015] Addition of new functionalities to the SIP Communicator and JAIN SIP Proxy server implemented in Java for the course "Software Engineering".
- [2014] Modification of a device driver for Linux and implementation of a virtual cryptographic device for QEMU-KVM for the course "Operating Systems Laboratory".
- [2014] Implementation of a hotel reservation database system and interface using JSP, HTML, SQL for the course "Databases" (3000 LoC).