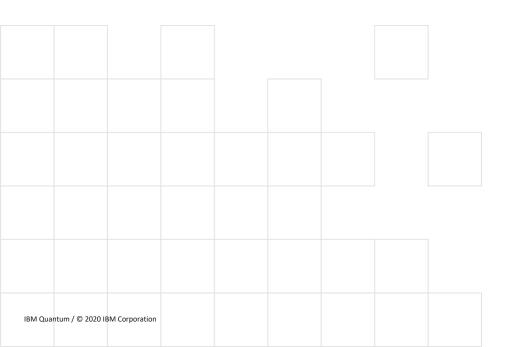
Good First Issues in Qiskit-Terra

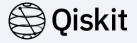
Tharrmashastha SAPV

Mentor: Luciano Bello



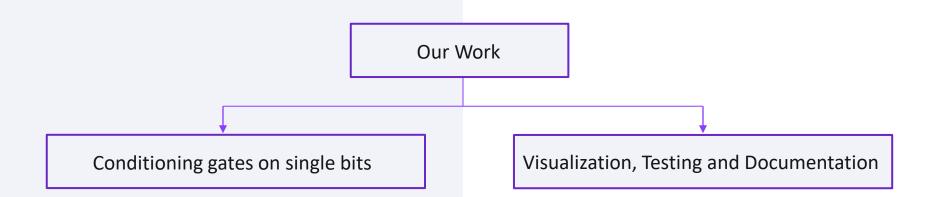
```
from qiskit import QuantumCircuit, execute
from giskit import Aer, IBMQ
from qiskit.providers.aer.noise import NoiseModel
# Choose a real device to simulate from IBMQ provider
provider = IBMQ.load_account()
backend = provider.get backend('ibmg vigo')
coupling_map = backend.configuration().coupling_map
# Generate an Aer noise model for device
noise model = NoiseModel.from backend(backend)
basis_gates = noise_model.basis_gates
# Generate 3-qubit GHZ state
num_qubits = 3
circ = QuantumCircuit(3, 3)
circ.h(0)
circ.cx(0, 1)
circ.cx(1, 2)
circ.measure([0, 1, 2], [0, 1 ,2])
# Perform noisy simulation
backend = Aer.get_backend('qasm_simulator')
job = execute(circ, backend,
             coupling_map=coupling_map,
              noise_model=noise_model,
              basis_gates=basis_gates)
result = job.result()
print(result.get_counts(θ))
```

Goal of the Project

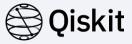


Fix Github issues of Qiskit Terra, as many as possible!









```
#Red
Issues caused (#6475):

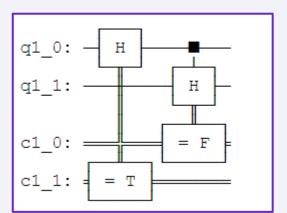
    Issue

                                                                          #Green - PR
  → Text, latex and MPL drawers break. (#6261, #6248, #6259)
                                                                          #Blue - To be fixed
  → circuit to dag() method breaks. (#6018)
  → qc.qasm() method breaks. (Will update with QASM3.0)
  → qc.depth() breaks. (#6476)
  → qc.num connected components() breaks.
circuit to instruction() breaks.
  assemble and disassemble methods break.
  check wires list() and substitute node with dag() methods of dagcircuit break.
  is same c conf() method in template matching of optimization breaks.
```

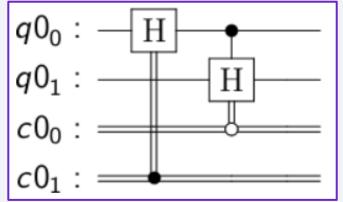




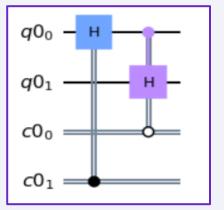
Text(#6261)



Latex(#6248)



MPL(#6259)

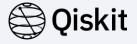




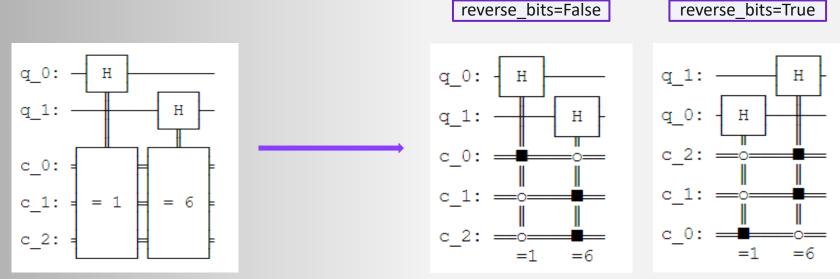


Inconsistency in drawing classical control in text drawer when cregbundle=False (#6290, #6370) → The ordering of condition bullets are incorrect when cregbundle=True and reverse bits=True (#6370) → Incorrect drawing of custom instructions involving classical bits when using text drawer (#6178, #6242) → Latex drawer ignores classical bits of custom instructions(#3006, #3202, #6240) Active wires of custom instructions unidentifiable in latex drawings (#2092, #6153) → Incorrect drawing of custom instructions with classical bits when using MPL drawer (#3006, #3201, #6339) Testing latex drawer using binder (#6371, #6450) Unroller raises unclear error when reaching a node without definition (#5840, #6235)

Visualization, Testing and Documentation:



Inconsistency in drawing classical control in text drawer when cregbundle=False (#6290, #6370) The ordering of condition bullets are incorrect when cregbundle=False and reverse bits=True (#6370)

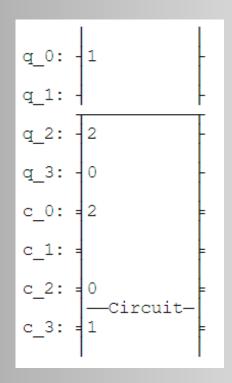


reverse_bits=True

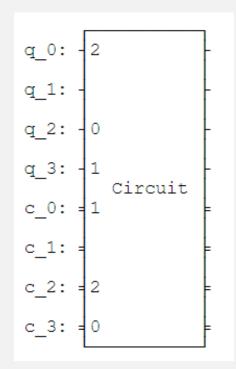
Visualization, Testing and Documentation:



Incorrect drawing of custom instructions involving classical bits when using text drawer (#6178, #6242)





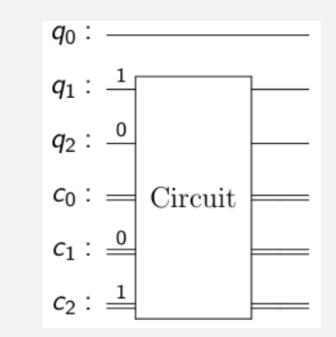


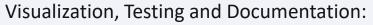
Visualization, Testing and Documentation:

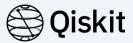


Latex drawer ignores classical bits of custom instructions(#3006, #3202, #6240)

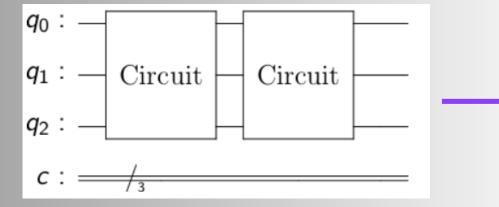
q_0 : ————————————————————————————————————
q_1 : Circuit
q ₂ :
<i>c</i> ₀ :
<i>c</i> ₁ :
<i>c</i> ₂ :

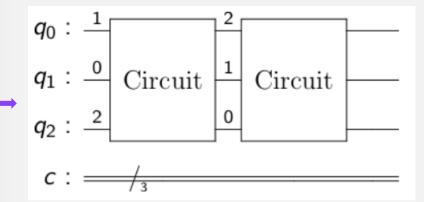




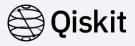


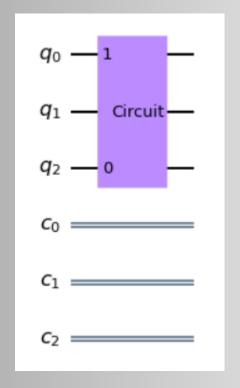
Active wires of custom instructions unidentifiable in latex drawings (#2092, #6153)

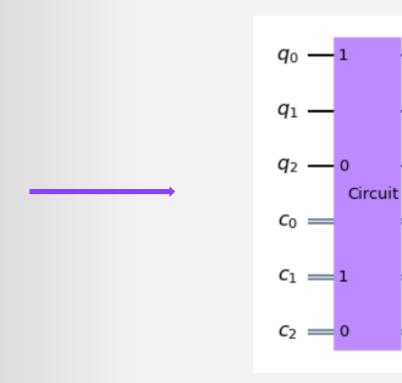




Visualization, Testing and Documentation: Incorrect drawing of custom instructions with classical bits when using MPL drawer (#3006, #3201, #6339)





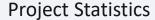


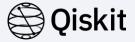




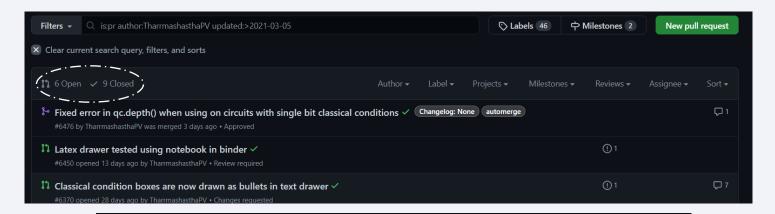
12

Inconsistency in drawing classical control in text drawer when cregbundle=False (#6290, #6370) → The ordering of condition bullets are incorrect when cregbundle=True and reverse bits=True (#6370) → Incorrect drawing of custom instructions involving classical bits when using text drawer (#6178, #6242) → Latex drawer ignores classical bits of custom instructions(#3006, #3202, #6240) Active wires of custom instructions unidentifiable in latex drawings (#2092, #6153) → Incorrect drawing of custom instructions with classical bits when using MPL drawer (#3006, #3201, #6339) Testing latex drawer using binder (#6371, #6450) Unroller raises unclear error when reaching a node without definition (#5840, #6235)



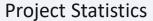


Total number of issues addresses and/or closed: 15



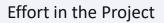
Classical conditioning on single classical bits #1160

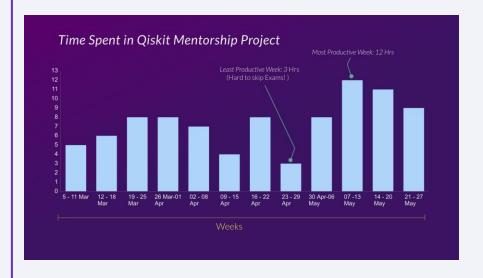
(F) Closed quantumjim opened this issue on Oct 30, 2018 · 9 comments



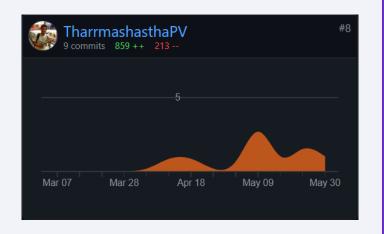


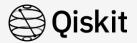
14





Pay-off of the Effort







Thank You