

COMP262-01

CPU SIM Project: a PEP8 Virtual Machine SIMULATION–PART6 CODING of the SIMULATION PROCESS/PROGRAM

Continue with and complete the coding of the Simulation program.

For this part (6), we will need the results of part5, a partial completion of the instruction processing methods.

**NOTE: YOU MAY NOT USE ANY ADDITIONAL JAVA LANGUAGE
OBJECTS/METHODS FOR THE REMAINDER OF THE CODE!!!!**

We will now proceed to implement these remaining instruction processing methods (‘invoked’ from the EI):

NOTr	Bitwise invert r
NEGr	Negate r
ASLr	Arithmetic shift left r
ASRr	Arithmetic shift right r
ROLr	Rotate left r
RORr	Rotate right r

At this point, the simulation will produce the complete update of the CPU State (Status bits, Registers, Memory), for those instructions that are implemented. The remaining instructions will only show a partial update.

Deliverables

The source for the program, with DETAILED comments indicating WHAT parts of the program are not working and what is left to be coded.