

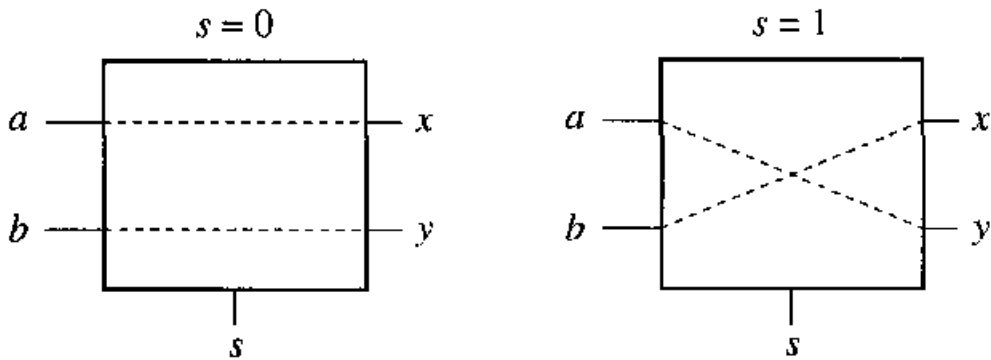
COMP262-01 LAB8-2, CH20 Digital Logic and Gates

NOTE: You may choose to use free hand drawing in preparing the requested Diagram and then 'scanning' and pasting into a WORD docx and/or converting it to a .pdf...

The block diagram depicts a three-input(a, b, s), two-output(x,y) combinational switching circuit.

If 's' is 0, the input 'a' is routed to 'x' and 'b' is routed to 'y'.

If 's' is 1, the input 'a' is routed to 'y' and 'b' is routed to 'x'.



To do: Draw a Logic Gate Diagram for the circuit implementation using gates from the functionally complete set AND/OR/NOT...