

## COMP262-01

# CPU SIM Project: a PEP8 Virtual Machine SIMULATION–PART4 CODING of the SIMULATION PROCESS/PROGRAM

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Continue with the coding of the Simulation program.

For this part (4), we will need the results of part 3, the completed CO, FO and WO stage methods.

We will now proceed to implement the EX stage method, which will identify and ‘call’ the appropriate individual instruction processing methods.

We will also implement a ‘shell’ for each of the instruction processing methods; the body of these methods will consist of JUST a simple message stating “XYZ method invoked.”

This will allow us to verify that the EX stage method is properly identifying and invoking each of the instruction processing methods.

At this point, the simulation will still produce only a partial update of the CPU State, showing the results of FETCHING, DECODING, CALCULATING OPERAND’s effective addresses, FETCHING/WRITING OPERANDS, and ‘invoking’ the methods(shell’s) that process the instructions.

### **Deliverables**

The source for the program, with DETAILED comments indicating WHAT parts of the program are not working and what is left to be coded.