COMP262-01
CPU MC Project: a PEP8 CU Micro code implementation
PART2(MC2) CODING of the:
Load/Store and Subtract/Compare Instructions

Continue with the micro coding.

In MC1, we have implemented the generic CO/FO steps for the 4 addressing modes.

For this part (MC2), we will implement the: LD/LDBYTE, ST/STBYTE and SUB/CP Instructions

We have provided a 'shell' for the micro code, which includes the RTL as comments.

Complete and verify that the required micro code produces the correct results, which are deterministic.

Deliverables

The completed micro code;

YOU MUST PROVIDE DETAILED comments indicating:

1) WHAT parts of the implementation are coded but not working and

2) WHAT is left to be coded.