

# **How To Play**

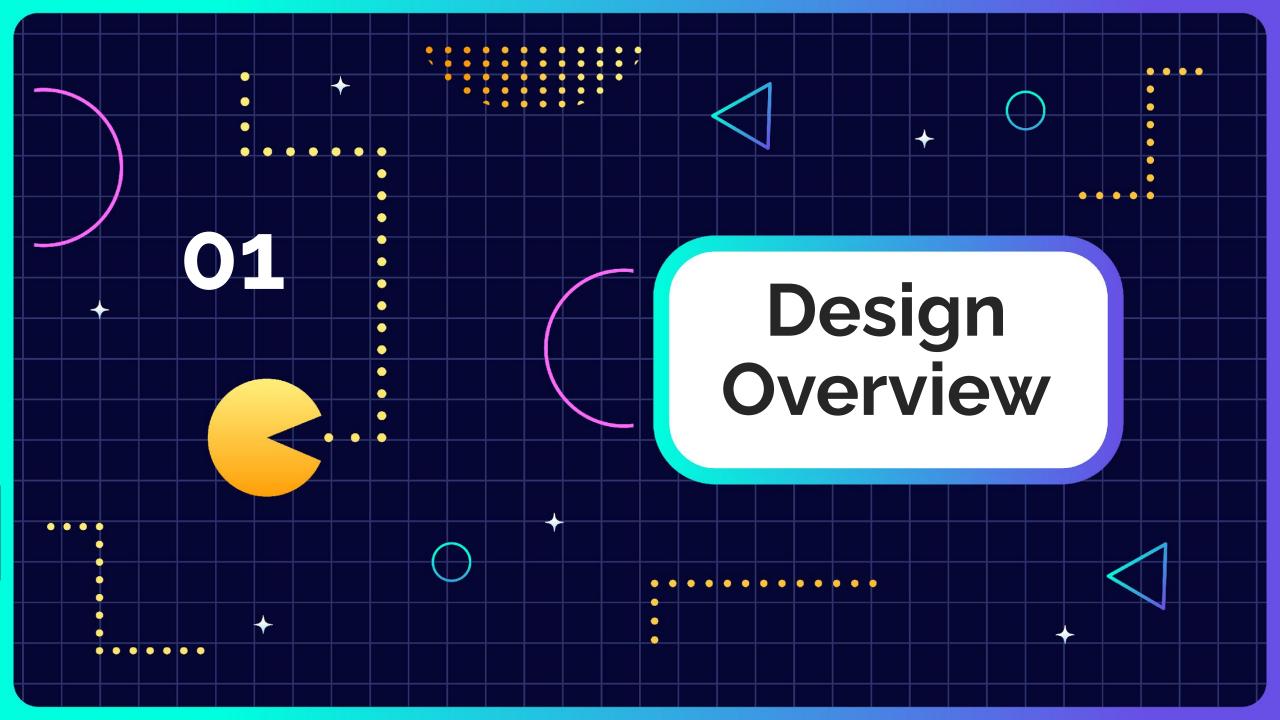
- 1. Select a 3-character username with the joystick (0-9, A-F).
- 2. Control Pac-Man with left/right/up/down joystick.
- 3. Navigate the maze and attempt to collect all the dots.
- 4. Each time a ghost catches Pac-Man, he loses a life.
- 5. Lose 3 lives and it's GAME OVER!

## Introduction

- Binary numbers (1s and 0s) are the basis for all digital electronics.
- Designers can utilize logic gates to form decision-making circuits.
- Basic operations include AND, NOT, & OR.
- State Machines prescribe certain outputs based on a range of input conditions.
- User inputs (4-way joystick) and internal signals (processor clock) drive outputs (LEDs and score/HDMI displays).
- Memory allows the system to read game data (map/player).

## **Technical Details**

- All the driver modules for this project were written in VHSIC Hardware
   Description Language (VHDL).
- Programming was completed in the **Vivado** integrated development environment.
- Team collaboration was aided by a cloned GitHub repository.
- Our Xilinx Spartan-7 XC7S50-CSGA324 Field Programmable Gate
   Array (FPGA) interprets VHDL instructions as a physical circuit.



## Game Design Goals

01

3-Character username selection using external joystick 02

Increasing difficulty over time (chase vs. scatter)

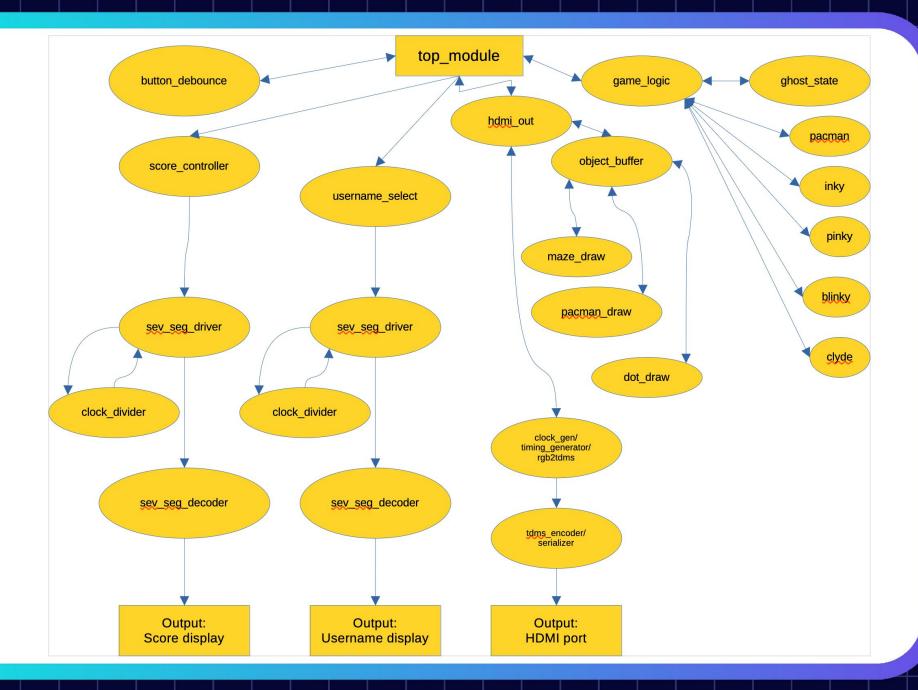
03

Score counting on seven segment display 04

Video output on 480p LCD display (VGA-to-HDMI) 05

Game continuation until all 3 lives are depleted.

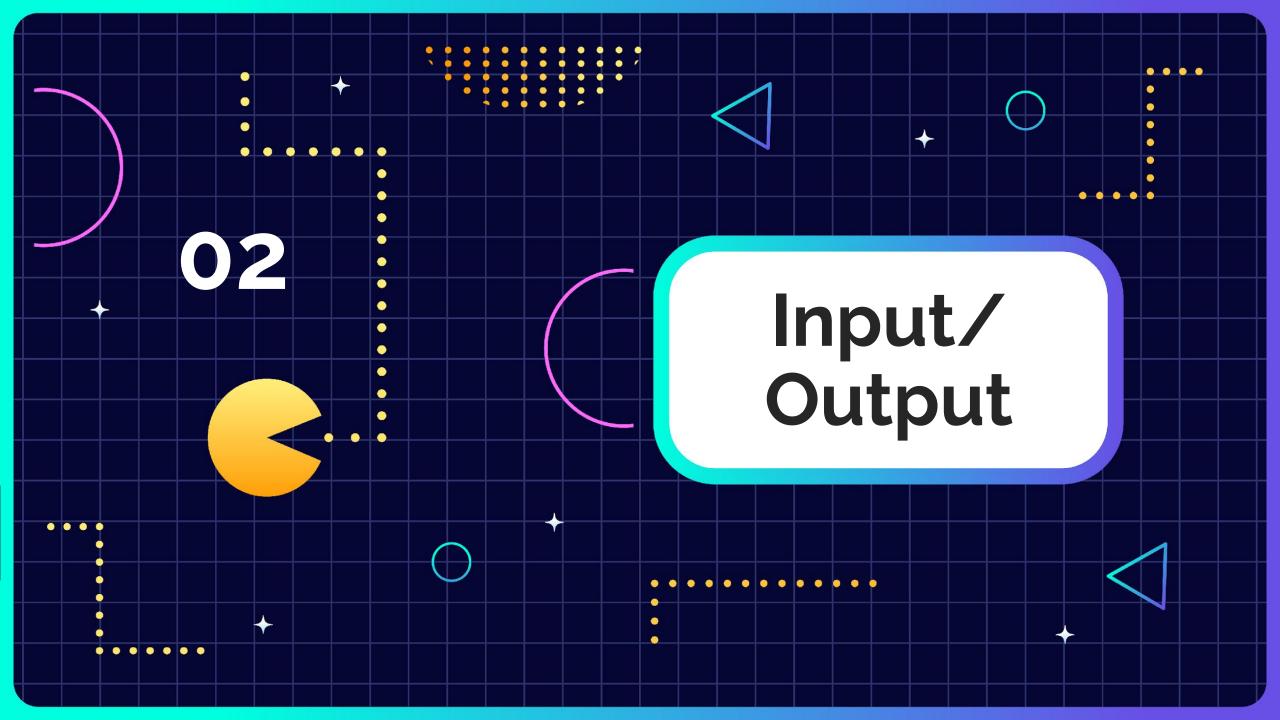
## System Flow Chart



## Pac-Man & Ghost ROM

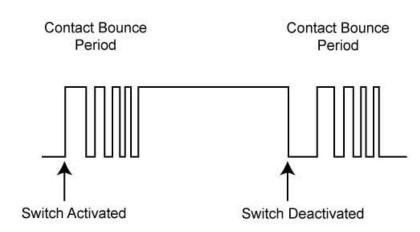
"0000**111111**0000" "000**11111111**000" "01111111001100" "00111111001110" "00000**111111111**" "00000**111111111**" "000**11111111**000" "0000**111111**0000"

"000000**111**00000" "000**11111111**000" "00**1111111111**00" "01100011000110" "0**11**000**11**000**11**0" "11100011000111" "**11**00**11**00**11**00**11**" "10000100100001"



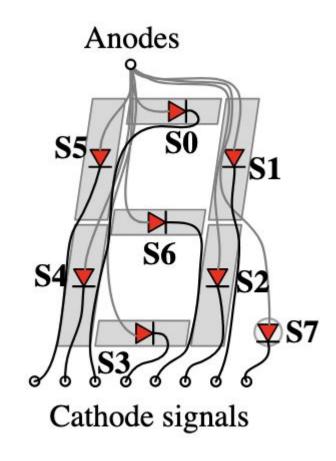
# Joystick Input

- Due to the circuit's electrical connection, some inconsistency ("bouncing") of the input signal is to be expected.
- A debouncer module receives raw input and delivers a stable output to the internal logic module.



# Seven Segment Display

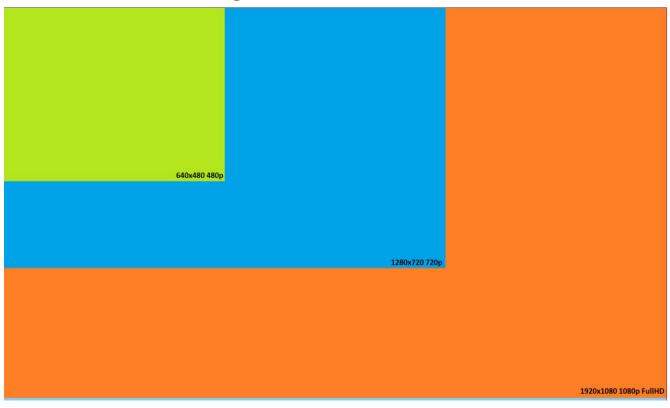
- This display converts a binary input into a user-readable hexadecimal output.
- The anodes and cathodes of each LED segment is controlled by a decoder module.
- The custom username is stored in memory, and the displayed score is incremented every time
   Pac-Man eats a pellet.



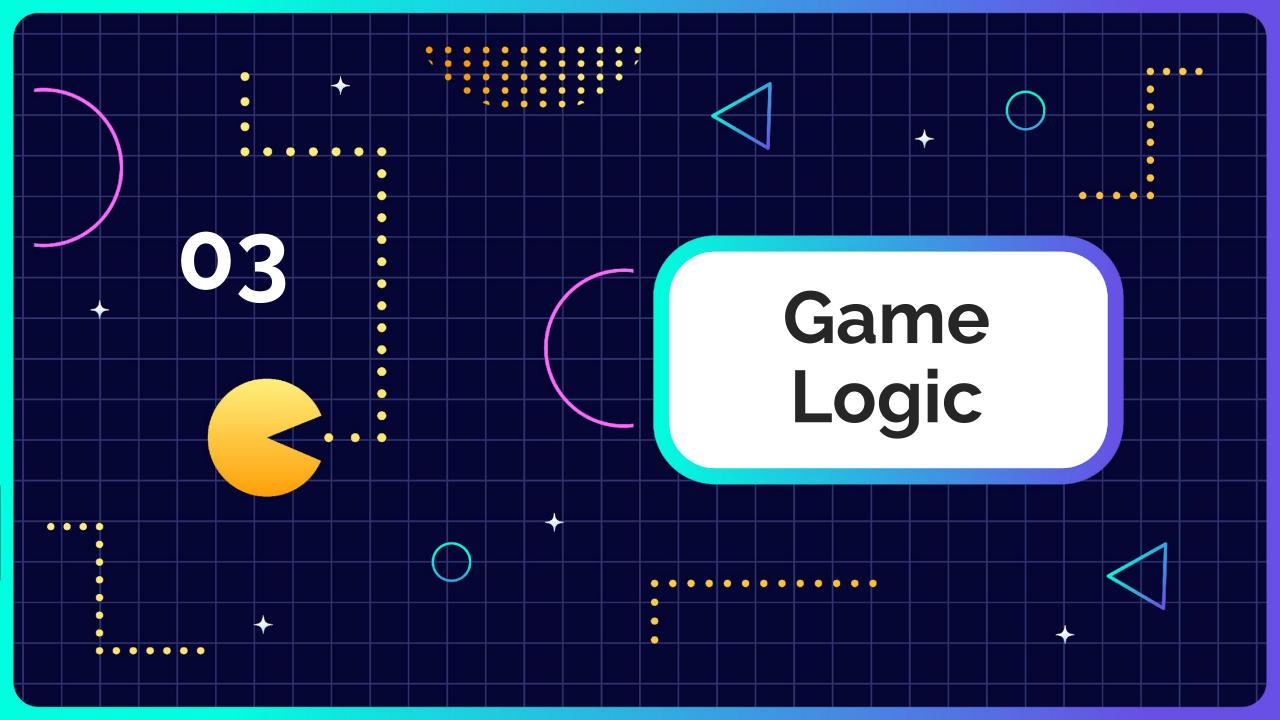
## Video Graphics Array (VGA)

#### Our VGA graphics use these signals:

- A 25MHz pixel clock, which defines when pixels are written on the display.
- A 125MHz clock, which aids in driver internal processing.
- HSYNC determines the start and end of each horizontal line of pixels
- VSYNC determines the start and end of each frame or vertical refresh.
- Red, Green and Blue (RGB) control the current pixel's color.



A challenge our team encountered was implementing a driver capable of providing VGA signals to a VGA-to-HDMI module.



#### **Ghost Al**

Each Ghost has a different way of catching Pac-Man



Blinky follows the inverted set course of Inky around the map.

## Inky

Inky follows a set course around the maze.



#### **Pinky**



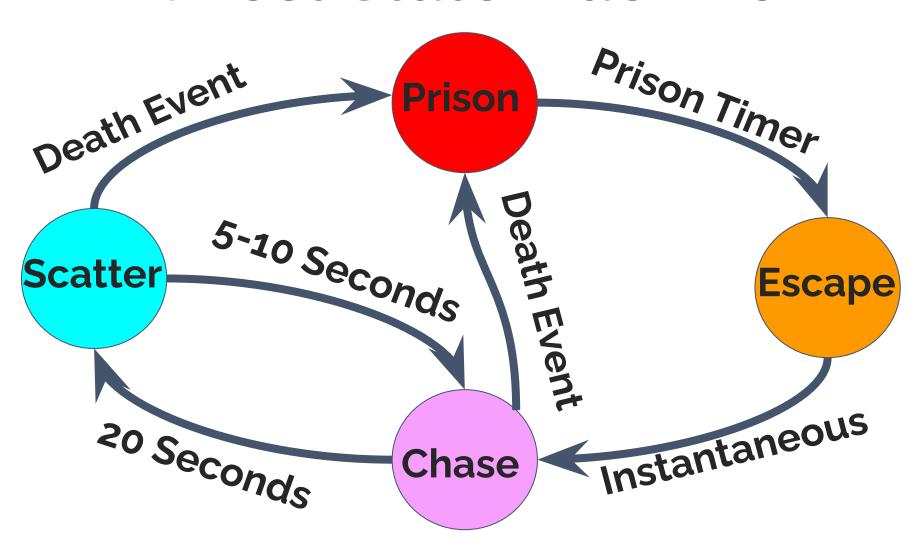
Pinky hunts Pac-Man prioritizing Pac-Man's x coordinates first in a direct manner.



Clyde hunts
Pac-Mans y
coordinates first,
essentially being the
inverse of Pinky.



## **Ghost State Machine**



# Map Logic

- Collisions are detected by comparing (X,Y) coordinates of game objects (walls, players, and pellets).
- Collision events determine game actions such as stoppage of movement, death, score increase.

