

STAGE 7.1 – POWER & PDN ONLY

This page defines power rails and references only.
No address/command, data, SPD, or connector signals
may appear on this page.

This page must be review-clean before any other
schematic pages are created.

A

A

B

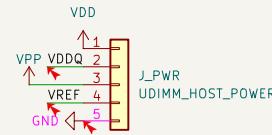
B

C

C

D

D



No CA, Data, SPD, or Connector signals allowed

Stage 7.1 – Power Schematic

OMI

Sheet: /

File: omi_v1_power.kicad_sch

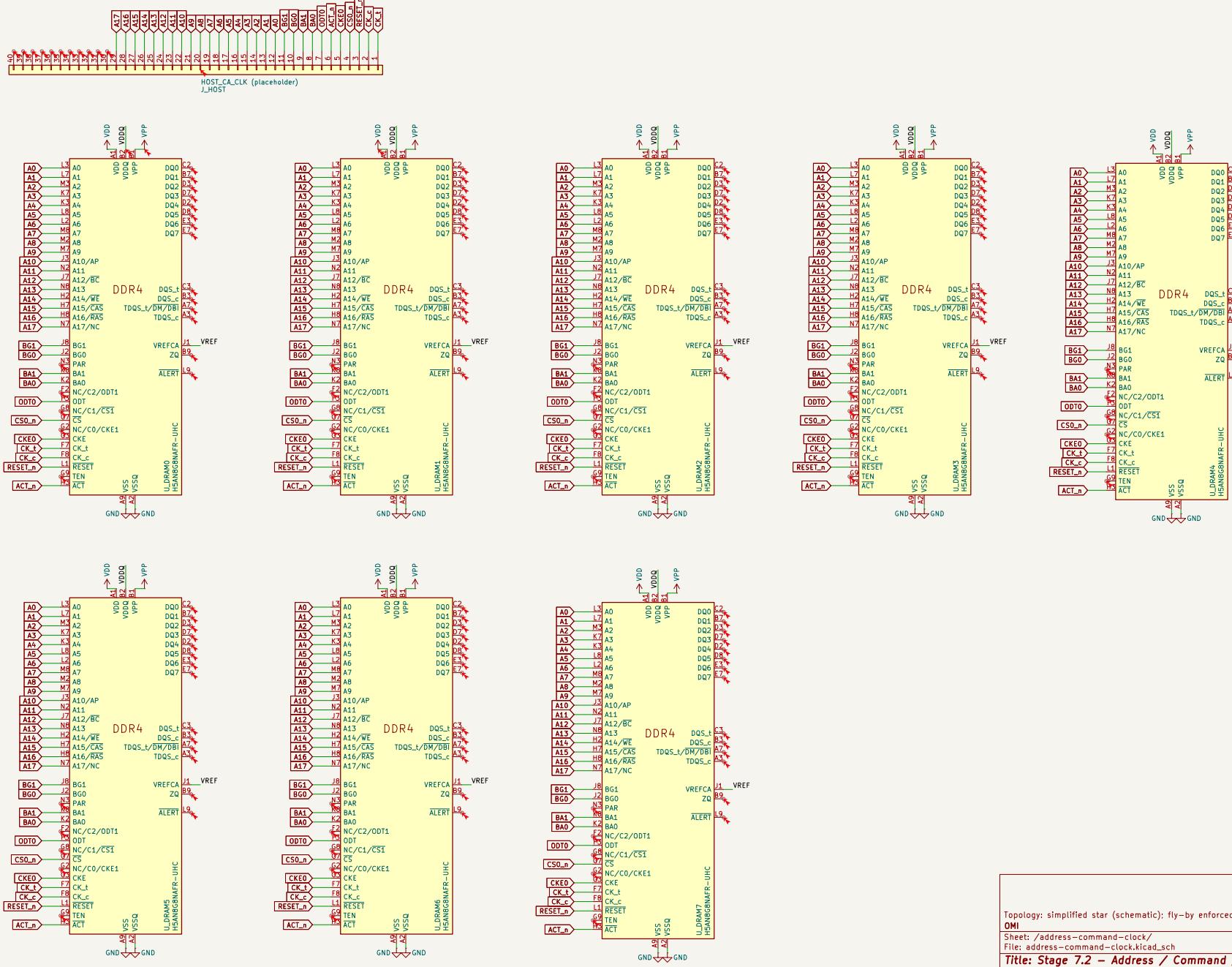
Title: Power & PDN

Size: A4 Date: 2026-01-27

KiCad E.D.A. 9.0.6

Rev: 1.1.0

Id: 1/2



Topology: simplified star (schematic); fly-by enforced at layout
OMI

Sheet: /address-command-clock/
File: address-command-clock.kicad_sch

Title: Stage 7.2 – Address / Command / Clock
Size: A3 | Date: 2026-01-27
KiCad E.D.A. 9.0.6

Rev: 1.0.0
Id: 2/2