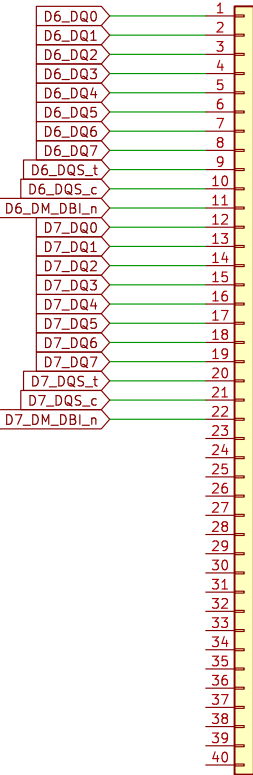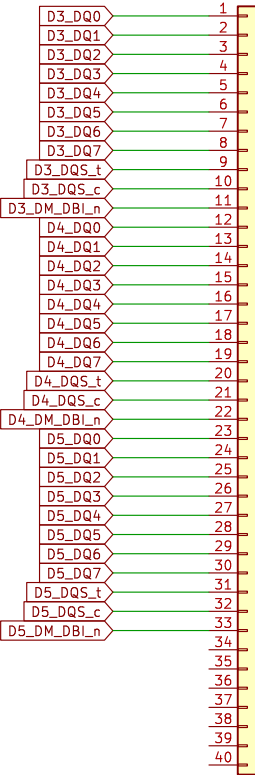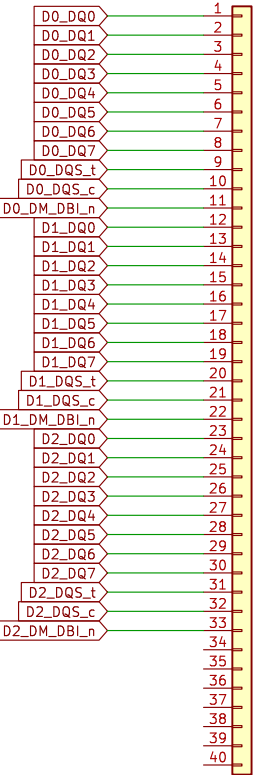Stage 7.3 – DQ/DQS (simplified star)
This sheet defines DQ/DQS net names and byte–lane grouping.
Physical routing and fly–by/order handled at layout stage.

J_HOST_DQ_A	J_HOST_DQ_B	J_HOST_DQ_C
HOST_DQ_DQS (placeholder)	HOST_DQ_DQS (placeholder)	HOST_DQ_DQS (placeholder)

Pin	A	B	C
1	D0_DQ0	D3_DQ0	D6_DQ0
2	D0_DQ1	D3_DQ1	D6_DQ1
3	D0_DQ2	D3_DQ2	D6_DQ2
4	D0_DQ3	D3_DQ3	D6_DQ3
5	D0_DQ4	D3_DQ4	D6_DQ4
6	D0_DQ5	D3_DQ5	D6_DQ5
7	D0_DQ6	D3_DQ6	D6_DQ6
8	D0_DQ7	D3_DQ7	D6_DQ7
9	D0_DQS_t	D3_DQS_t	D6_DQS_t
10	D0_DQS_c	D3_DQS_c	D6_DQS_c
11	D0_DM_DBI_n	D3_DM_DBI_n	D6_DM_DBI_n
12	D1_DQ0	D4_DQ0	D7_DQ0
13	D1_DQ1	D4_DQ1	D7_DQ1
14	D1_DQ2	D4_DQ2	D7_DQ2
15	D1_DQ3	D4_DQ3	D7_DQ3
16	D1_DQ4	D4_DQ4	D7_DQ4
17	D1_DQ5	D4_DQ5	D7_DQ5
18	D1_DQ6	D4_DQ6	D7_DQ6
19	D1_DQ7	D4_DQ7	D7_DQ7
20	D1_DQS_t	D4_DQS_t	D7_DQS_t
21	D1_DQS_c	D4_DQS_c	D7_DQS_c
22	D1_DM_DBI_n	D4_DM_DBI_n	D7_DM_DBI_n
23	D2_DQ0	D5_DQ0	
24	D2_DQ1	D5_DQ1	
25	D2_DQ2	D5_DQ2	
26	D2_DQ3	D5_DQ3	
27	D2_DQ4	D5_DQ4	
28	D2_DQ5	D5_DQ5	
29	D2_DQ6	D5_DQ6	
30	D2_DQ7	D5_DQ7	
31	D2_DQS_t	D5_DQS_t	
32	D2_DQS_c	D5_DQS_c	
33	D2_DM_DBI_n	D5_DM_DBI_n	
34			
35			
36			
37			
38			
39			
40			