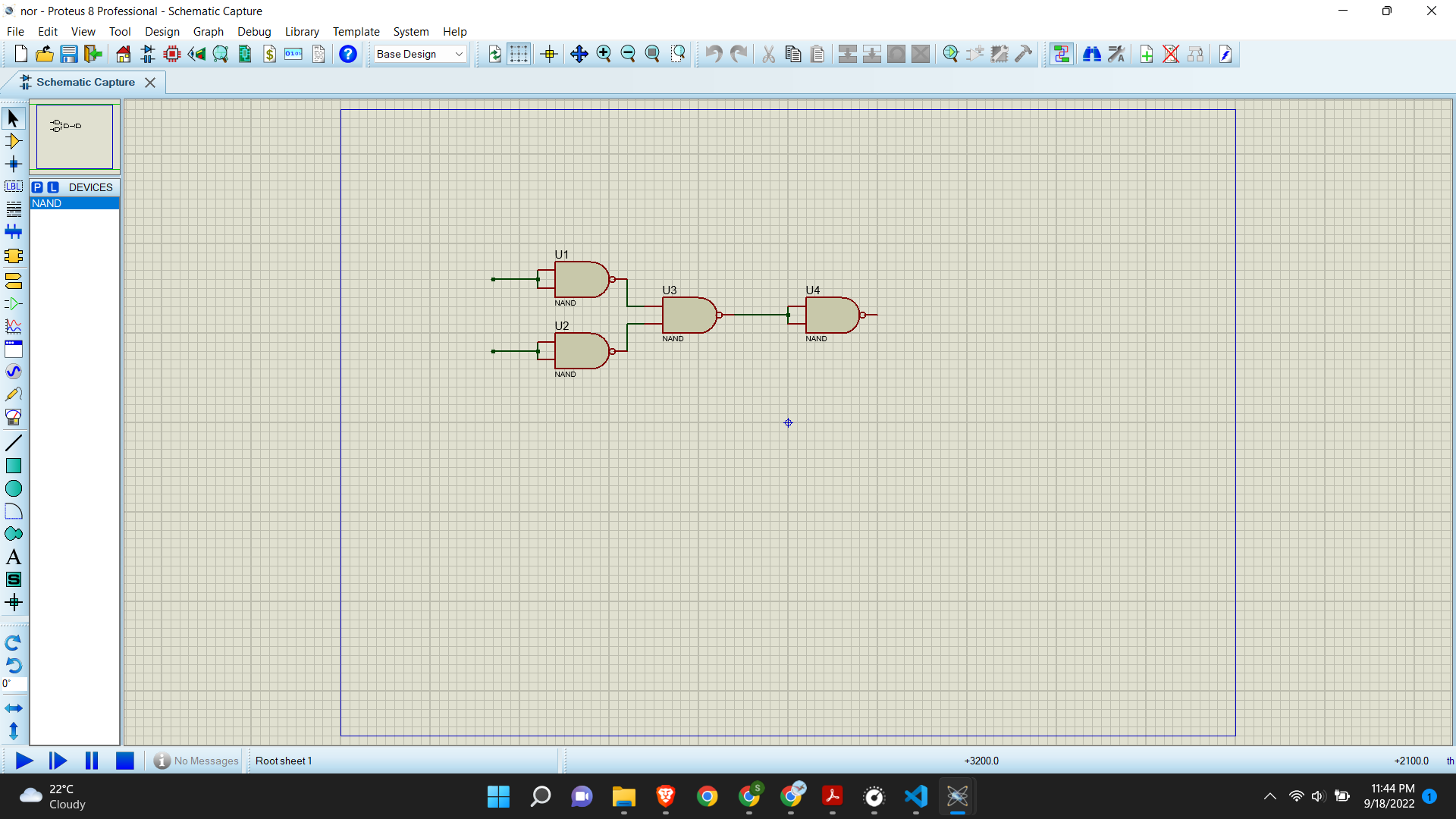
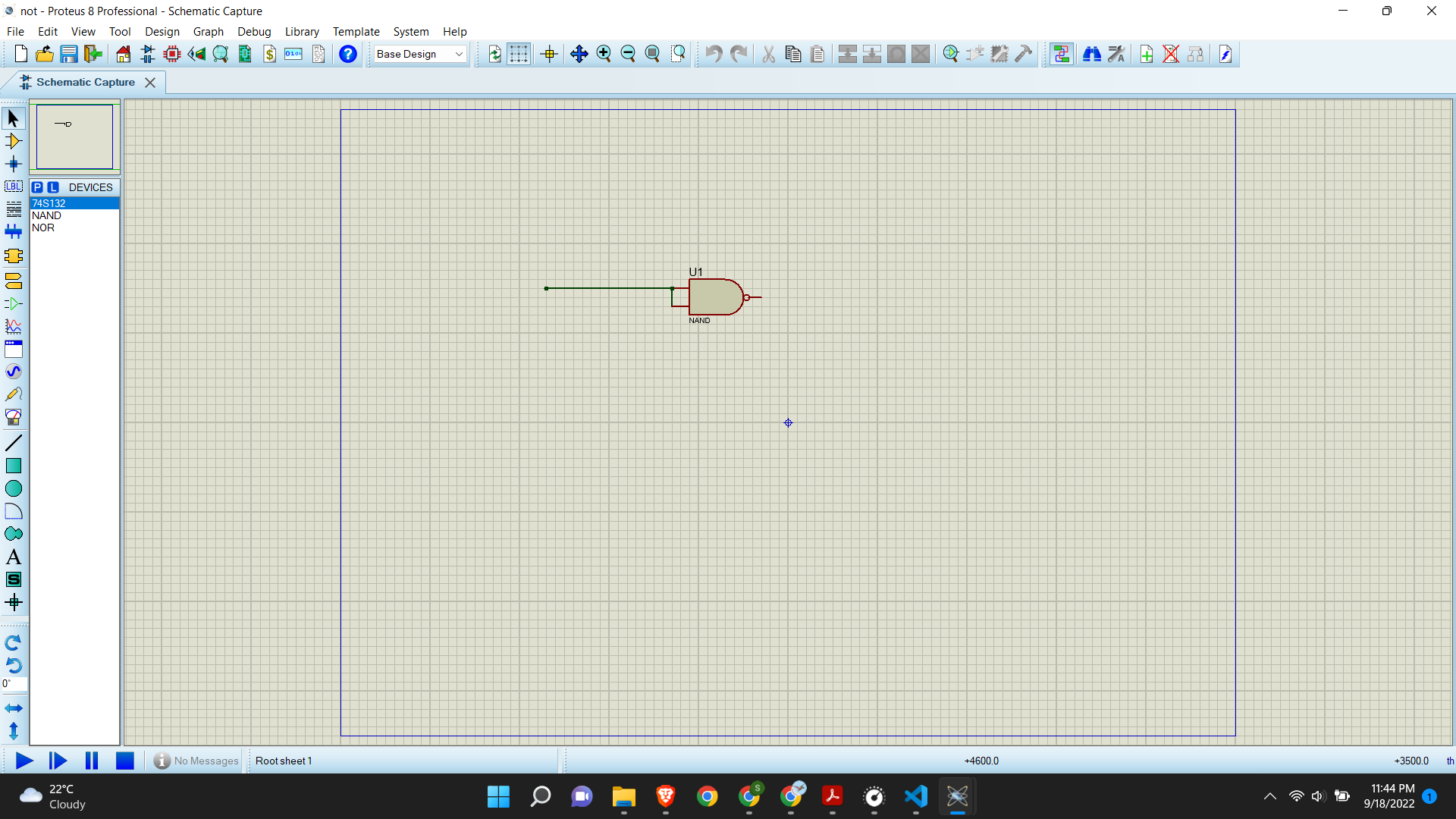
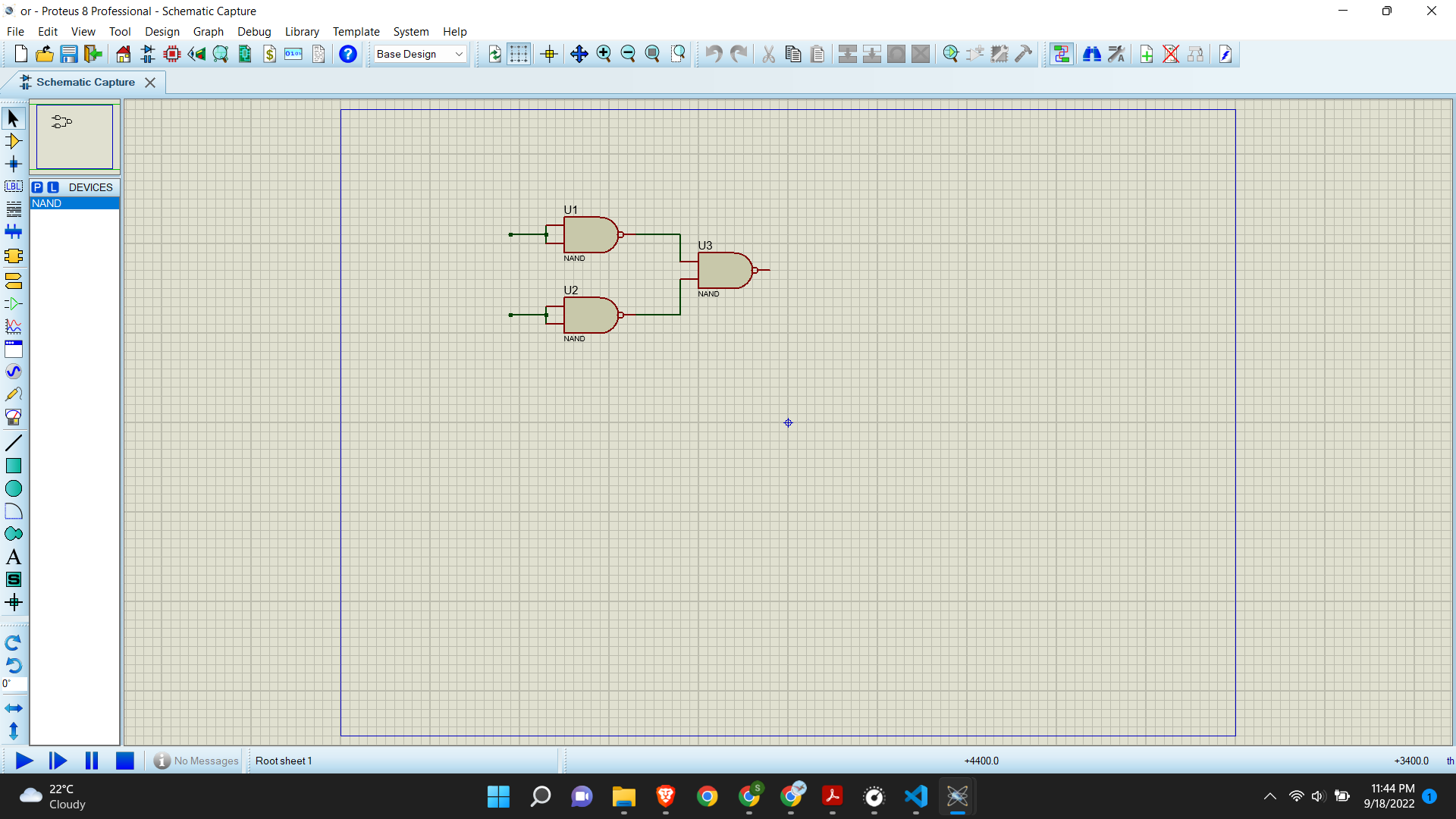
**GATES USING NAND GATE**



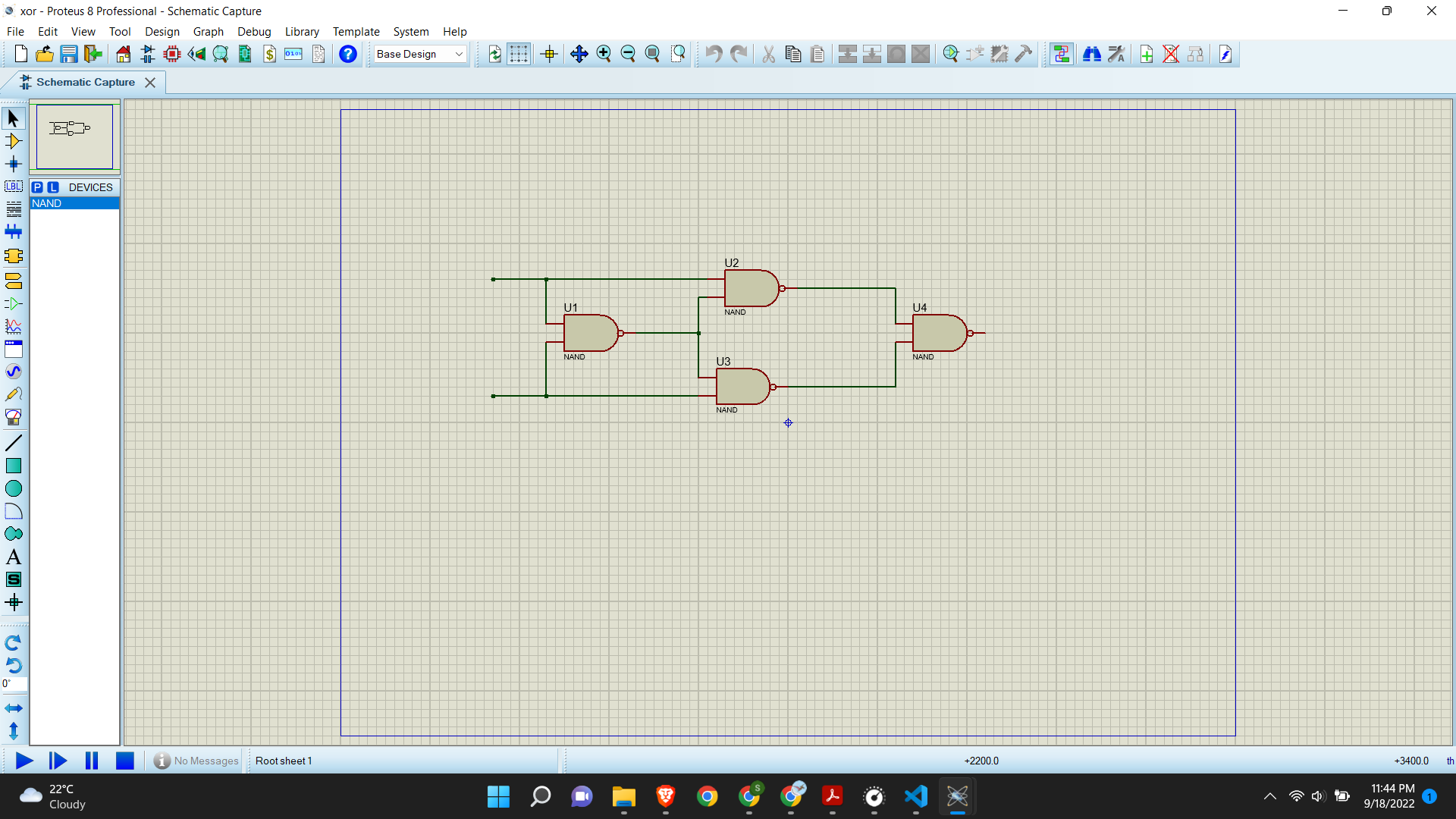
XOR



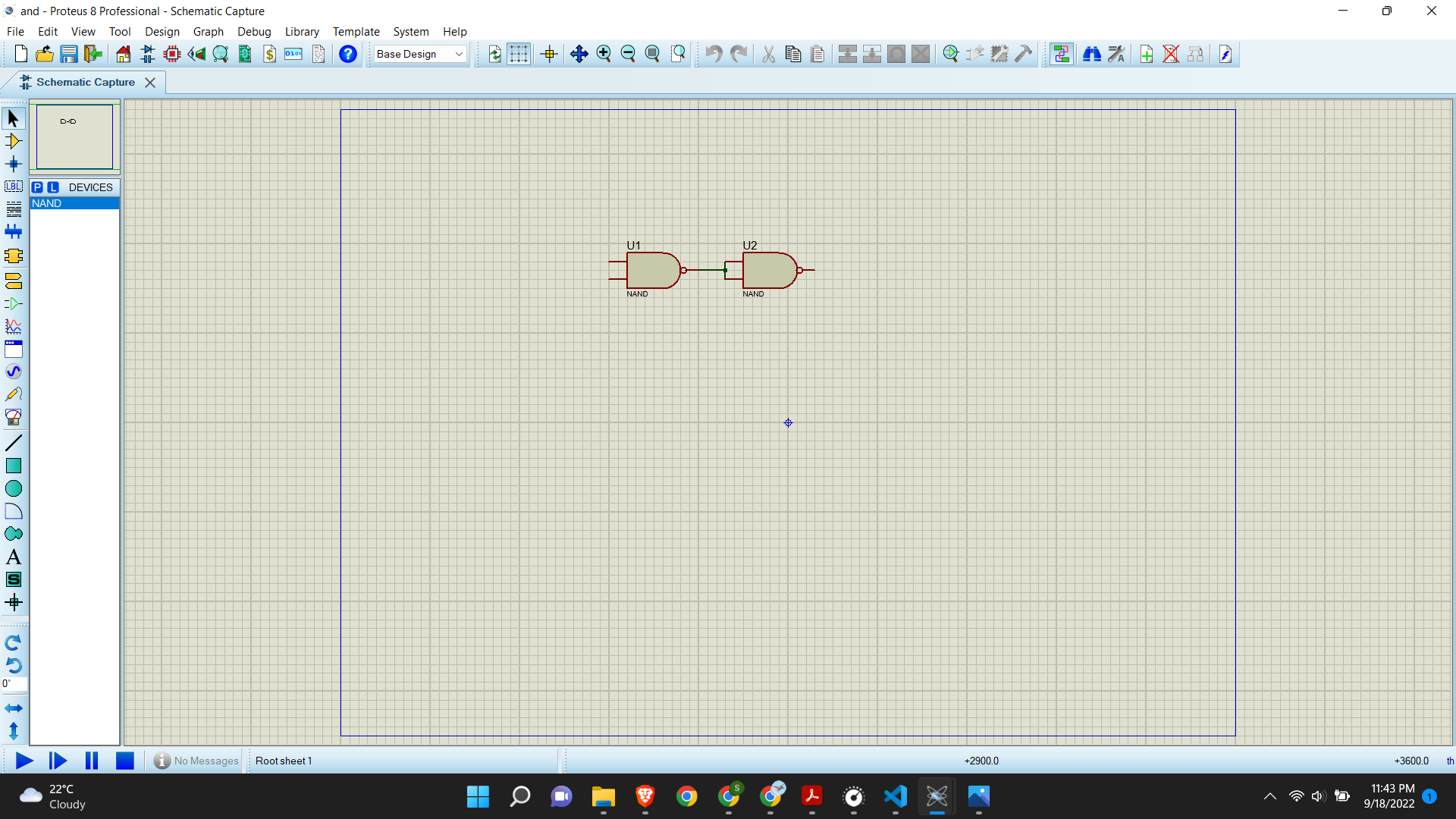
OR



NOT



NOR



AND