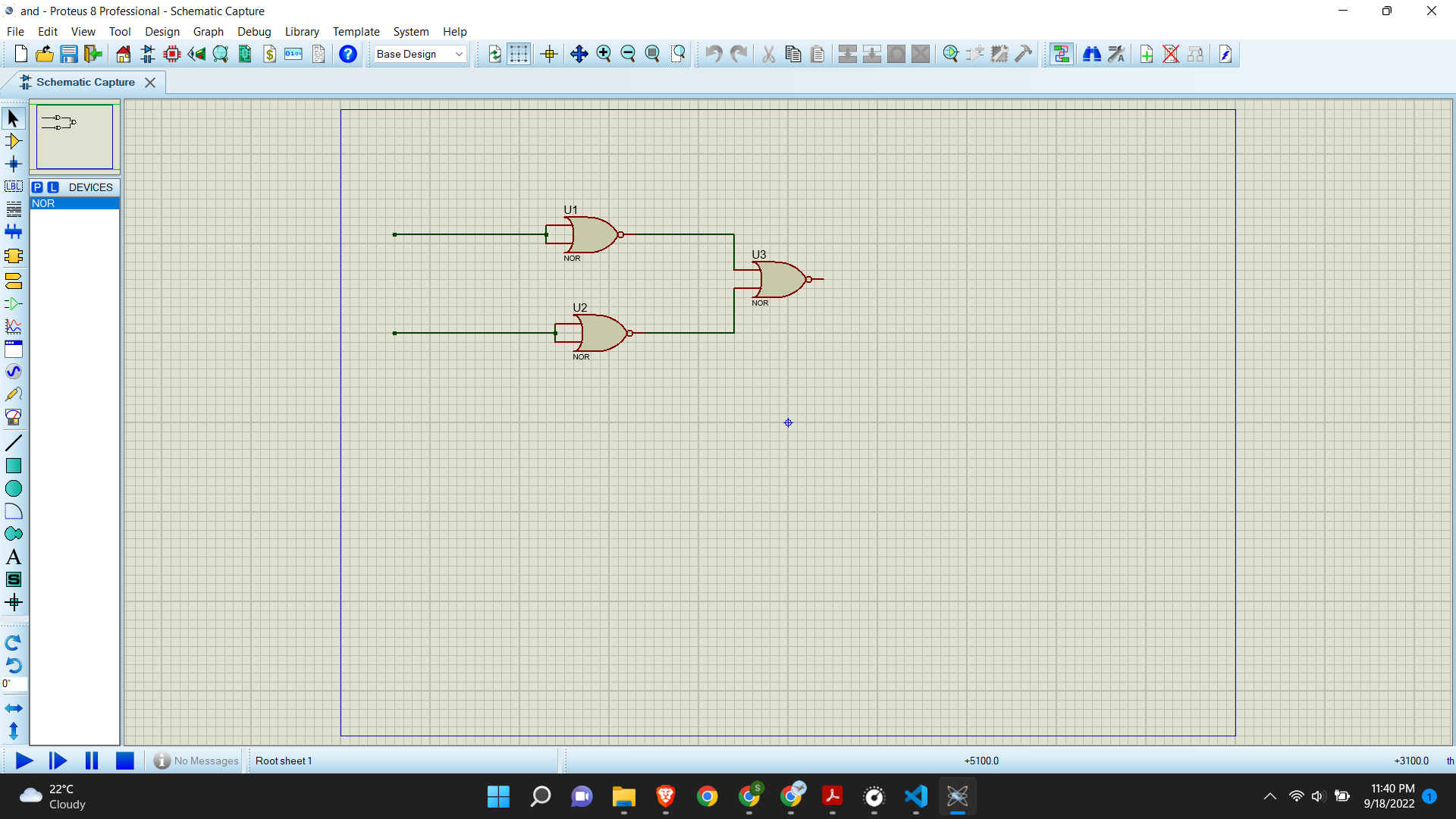
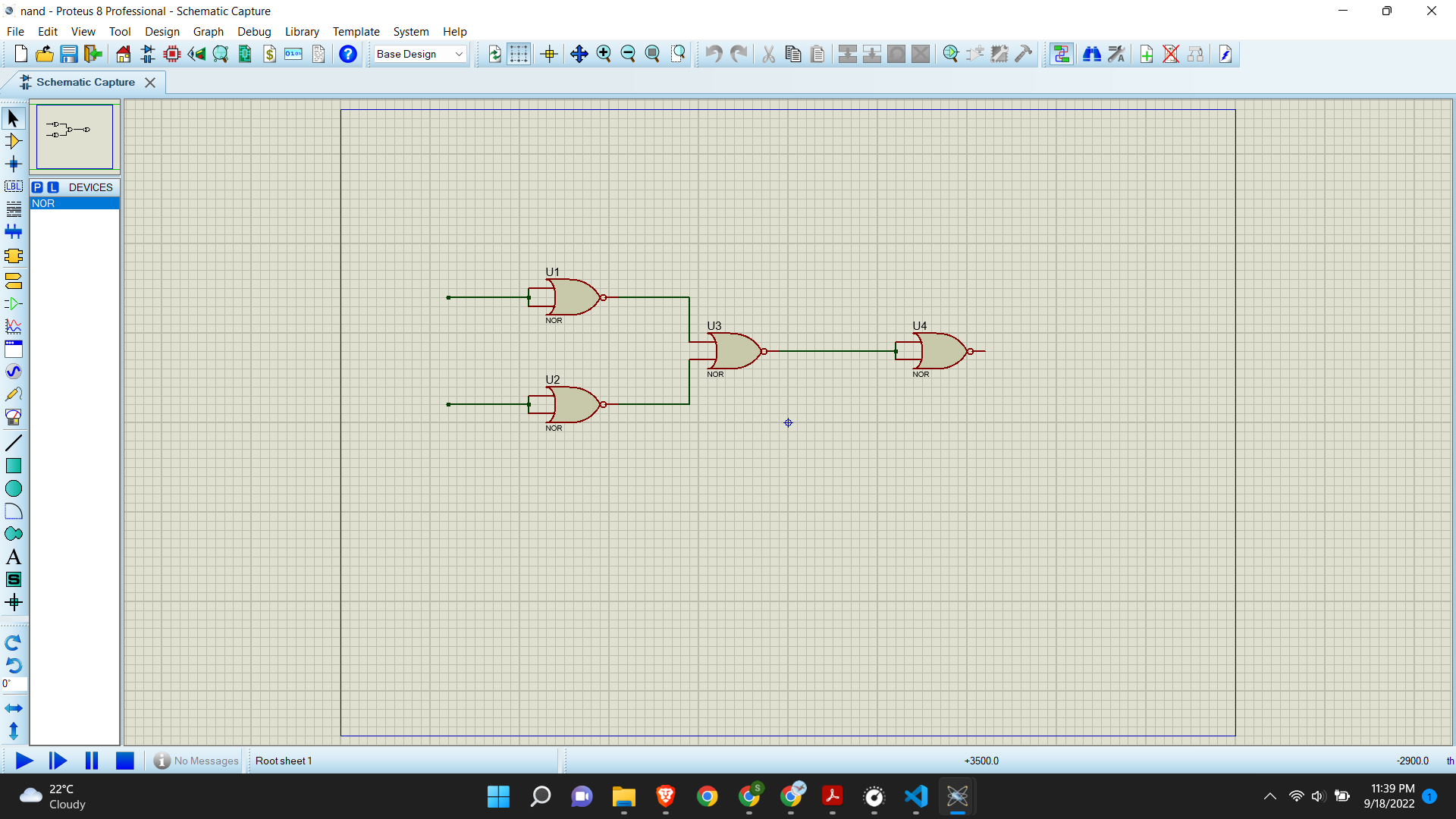
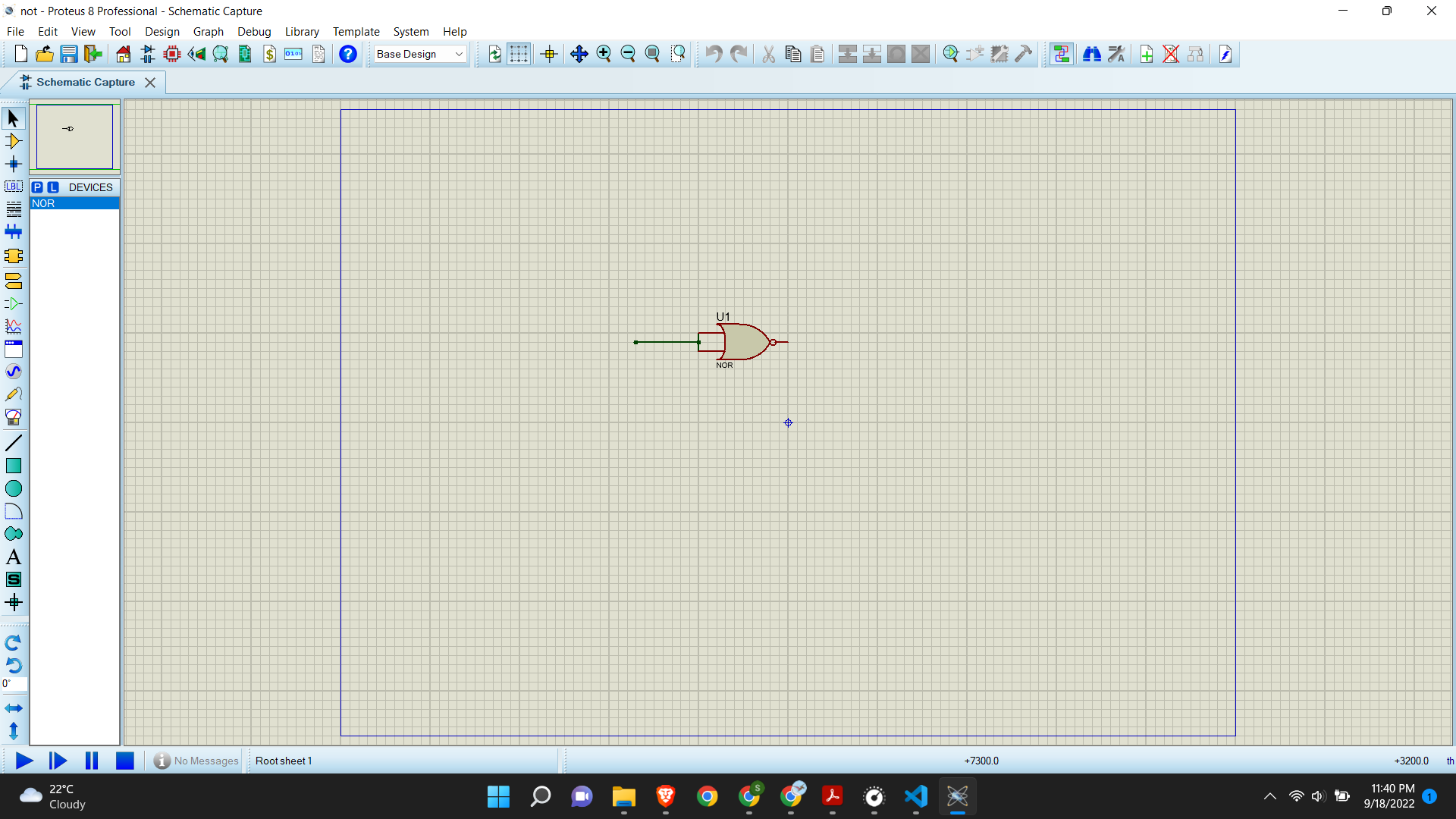
**GATES USING NOR GATE**



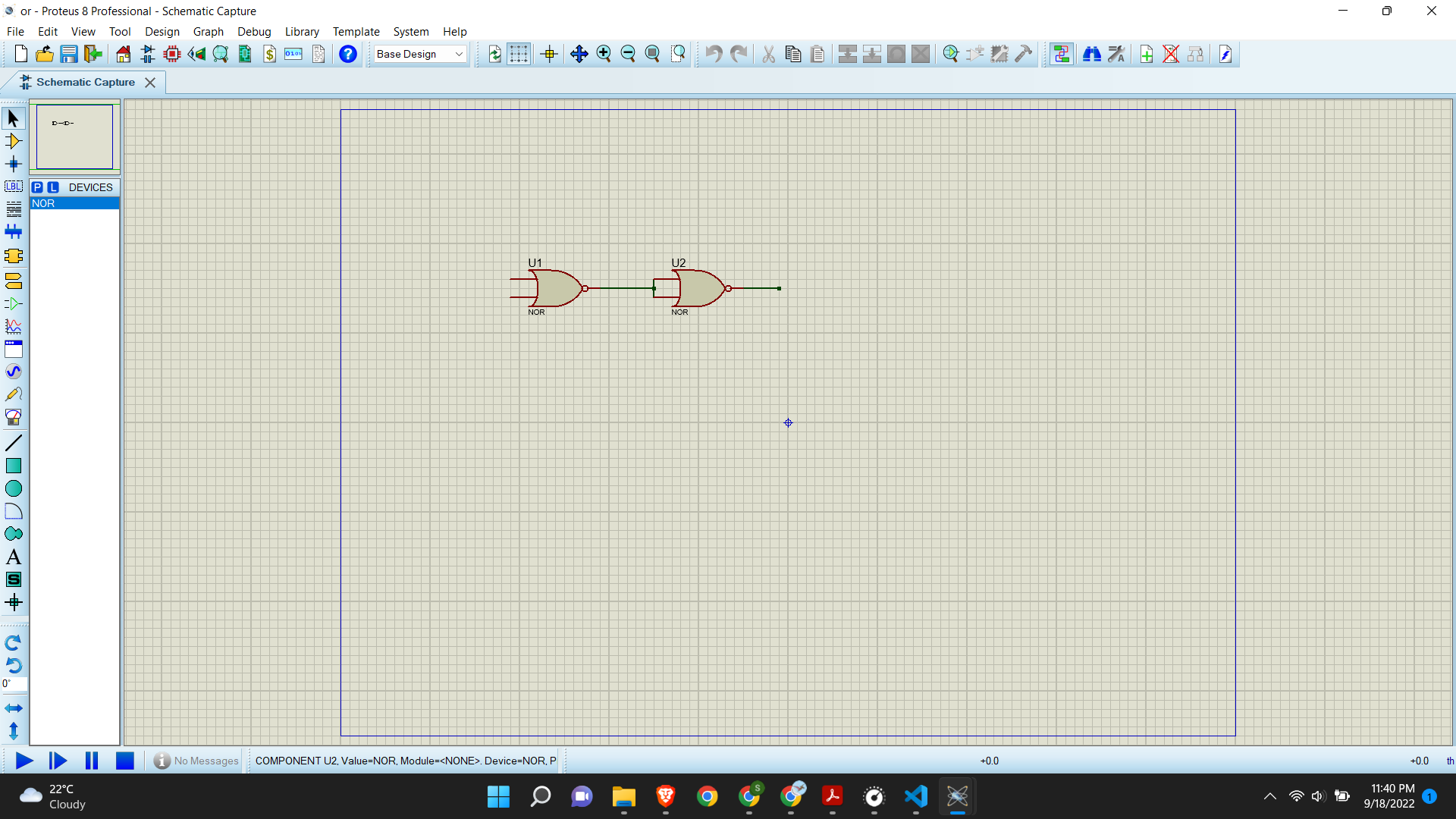
AND



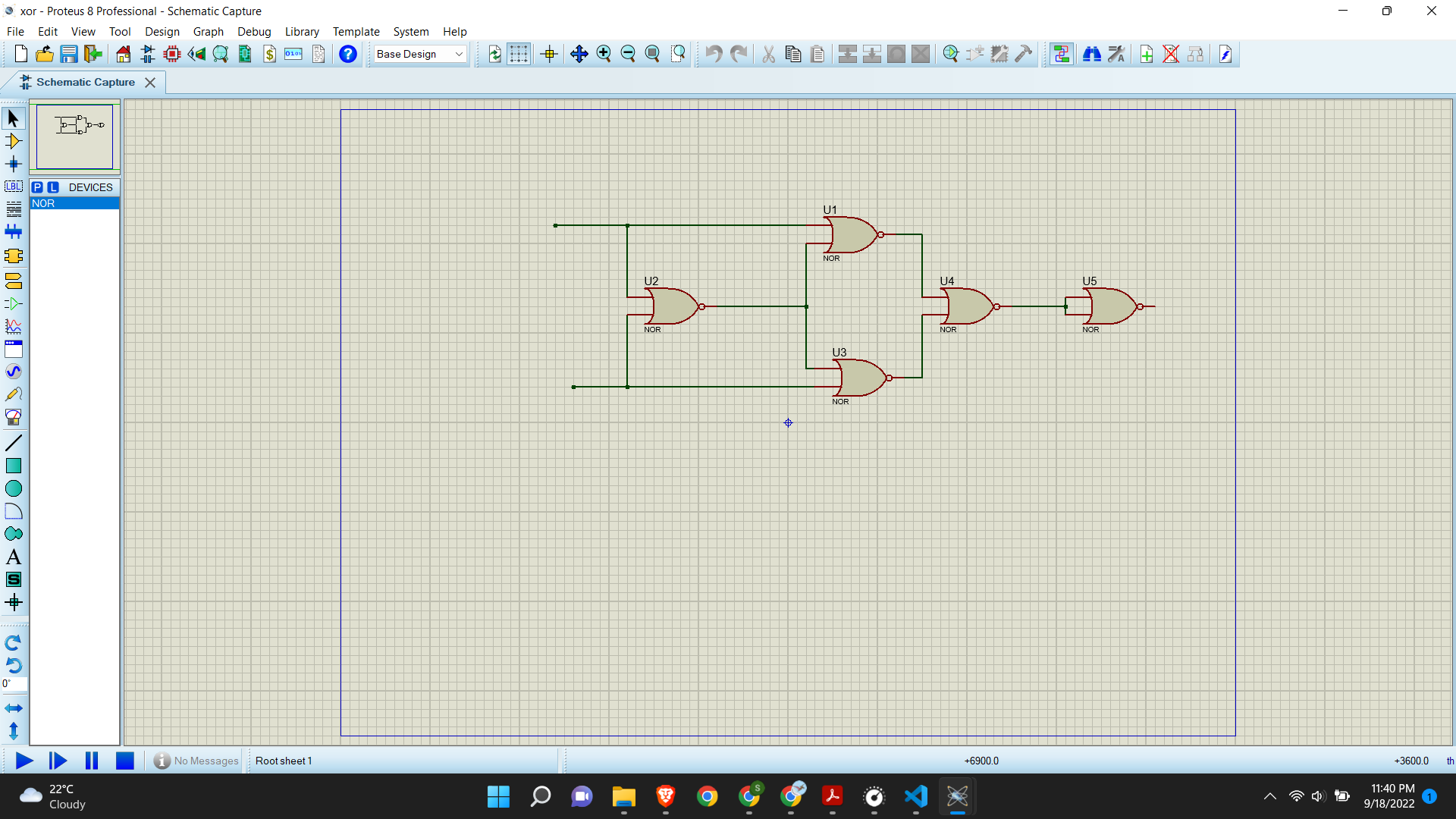
NAND



NOT



OR



XOR