Part 2

T1	T2	T3
R(A)		
W(A)		
		R(A)
		W(A)
	R(A)	
R(B)		
		R(B)
W(B)		
		W(B)
	R(B)	
	Commit	
Commit		
		Commit

Drew arrows for all read-write, write-write, and write-read dependencies

Precedence graph:
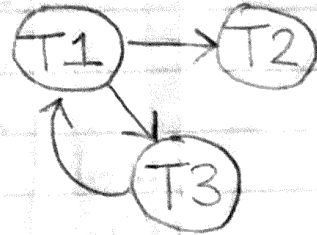
There is a cycle between T1 and T3 in the precedence graph, thus, this schedule is not conflict-serializable