# Joseph Telaak

https://linkedin.com/in/jtelaak/

**EDUCATION** 

University of South Carolina (USC)

Columbia, SC

Email: jtelaak@sc.edu

Mobile: 704-351-7396

BSE in Computer Engineering, Minor in Mathematics (GPA: 3.58);

Aug. 2022 - Dec. 2024 (Exp)

South Carolina Governor's School for Science and Mathematics (SCGSSM)

Hartsville, SC

High School Diploma, Concentration in CS and Math;

Aug. 2020 - May 2022

**EXPERIENCE** 

**USC SyReX Lab** 

Columbia, SC

Feb. 2023 - Present

Undergraduate Research Assistant

- o Built an ML model for contactless prediction of ECG and vitals using mmWave radar.
- o Trained an ML model to generate CV-like 3D bounding boxes without a camera using mmWave radar. MobiSys
- o Designed a system to combine multiple mmWave radars in an larger array structure to increase resolution.

## **SCGSSM Autonomous Golf Cart Research**

Hartsville, SC

Founder, Part-time consultant

Jan. 2022 - Feb. 2023, Jan. 2024 - Present

- o Managed funding (Over \$50k), part procurement, and technical design.
- o Retrofitted a drive-by-wire and ADAS system while retaining normal manual operation.
- Designed custom Nvidia Jetson carrier board with analog to CSI video capture.
- o Used computer vision to avoid obstacles, recognize street signs, and follow road lines.
- o Consult on the ongoing project and planning a fleet managment system for students to implement.

# **USC Cyberinfrastructure Lab**

Columbia, SC

Summer 2021

- Automated throughput and packet loss measurements.
  - o Developed a on-switch network load balancer in P4.

#### VOLUNTEERING

Research Assistant

#### **SCGSSM Board of Directors**

Hartsville, SC

Alumni Association Board Member

Jul. 2023 - Present

FIRST Robotics

Columbia, SC

FIRST Technical Advisor

Jan. 2022 - Present

## **SELECTED OTHER PROJECTS**

- Rocket Flight Computer: Arduino flight computer with GPS, IMU, barometer, and LoRA telemetry on custom PCB.
- RISC-V CPU: Designed a custom multicore RISC-V CPU with IO and matrix coprocessor for an Altera FPGA.
- Pick-and-place machine: Built a machine for automated PCB assembly using Marlin and OpenPNP.

## **OTHER**

- Languages: C/C++, Python, Java, MATLAB, Rust, LUA, P4, Verilog, SQL, MIPS, x86 AssemblyL
- Technologies: mmWave Studio, ROS, RTOS, Quartus, Kubernetes, STM32 Cube, Altium, RF Design, Signal Processing, PLC, AutoCAD, Electronics Testing and Measurement, 3D Printing
- Memberships: IEEE Eta Kappa Nu, IEEE MTTS, ACM, AIAA