ARM Assembly Programming:

Part 1:

We started the assembly programming by opening a terminal, then opened a third.s file to write our program. We used the directive, .data, to indicate that we were going to declare some variables. We created a 2-byte signed memory size at location **a** initialized with -2. We then loaded the registers with some signed hexadecimal integers.

```
@Third program
.section .data
a:.shalfword -2

.section .text
.globl _start
_start:

mov r0,#0x1
mov r1,#0xFFFFFFF
mov r2,#0xFF
mov r2,#0xFF
mov r3,#0x101
mov r4,#0x400

mov r7,#1
svc #0
.end
```

After we created the program, we tried to assemble it to get an object file and link, unfortunately, we couldn't assemble it because there was an error in the program with the following message:

```
pi@raspberrypi:~ $ nano third.s
pi@raspberrypi:~ $ as -g -o third.o third.s
third.s: Assembler messages:
third.s:3: Error: unknown pseudo-op: `.shalfword'
pi@raspberrypi:~ $ [
```

This error indicated that the 'shalfword' wasn't a keyword and therefore can't be used as a memory size. We fixed this error using the following program:

After the fix, we assembled and linked the program again, fortunately, it was able to assemble and link. We launched the gdb debugger for third.

We ran the **list** command to make sure our program was correct.

```
pi@raspberrypi:~ $ as -g -o third.o third.s
pi@raspberrypi:~ $ ld -o third third.o
pi@raspberrypi:~ $ gdb third
```

```
(gdb) list
1  @Third program
2    .section .data
3    a:.hword -2
4
5    .section .text
6    .globl _start
7    _start:
8
9    mov r0,#0x1
10    mov r1,#0xFFFFFFF
(gdb) □
```

Since we didn't load the variable **a** into any register, it didn't matter where we set the breakpoint as long as it is outside the **.data section**. We set a breakpoint at line 14 using the syntex **b** 14 to avoid stepping an instruction each time since we will be showing the content of the registers too. After that, we ran the program using **run**. After the program was successfully executed, we checked the memory location to make sure that the integer initialized (-2) was present in the 2-byte sized memory. We used both **x/1xh** and **x/1xsh** accompaning with the address of **a** (&**a**) to display the content of the memory in hexadecimal.

We observed that viewing the content of the memory using the halfword (x/1xh) is different from the signed halfword (x/1xsh), however, they both seem to provide us with the 2-byte memory with the integer -2 in hexadecimal.

We also examine the content of the registers to make sure that everything that we loaded stayed in the registers.

```
(gdb) info registers
                  0x1
-0
1
2
3
4
5
6
                  0xffffffff
                                          4294967295
                                          255
                  0x101
                                          257
                  0x400
                                          1024
                                          0
                  0x0
                                          0
                  0x0
                                          0
                  0x0
-8
                                          0
                  0x0
-9
                                          0
                  0x0
10
                                          0
                  0x0
11
                                          0
                  0x0
12
                  0x0
                                          0x7efff3c0
                  0x7efff3c0
sp
                  0 \times 0
                                          0x10088 <_start+20>
                  0x10088
                  0x10
                                          16
cpsr
                                          0
                  0x0
```

Part 2:

We created a second program called arithmetic3.s using Part 1 as a guide. We first planned what our program should do using the equation given as **Register** = val2 + 3 + val3 - val1. Given that Val1 is initialized with with -60, we automatically assume that we are dealing with a signed integer, so on a piece of paper, we illustrated what the program should look like.

```
Register = Val2 + 3 + Val3 - Val.1
· section · data
  Vall: stayte -60
Vall: byte 11
Vall: byte 16
  · Section . text
  · glob! - start
    ldrb rd,=Yald
     [94p LT/[14]
     12rb 12, = Val2
     Idrob ra, [ra]
     (drsbr3, = Vell3
      ldrsbr3, [r3]
      add 12,12,#3
       add ra, ra, r3
       Sub 12, 12, 11
      mox 57, #1
      SUC# D
       - end
```

However, we got an error while trying to load the registers as a signed integer using **ldrsb** as suggested in the handout. As a result of trying to fix the program, we ended up with the following program:

```
Register = Val 2 + 3 + Val 3 - Val -
Vall: byte -60
Vall: byte II
Vall: byte II
 · Section . text
 · globl _start
  _stout:
  ldr rd,=Yald
   ldr MILMI
   1dr 12 = Val2
   ldr ra, [ra]
    ldr r3, = Voll3
    ldr r3, [r3]
    add 12,12,#3
    add r2, r2, r3
    5 ub 12, 12, 11
    mov (7, #1
    SUC#0
    · end
```

To determine the output of the program, we first opened an arithmetic 3.s file to write our program. We used the directive, .data, to indicate that we were going to declare some variables. We created 3 byte variables: Val1, Val2, Val3. Val1 was initialized with -60, Val2 was initialized with 11, and Val3 was initialized with 16.

To use variable **val1**, we had to load (**ldr**) the memory address of **val1** into register r1. Then, we had to load the value of **val1** ([**r1**]) into register r1. After we loaded all the variables, we added 3 to the value of **r2**. We added the value of **r3** to the value of **r2**. Finally, we subtracted the value of **r1** from the value of **r2**.

```
section .data
al1:.byte
    .byte
    .byte
          16
section .text
globl _start
ldr
    r1,=Val1
    r1,[r1]
ldr
    r2,=Val2
    r2, [r2]
ldr
   r3,=Val3
ldr
    r3,[r3]
ldr
add
    r2, r2, #3
add
    r2, r2, r3
sub r2, r2, r1
mov
    r7,#1
    #O
SVC
end
```

After the code, we assembled, linked and debugged using (gdb arithmetic3). We listed the program to make sure that everything was correct, then we set a breakpoint at 20 (b 20) and ran the program. We examined the content of the memory using x\1xb &Val1,Val2,Val3.

We then checked the value in register 2 (hexadecimal) in **info registers** to make sure that the result from the program was correct.

```
r0
                0x0
                                      0x0
r1
                0x41100bc4
                                      0x41100bc4
r2
                0xd042455a
                                     0xd042455a
r3
                0x114110
                                      0x114110
r4
                0x0
                                      0x0
r5
                0x0
                                      0x0
r6
                0x0
                                      0x0
r7
                0x1
                                      0x1
r8
                0x0
                                      0x0
r9
                0x0
                                      0x0
r10
                0x0
                                      0x0
r11
                0x0
                                      0x0
r12
                0x0
                                      0x0
                0x7efff3b0
                                     0x7efff3b0
sp
lr
                0x0
                                      0x0
pc
                0x1009c
                                     0x1009c < start+40>
cpsr
                0x10
                                      0x10
fpscr
                0x0
                                      0x0
```

We then checked for the flags

```
[ Legend: Modified register | Code | Heap | Stack | String ]
    : 0x0
    : 0x41100bc4
    : 0xd042455a
    : 0x114110
    : 0x0
    : 0x0
    : 0x0
    : 0x1
    : 0x0
   : 0x0
r10 : 0x0
r11 : 0x0
r12 : 0x0
    : 0x7efff3b0 \rightarrow 0x00000001
   : 0x0
$pc

→ < start+40> svc 0x000000000
cpsr: [negative zero carry overflow interrupt fast thumb]
0x7efff3b0|+0x0000: 0x00000001 - $sp
0x7efff3b4 +0x0004: 0x7efff535
0x7efff3b8 +0x0008: 0x00000000
0x7efff3cc +0x001c: 0x7efff5c1 → "XDG_MENU_PREFIX=lxde-pi-"
```