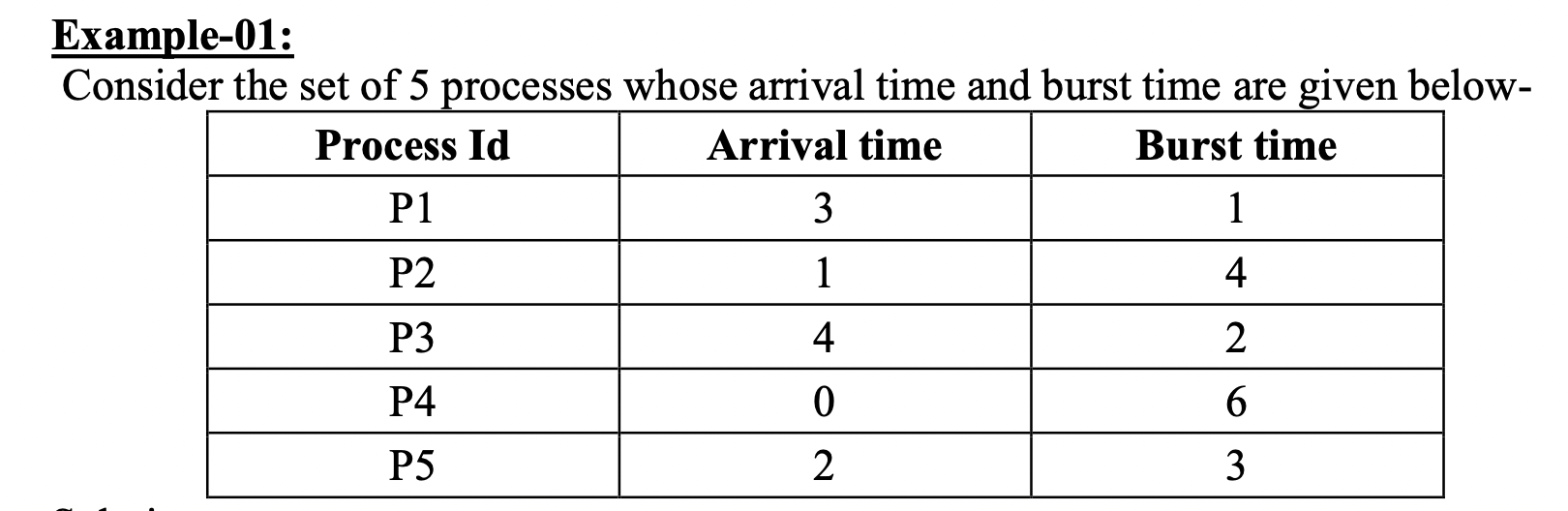
**OPERATING SYSTEM LAB 6**

|  |  |
| --- | --- |
| Roll No.: K041 | Name: Anish Sudhan Nair |
| Batch No.: A2/K2 | Date: 04/02/2022 |

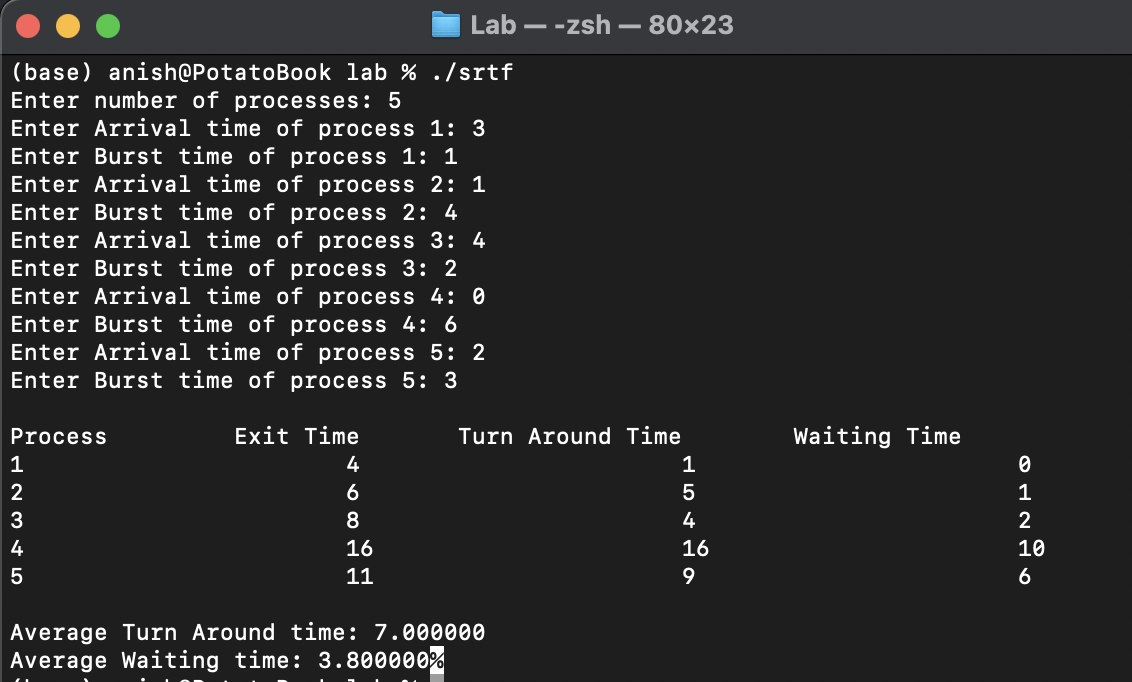
Aim: To familiarise and implement the shortest remaining time first algorithm

1. Example 1

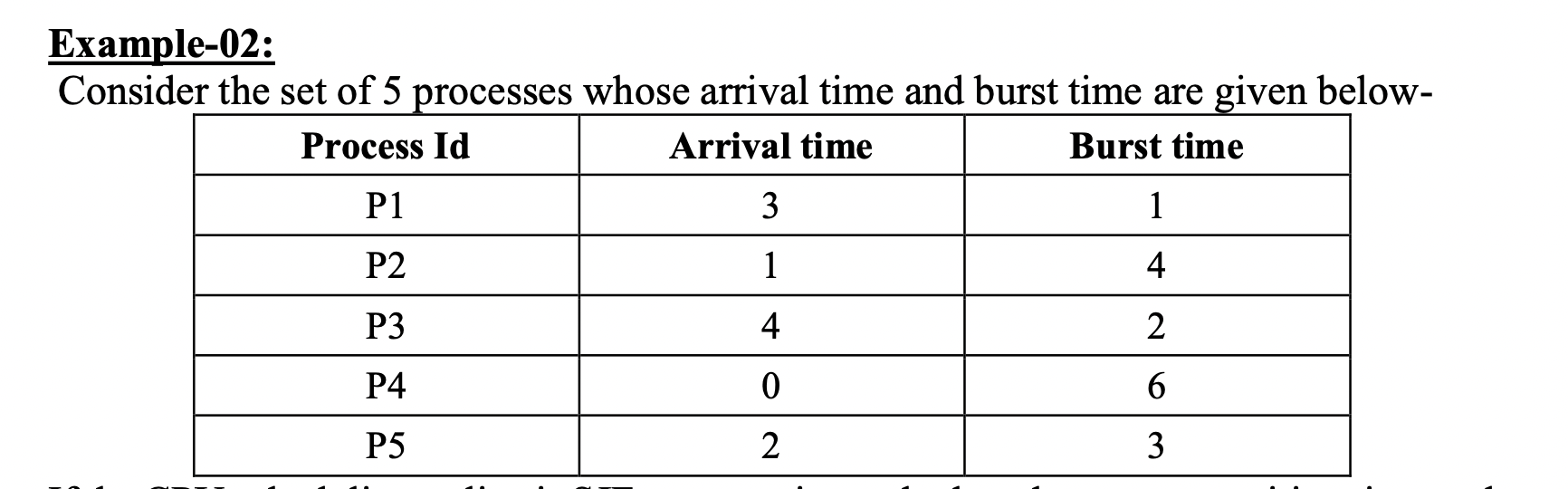


Output:

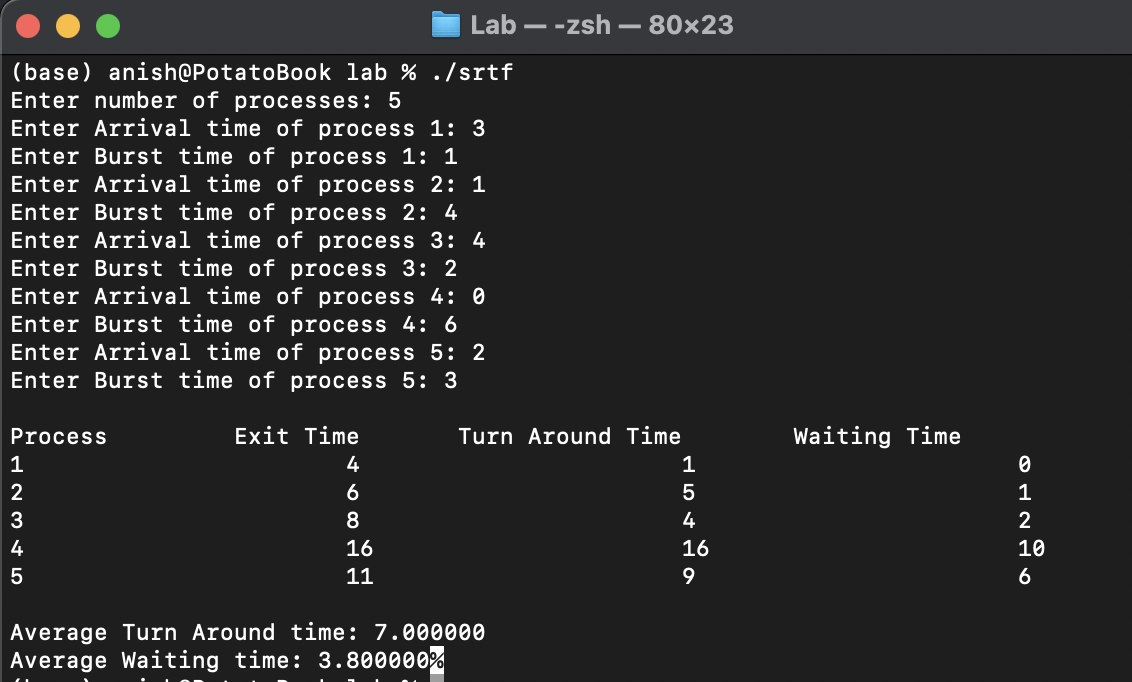
SRTF pre emptive done



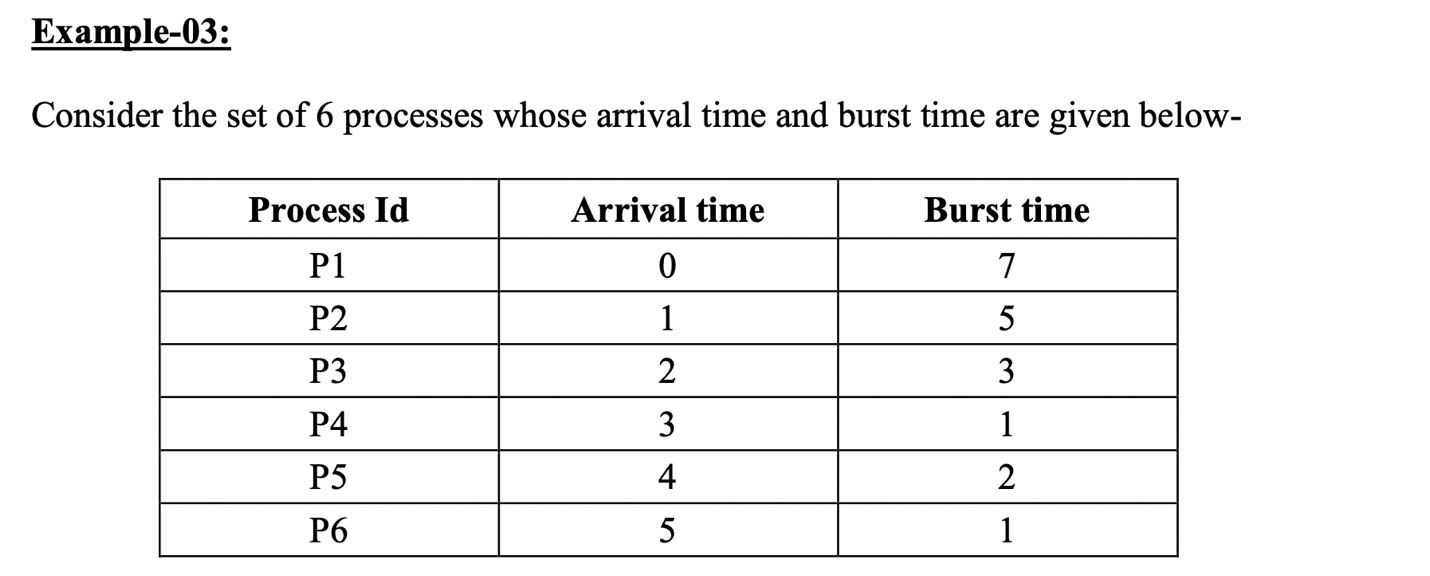
1. Example 2



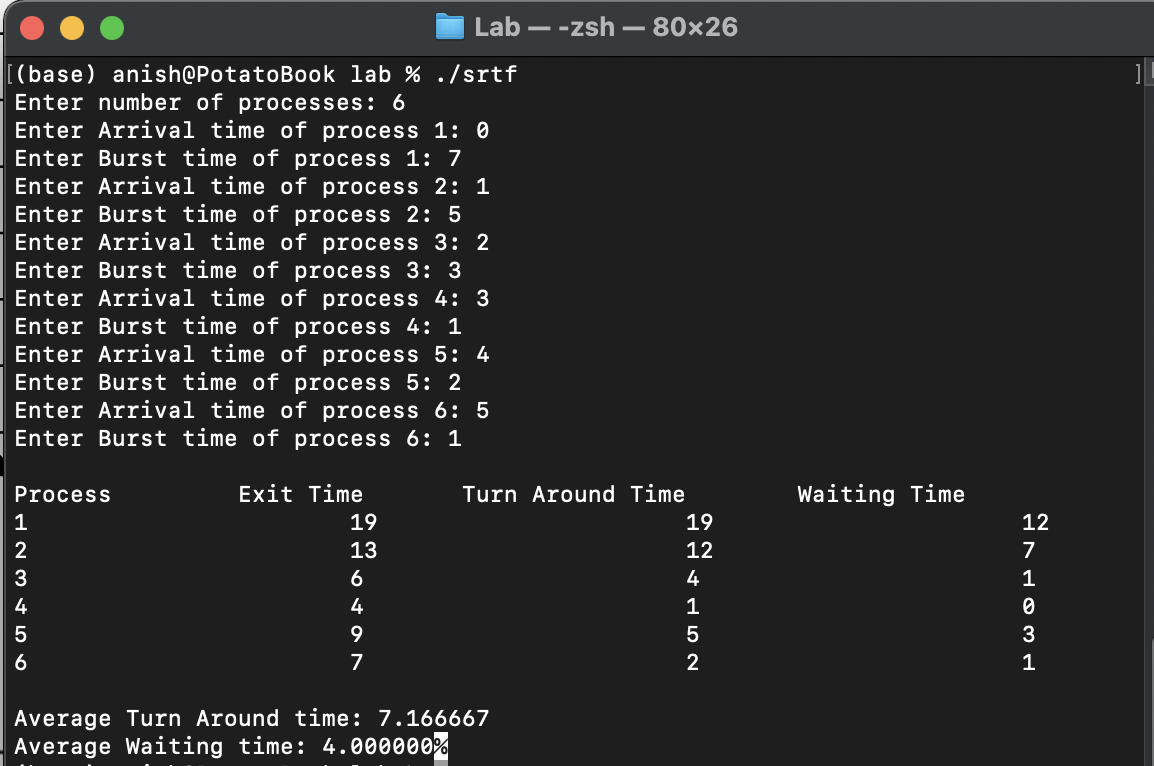
Output:



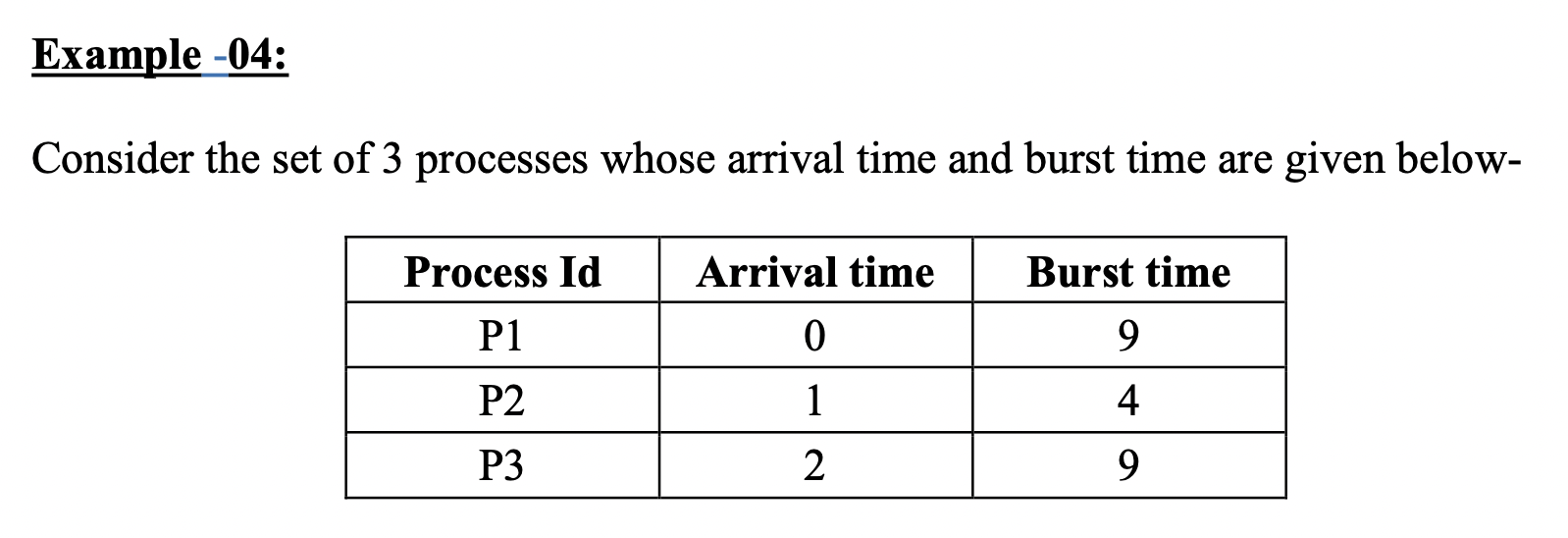
1. Example 3



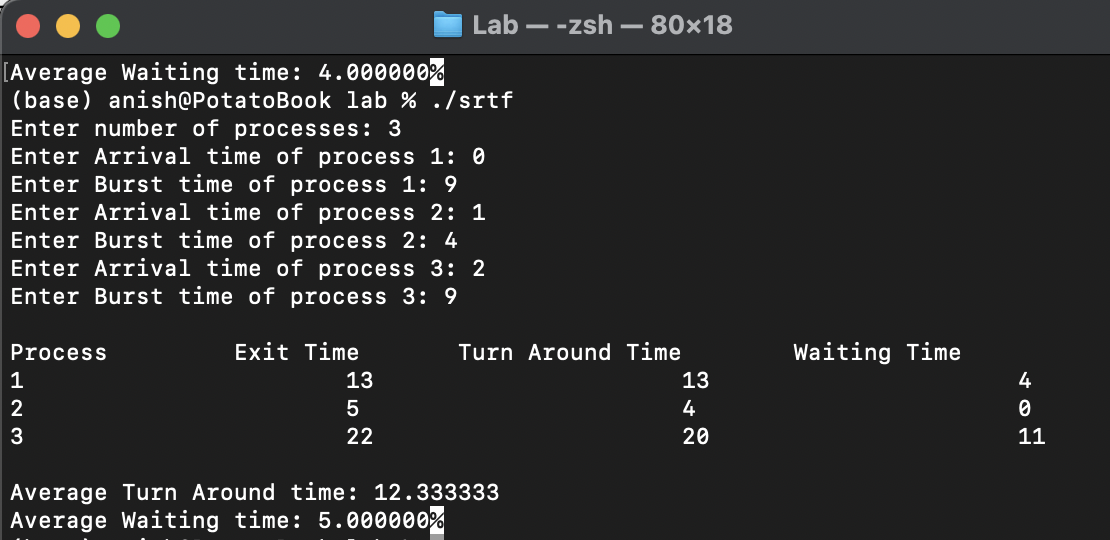
Output:



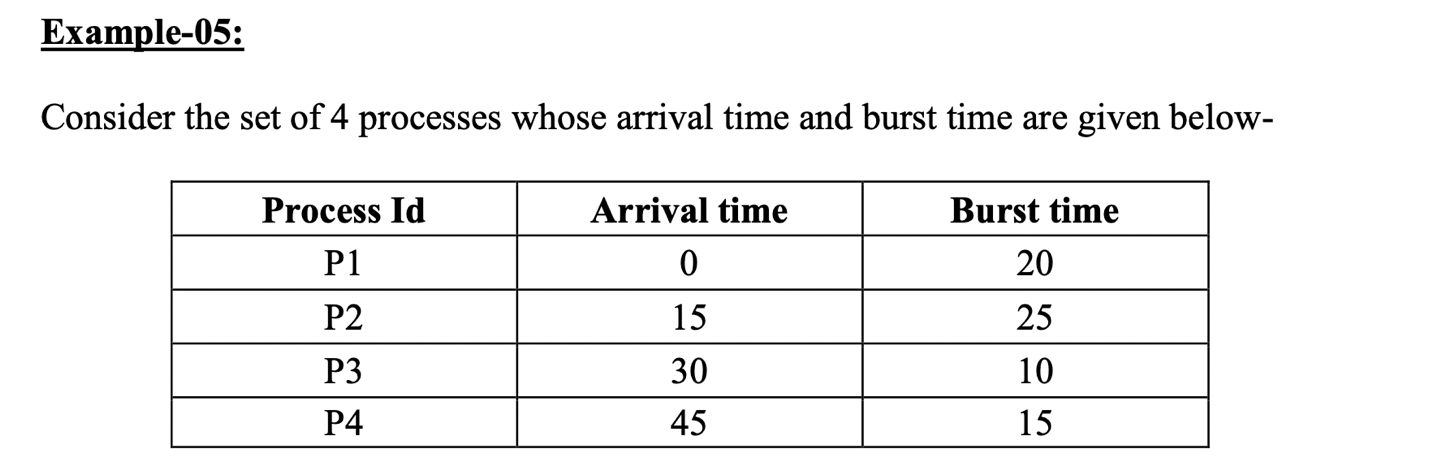
1. Example 4



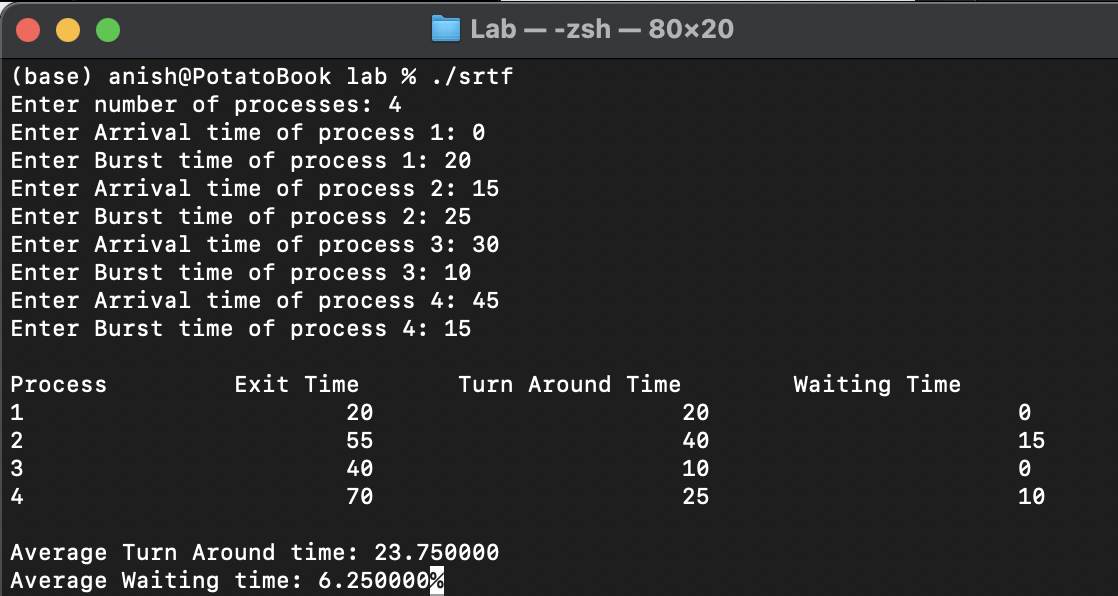
Output:



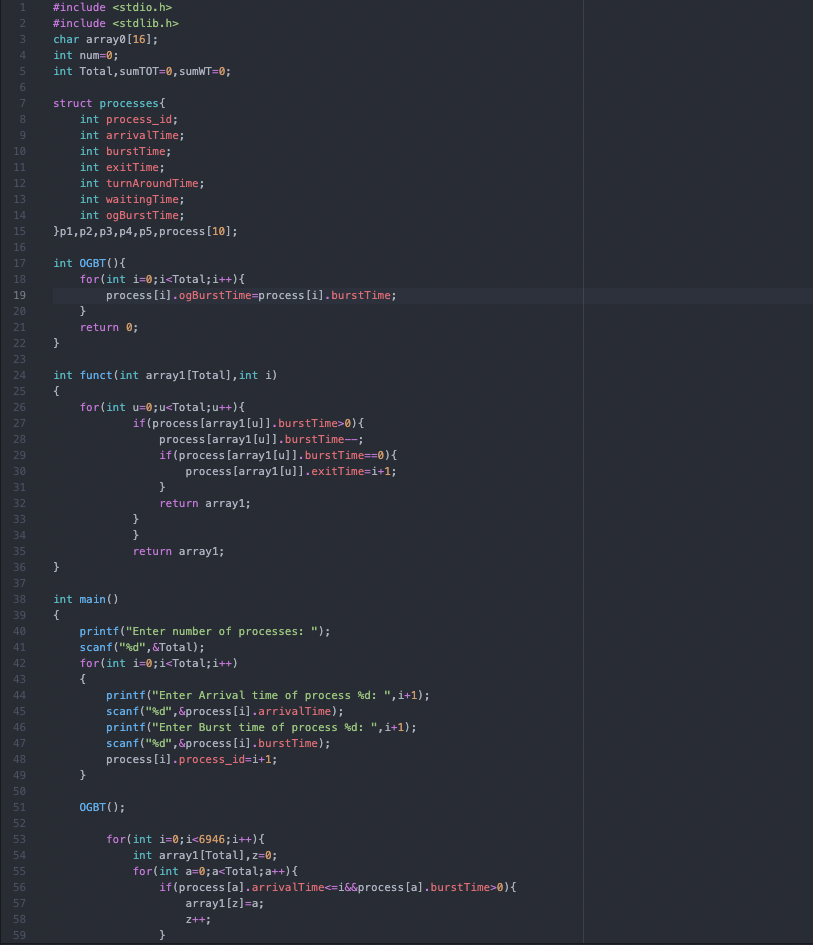
1. Example 5

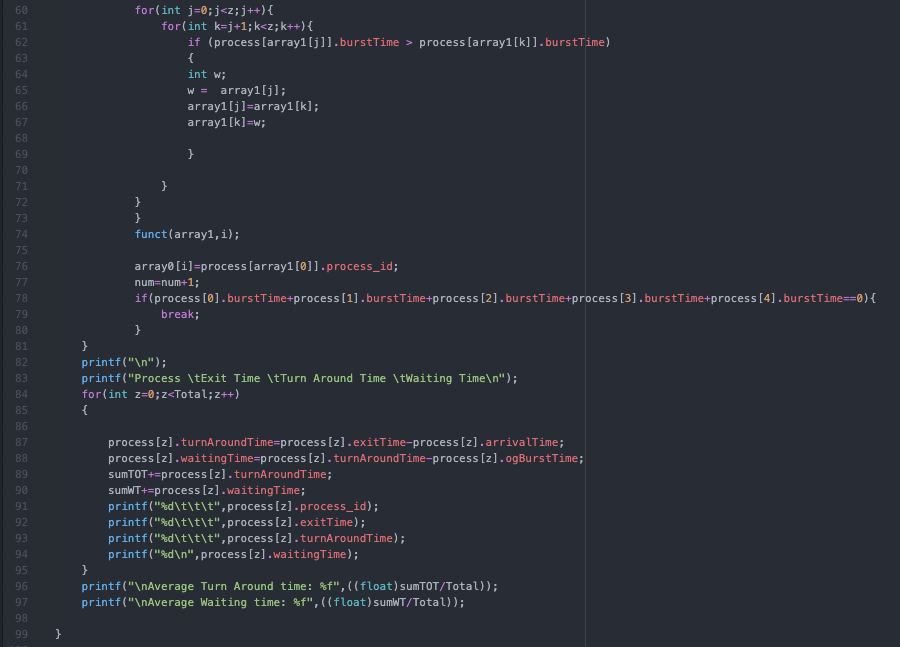


Output:



Code:





CONCLUSION:

In this lab, we were to implement and demonstrate the working of the shortest remaining time first algorithm. By actually coding the algorithm, it helped to reinforce the working of this process and the manner in which it schedules the processes in a CPU.