

...done independently but "Student A" helped me troubleshoot and change a few lines of code.

This is a voltage divider / R2R circuit...

SW1

$$V_{OUT} = 5 \left(\frac{1760}{1000 + 1760} \right) = 3.18 \text{ V}$$

$$V_{OUT} = V_S \left(\frac{R_2}{R_1 + R_2} \right)$$

SW2

$$V_{OUT} = 5 \left(\frac{1320}{1000 + 1320} \right) = 2.84 \text{ V}$$

SW3

$$V_{OUT} = 5 \left(\frac{880}{1000 + 880} \right) = 2.34 \text{ V}$$

SW4

$$V_{OUT} = 5 \left(\frac{440}{1000 + 440} \right) = 1.52 \text{ V}$$

Outside of the AtoDtest.c program from class that I based this software on, all work is my own.