



Pin #	Full Pin Name	Pin #	Full Pin Name
1	PGED1/AN2/C1IND/C2NB/C3IND/RP80/PM0/RB0	15	TDORPB9/SDA1/CTED4/PM03/RB9
2	PGEC1/AN3/C1INC/C2NA/RPB1/CTED12/PM01/RB1	16	Vss
3	AN4/C1NB/C2IND/RPB2/SDA2/CTED13/PM02/RB2	17	Vcap
4	AN5/C1NA/C2INC/RTCC/RPB3/SL2/PMWR/RB3	18	PGED2/RPB10/D+CTED11/RB10
5	Vss	19	PGEC2/RPB11/D-RB11
6	OSC1/CLK/RPA2/RA2	20	Vusbv3
7	OSC2/CLK/RPA3/PMAR/RA3	21	AN11/RPB13/CTPLS/PMRD/RB13
8	SOSC1/RPB4/RB4	22	CVREFOUT/AN10/C3NB/RPB14/Vusb0N/SK1/CTED5/RB14
9	SOSC0/RPA4/T1CK/CTED9/PA1/RA4	23	AN9/C3NA/RPB15/SCK2/CTED6/PMCS1/RB15
10	Vdd	24	AVss
11	TMS/RPB5/USBID/RB5	25	AVdd
12	Vbus	26	MCLR
13	TDI/RPB7/CTED3/PMDS/INT0/RB7	27	PGED3/VREF+CVREF+AN0/C3INC/RPA0/CTED1/PM07/RA0
14	TCR/RPB8/SLC1/CTED10/PM04/RB8	28	PGEC3/VREF-CVREF-AN1/RPA1/CTED2/PM06/RA1

Pin #	Full Pin Name	Pin #	Full Pin Name
1	AN4/C1NB/C2IND/RPB2/SDA2/CTED13/PM02/RB2	19	TDORPB9/SDA1/CTED4/PM03/RB9
2	AN5/C1NA/C2INC/RTCC/RPB3/SL2/PMWR/RB3	20	RPC9/CTED7/RB9
3	PGED4/AN6/RPC0/RC0	21	Vss
4	PGEC4/AN7/RPC1/RC1	22	Vcap
5	Vdd	23	Vdd
6	Vss	24	PGED2/RPB10/D+CTED11/RB10
7	OSC1/CLK/RPA2/RA2	25	PGEC2/RPB11/D-RB11
8	OSC2/CLK/RPA3/PMAR/RA3	26	Vusbv3
9	SOSC1/RPB4/RB4	27	AN11/RPB13/CTPLS/PMRD/RB13
10	SOSC0/RPA4/T1CK/CTED9/PA1/RA4	28	CVREFOUT/AN10/C3NB/RPB14/Vusb0N/SK1/CTED5/RB14
11	AN12/RPC3/RC3	29	AN9/C3NA/RPB15/SCK2/CTED6/PMCS1/RB15
12	Vss	30	AVss
13	Vdd	31	AVdd
14	Vdd	32	MCLR
15	TMS/RPB5/USBID/RB5	33	PGED3/VREF+CVREF+AN0/C3INC/RPA0/CTED1/PM07/RA0
16	Vbus	34	PGEC3/VREF-CVREF-AN1/RPA1/CTED2/PM06/RA1
17	TDI/RPB7/CTED3/PMDS/INT0/RB7	35	PGED1/AN2/C1IND/C2NB/C3IND/RPB0/PM00/RB0
18	TCR/RPB8/SLC1/CTED10/PM04/RB8	36	PGEC1/AN3/C1INC/C2NA/RPB1/CTED12/PM01/RB1

Pin #	Full Pin Name	Pin #	Full Pin Name
15	Vbus	15	MCLR
16	TDORPB9/SDA1/CTED4/PM03/RB9	16	PGED3/VREF+CVREF+AN0/C3INC/RPA0/CTED1/PM07/RA0
17	TCORPB8/SLC1/CTED10/PM04/RB8	17	PGEC3/VREF-CVREF-AN1/RPA1/CTED2/PM06/RA1
18	TDORPB8/SLC1/CTED10/PM04/RB8	18	PGED1/AN2/C1IND/C2NB/C3IND/RPB0/PM00/RB0
19	Vss	19	PGEC1/AN3/C1INC/C2NA/RPB1/CTED12/PM01/RB1
20	Vcap	20	AN4/C1NB/C2IND/RPB2/SDA2/CTED13/PM02/RB2
21	PGED2/RPB10/D+CTED11/RB10	21	AN5/C1NA/C2INC/RTCC/RPB3/SL2/PMWR/RB3
22	PGEC2/RPB11/D-RB11	22	AN6/RPC0/RC0
23	Vusbv3	23	AN7/RPC1/RC1
24	AN11/RPB13/CTPLS/PMRD/RB13	24	AN8/RPC2/PA2/RC2
25	CVREFOUT/AN10/C3NB/RPB14/Vusb0N/SK1/CTED5/RB14	25	Vdd
26	AN9/C3NA/RPB15/SCK2/CTED6/PMCS1/RB15	26	Vss
27	AVss	27	OSC1/CLK/RPA2/RA2
28	AVdd	28	OSC2/CLK/RPA3/PMAR/RA3
		29	SOSC1/RPB4/RB4
		30	SOSC0/RPA4/T1CK/CTED9/PA1/RA4
		31	Vdd
		32	TMS/RPB5/USBID/RB5

TABLE 4-1: SFR MEMORY MAP

Peripheral	Virtual Address	
	Base	Offset Start
Watchdog Timer	0xBF80	0x0000
RTCC		0x0200
Timer1-5		0x0600
Input Capture 1-5		0x2000
Output Compare 1-5		0x3000
IC1 and IC2		0x5000
SPI1 and SPI2		0x5800
UART1 and UART2		0x6000
PMP		0x7000
ADC		0x9000
CVREF		0x9000
Comparator		0xA000
CTMU		0xA200
Oscillator		0xF000
Device and Revision ID		0xF220
Peripheral Module Disable		0xF240
Flash Controller	0xBF88	0xF400
Reset		0xF600
PPS		0xFA04
Interrupts		0x1000
Bus Matrix		0x2000
DMA		0x3000
USB		0x5050
PORTA-PORTC		0x6000
Configuration	0xBF00	0xBF00

TABLE 26-2: PERIPHERAL MODULE DISABLE REGISTER MAP

Virtual Address (BF00_#)	Register Name	Bit Range	Bits																All Resets
			31/15	30/14	29/13	28/12	27/11	26/10	25/9	24/8	23/7	22/6	21/5	20/4	19/3	18/2	17/1	16/0	
F240	PMD1	31:16 15:0	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	0000	
F250	PMD2	31:16 15:0	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	0000	
F260	PMD3	31:16 15:0	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	0000	
F270	PMD4	31:16 15:0	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	0000	
F280	PMD5	31:16 15:0	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	0000	
F290	PMD6	31:16 15:0	—	—	—	—	—	—	—	—	—	—	—	—	—	—	—	0000	

Legend: x = unknown value on Reset, — = unimplemented, read as '0'. Reset values are shown in hexadecimal.

Note: 1: All registers in this table have corresponding CLR, SET and INV registers at their virtual addresses, plus offsets of 0x4, 0x8 and 0xC, respectively. See Section 11.2 "CLR, SET and INV Registers" for more information.

Pin #	Full Pin Name	Pin #	Full Pin Name
1	RPB9/SDA1/CTED4/PM03/RB9	23	AN4/C1NB/C2IND/RPB2/SDA2/CTED13/PM02/RB2
2	RPC6/PA1/RC6	24	AN5/C1NA/C2INC/RTCC/RPB3/SL2/PMWR/RB3
3	RPC7/PA0/RC7	25	AN6/RPC0/RC0
4	RPC8/PA5/RC8	26	AN7/RPC1/RC1
5	RPC9/CTED7/PA6/RC9	27	AN8/RPC2/PA2/RC2
6	Vss	28	Vdd
7	Vcap	29	Vss
8	PGED2/RPB10/D+CTED11/RB10	30	OSC1/CLK/RPA2/RA2
9	PGEC2/RPB11/D-RB11	31	OSC2/CLK/RPA3/RA3
10	Vusbv3	32	TDORPB8/PA8/RB8
11	AN11/RPB13/CTPLS/PMRD/RB13	33	SOSC1/RPB4/RB4
12	PGED4/TMS/PA10/RA10	34	SOSC0/RPA4/T1CK/CTED9/RA4
13	PGEC4/TC/CTED8/PA7/RA7	35	TDORPB8/PA8/RB8
14	CVREFOUT/AN10/C3NB/RPB14/Vusb0N/SK1/CTED5/RB14	36	AN12/RPC3/RC3
15	AN9/C3NA/RPB15/SCK2/CTED6/PMCS1/RB15	37	RPC4/PA4/RC4
16	AVss	38	RPC5/PA3/RC5
17	AVdd	39	Vss
18	MCLR	40	Vdd
19	PGED3/VREF+CVREF+AN0/C3INC/RPA0/CTED1/PM07/RA0	41	RPB5/USBID/RB5
20	PGEC3/VREF-CVREF-AN1/RPA1/CTED2/PM06/RA1	42	Vbus
21	PGED1/AN2/C1IND/C2NB/C3IND/RPB0/PM00/RB0	43	RPB7/CTED3/PMDS/INT0/RB7
22	PGEC1/AN3/C1INC/C2NA/RPB1/CTED12/PM01/RB1	44	RPB8/SLC1/CTED10/PM04/RB8

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