



# **Experiment Number 4**

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Branch :: CSE - IoT Sec/Grp :: 1/A

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Subject :: Embedded System Lab CODE :: CSD-333

#### 1. Aim:

To examine the flag bits of status register.

#### 2. Task:

- 1. Write an assembly program to add the following data and use the simulator to examine the various flag like carry, digital carry and zero flag after the execution of each addition. 99H, 01H, 66H, 89h, 97H, 35H.
- 2. Write assembly program to load the value into file registers from 20H to 24H location. Use the compliment command in each location and then use the simulator to examine the status of flag and file register content after the execution of each instruction.

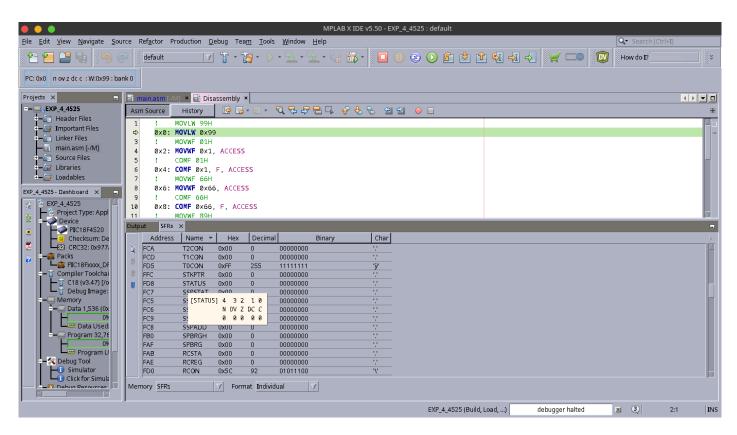






### 3A. Algorithm:

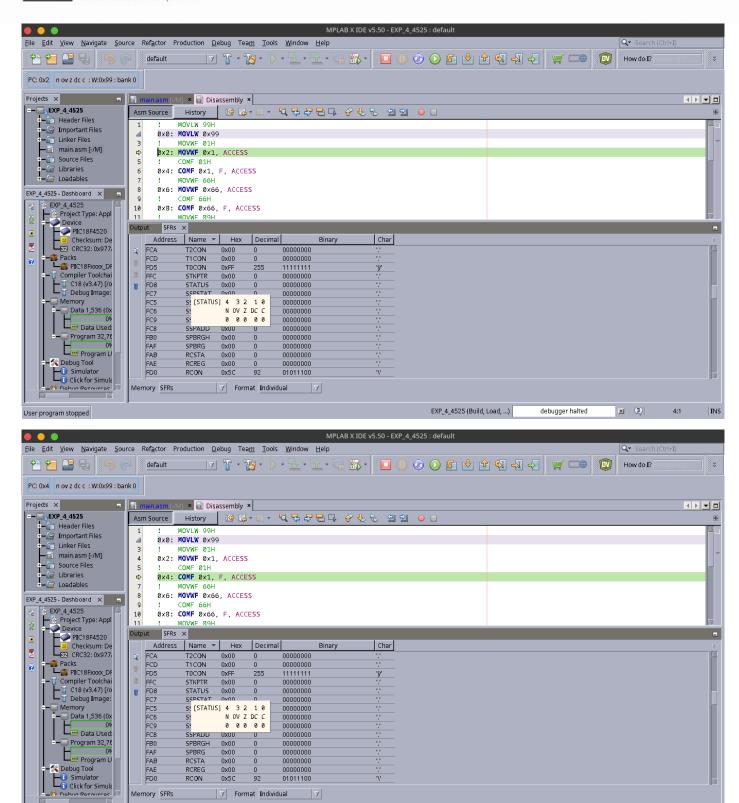
- Create a new file
- Write the header file
- Write the starting location
- Move the literal value to the working register
- Move the working register to file register
- Now, Complement the file register
- Repeat Step 5 and Step 6 for every literal value until you reach the end using different values to move from working register to file register and the complement that file register.











EXP\_4\_4525 (Build, Load, ...)

debugger halted



User program stopped

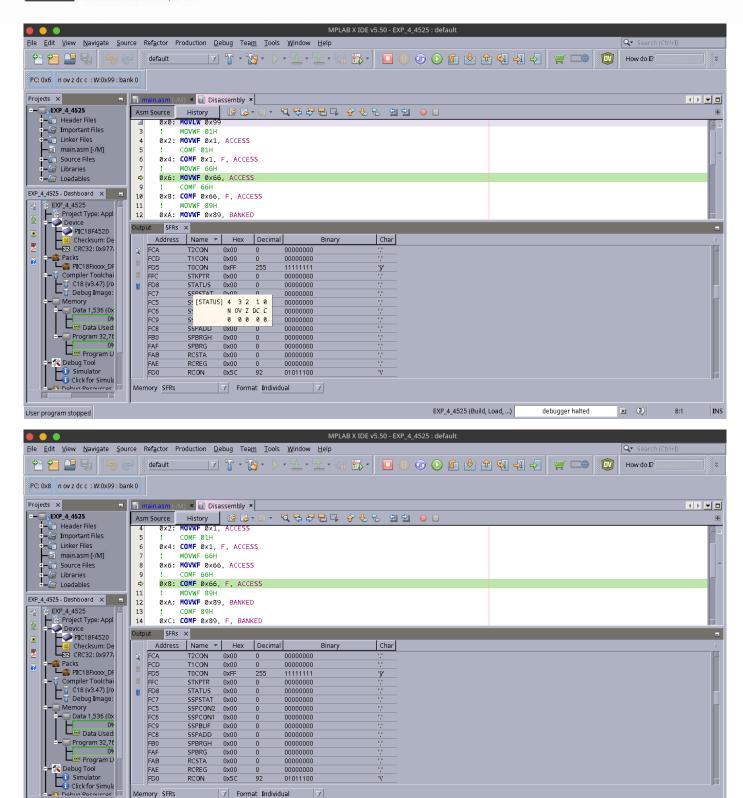
6:1

INS

**X** (3)







EXP\_4\_4525 (Build, Load, ...)

debugger halted



User program stopped

10:1

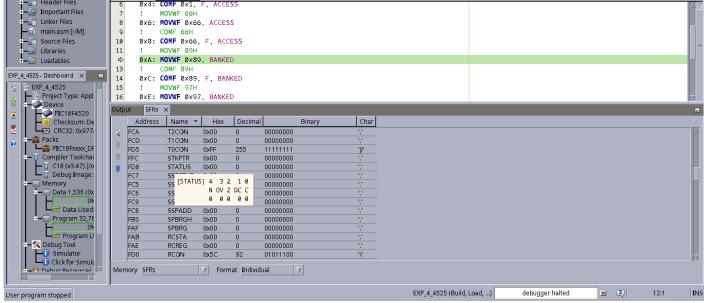
INS

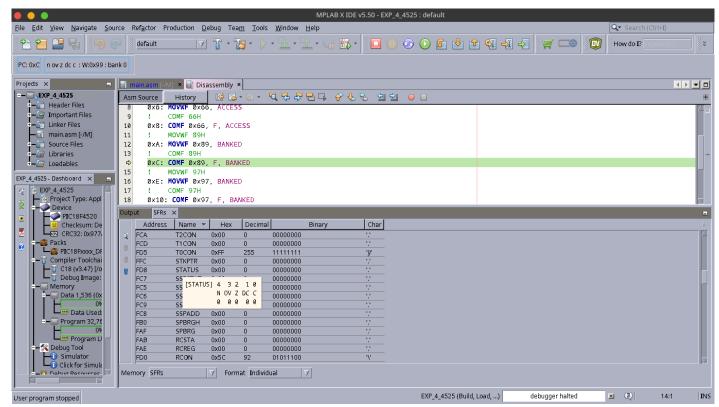
**X** (3)









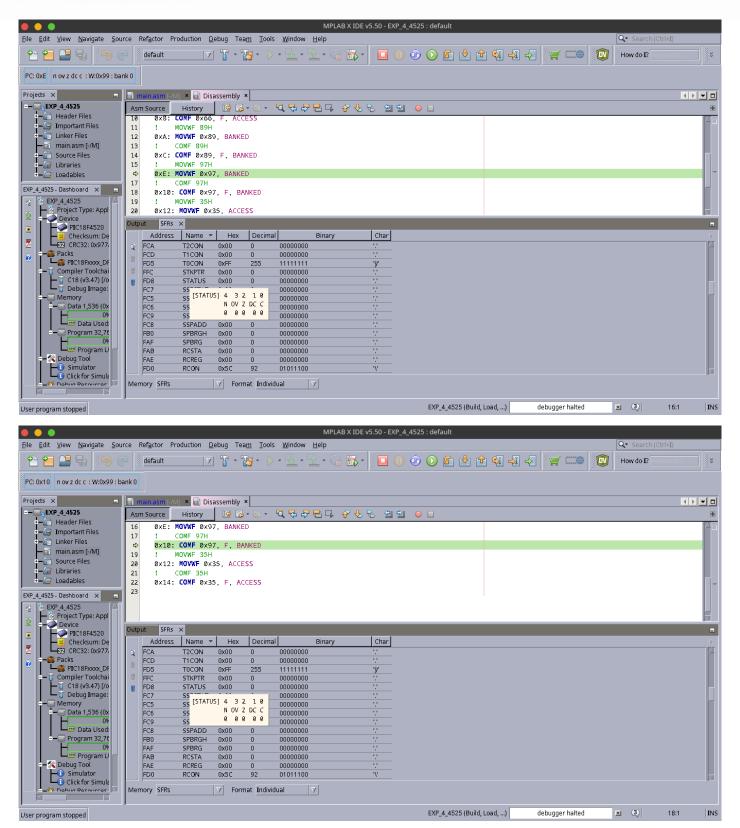








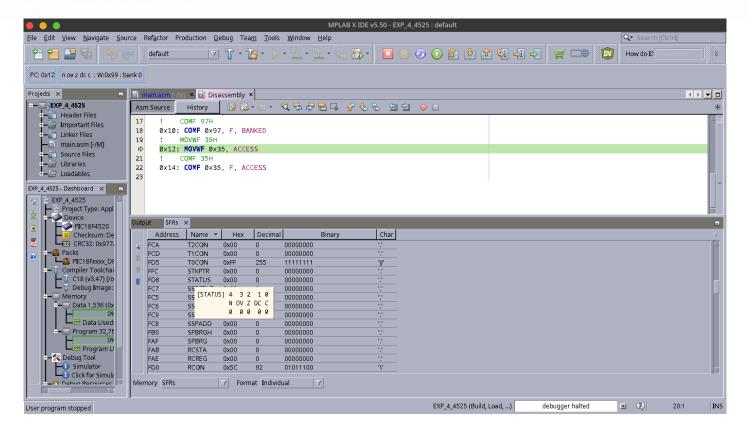












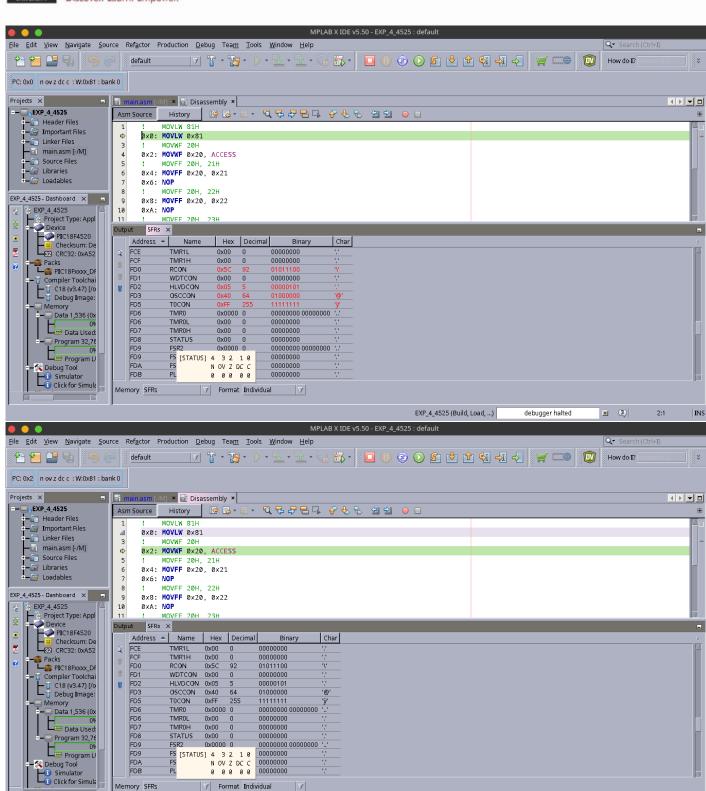
# 3B. Algorithm:

- Create the new asm file
- Write the header file
- Write the starting location
- Move the literal value to the WREG
- Move the working register to the file register
- Now, load the value of file register from 20H to 24H
- Now, Complement the file register











User program stopped

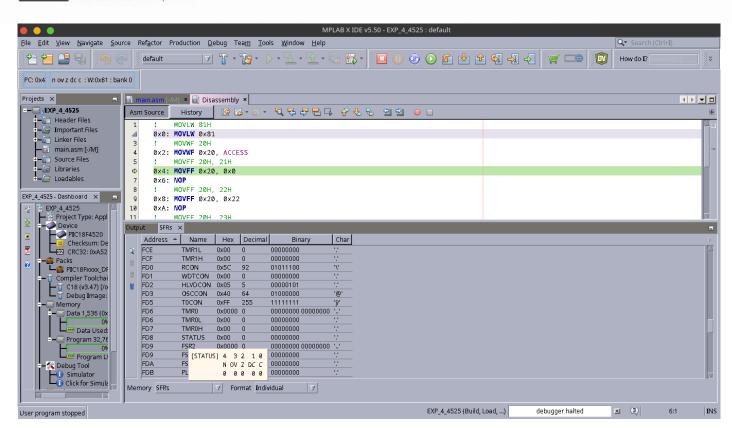
**x** (3)

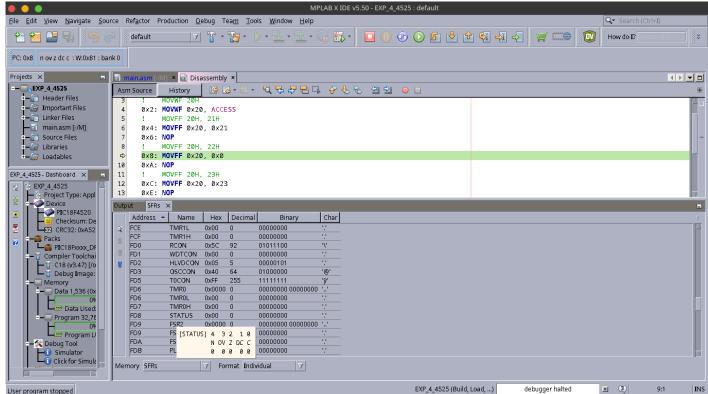
debugger halted

EXP\_4\_4525 (Build, Load, ...)





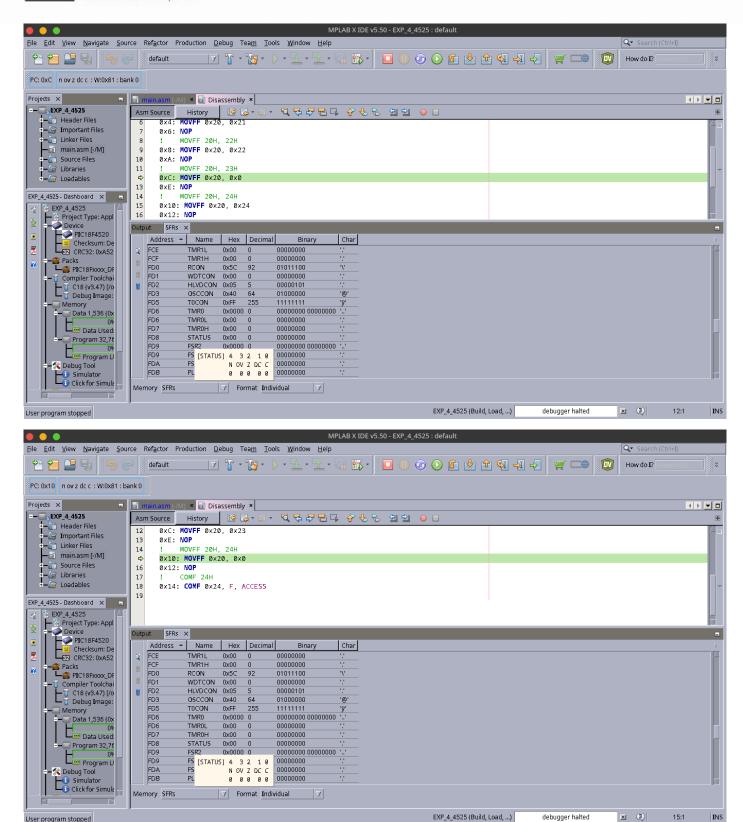








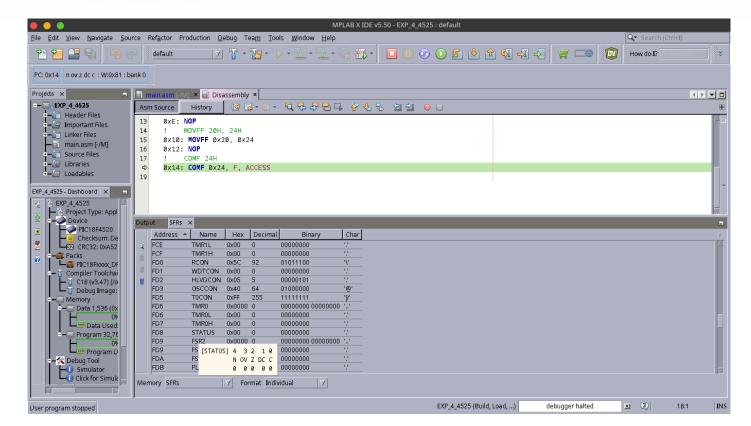


















#### 4A. Source Code:

#include p18f4520.inc
org 00
MOVLW 99H
MOVWF 01H
COMF 01H
MOVWF 66H
COMF 66H
MOVWF 89H
COMF 89H
MOVWF 97H
COMF 97H
MOVWF 35H
COMF 35H
END

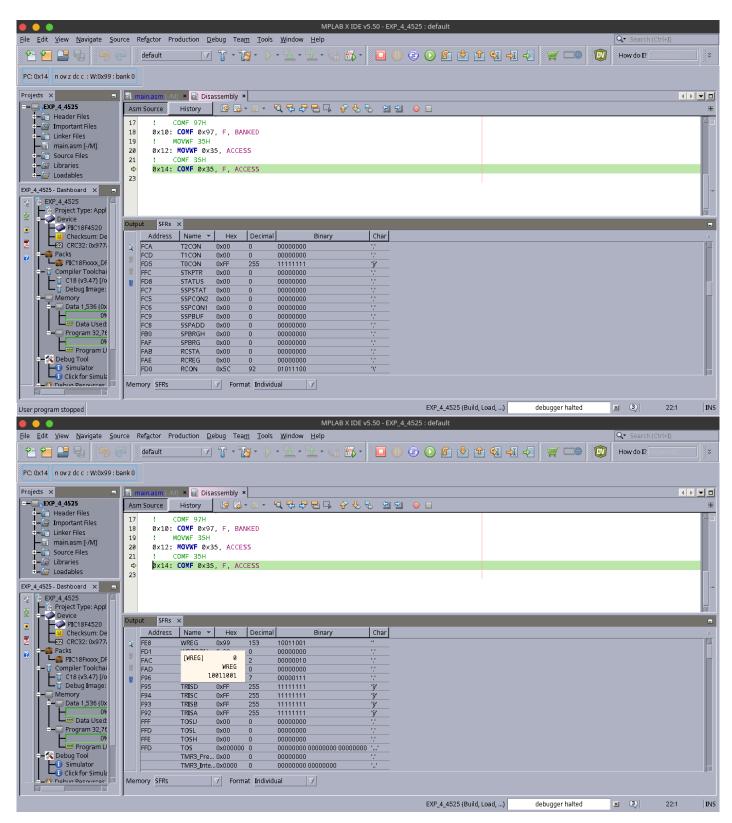
#### **4B Source Code:**

#include p18f4520.inc org 00 MOVLW 81H MOVWF 20H MOVFF 20H, 21H MOVFF 20H, 22H MOVFF 20H, 23H MOVFF 20H, 24H COMF 24H END





#### **5A. Observations:**

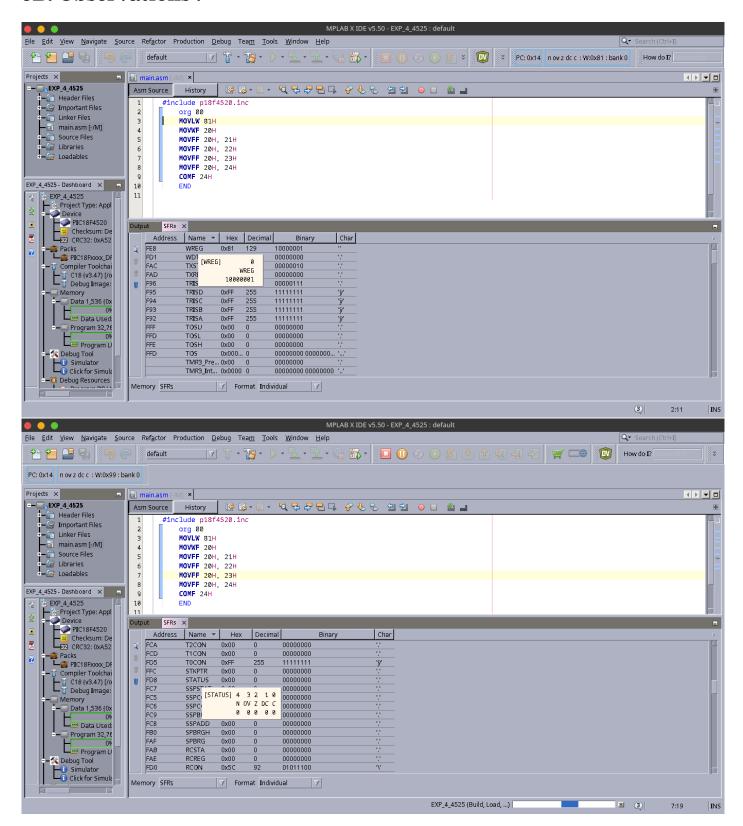








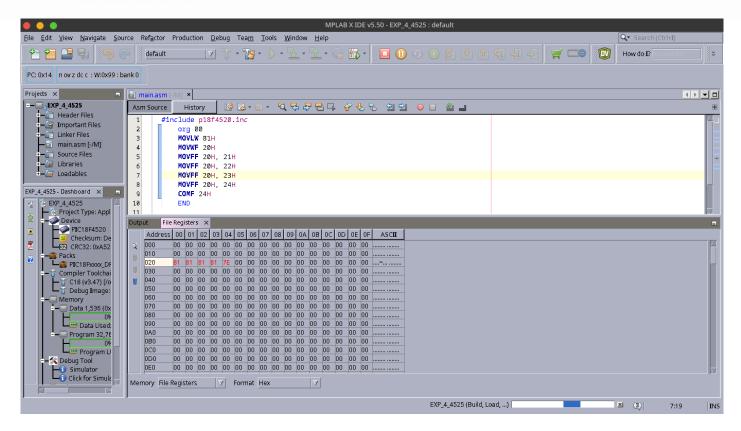
#### 5B. Observations:











## **Learning Outcomes:**

- Learn the different instructions such as COMF use to complement the file register.
- Learn to check the status register.
- Learn to load the value of file register and use complement of them.

S. No.	Parameters	Marks Obtained	Maximum Marks
1.			
2.			
3.			

