Experiment Number 1

Name ::	Rishabh Anand	UID ::	19BCS4525
Branch ::	CSE - IoT	Sec/Grp ::	1/A
Semester ::	5th	Date ::	2nd Sept, 2021
Subject ::	Embedded System Lab	CODE ::	CSD-333

1. Aim :

To examine and use MP Lab assembler and simulator.

2. Task :

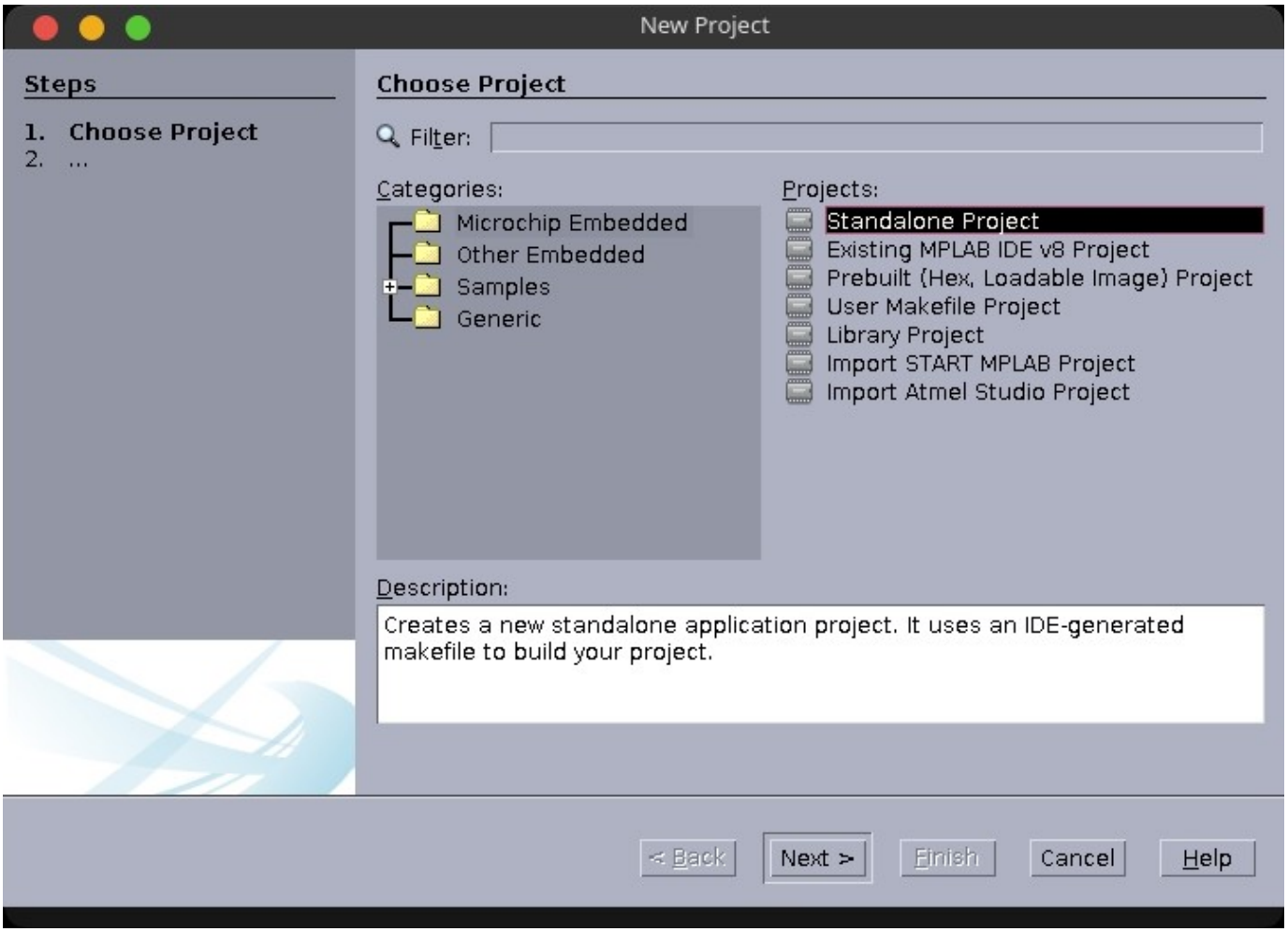
Write an assembly program to move value 80H to working register-WREG then from to WREG move it to file location 30H .

3. Algorithm :

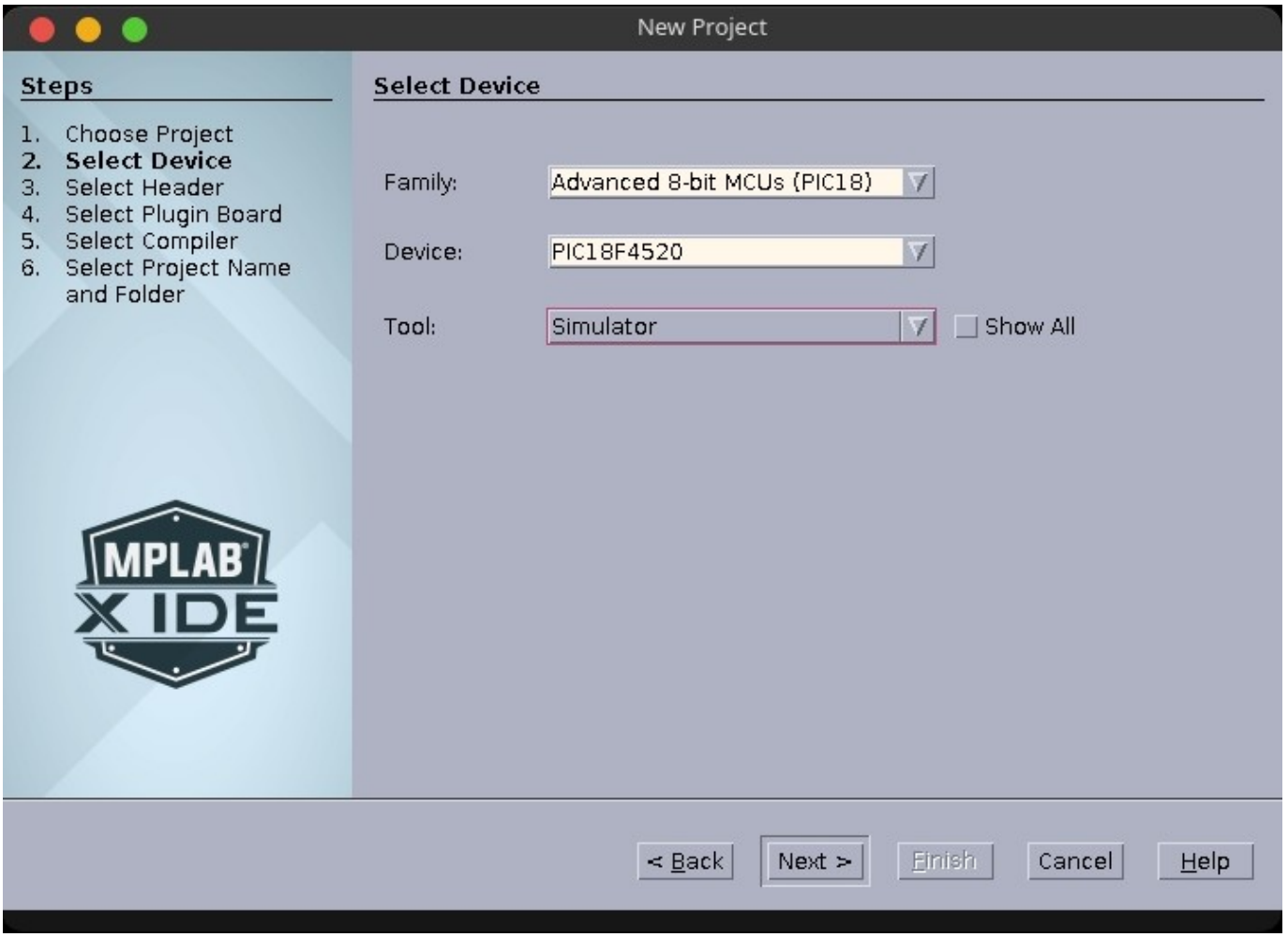
1. Move 80H at WREG
2. Move 80H from WREG to file location 30H

4. Steps / Source Code :

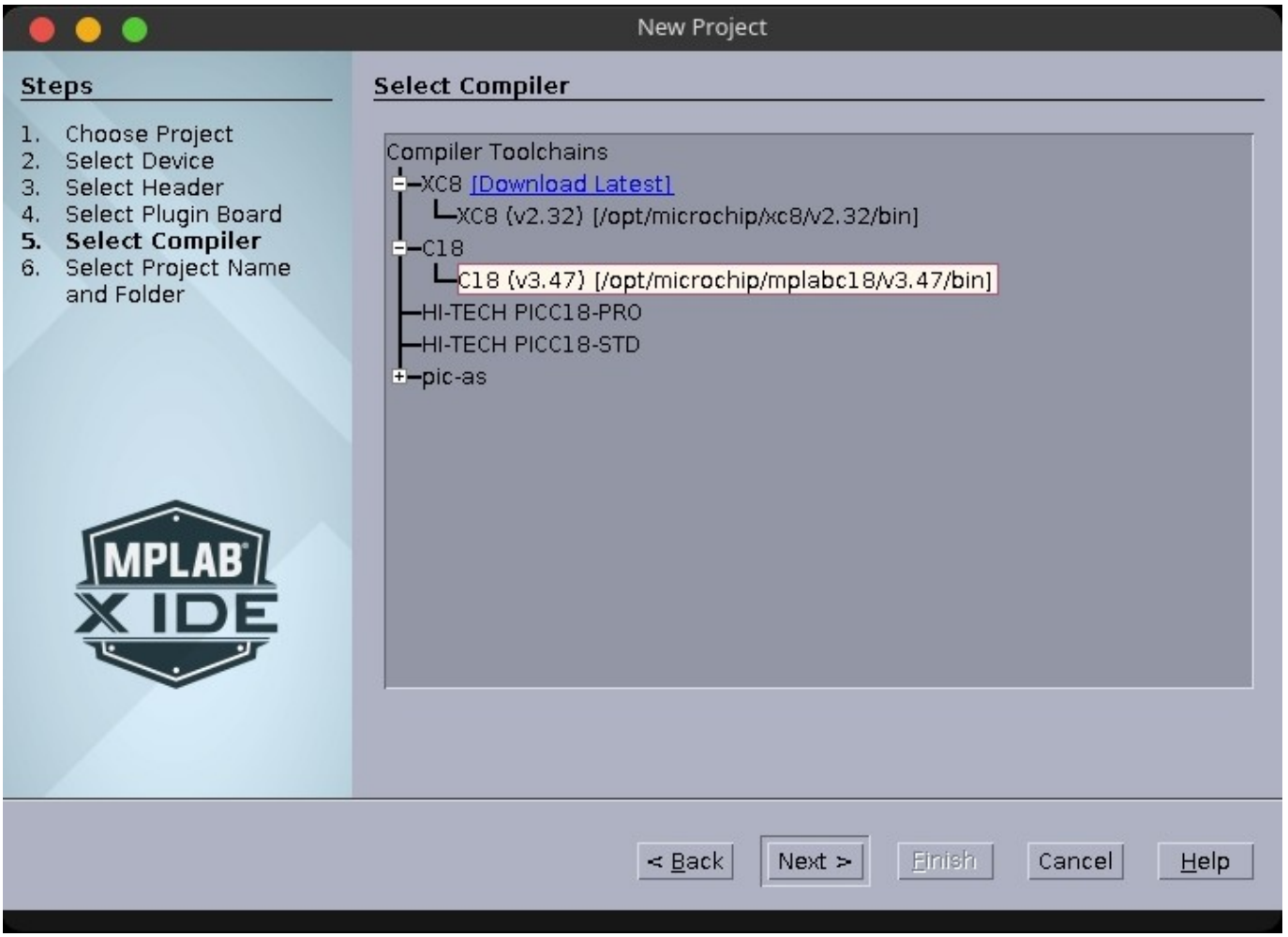
1. Make a new project



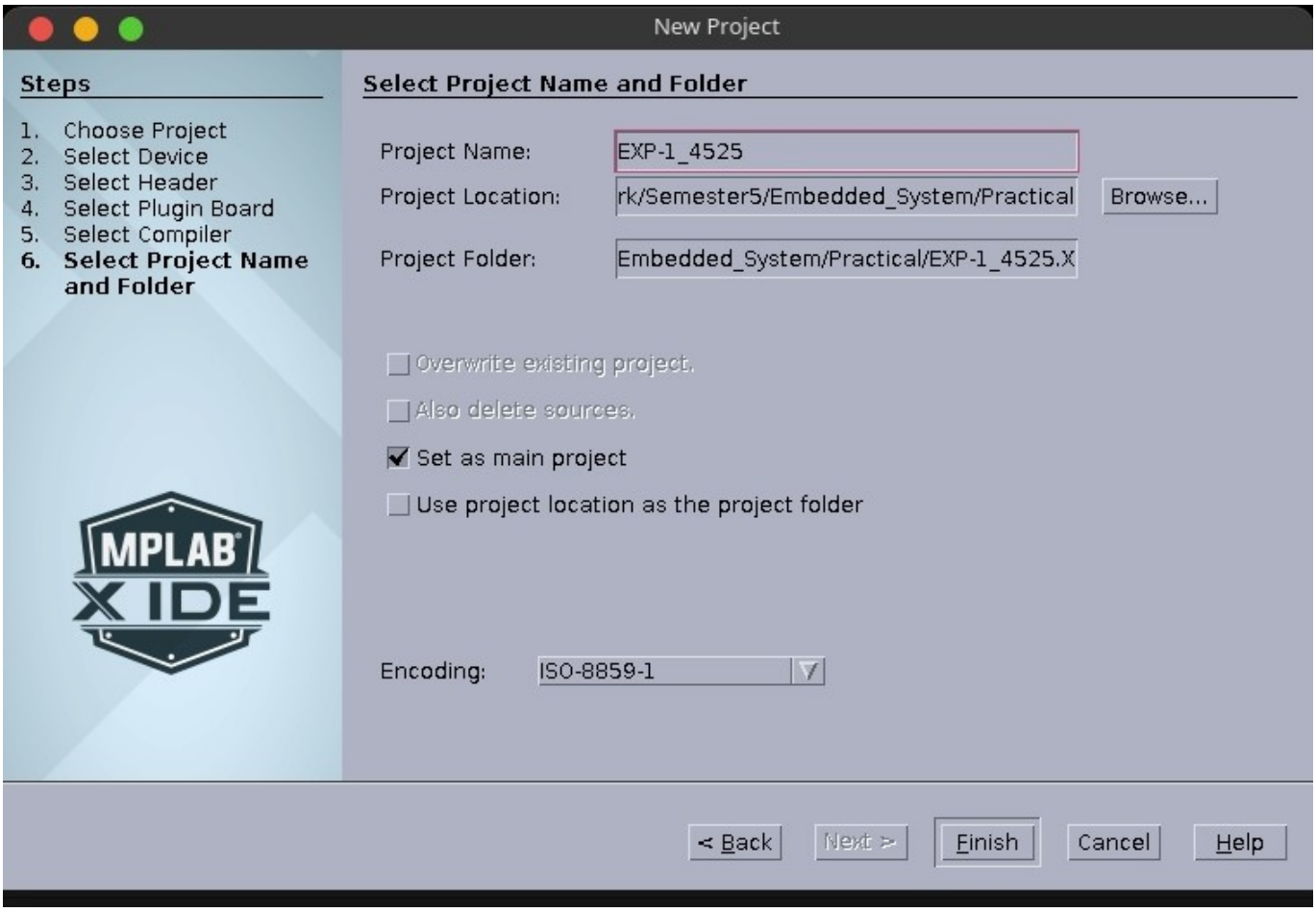
2. Choose Device



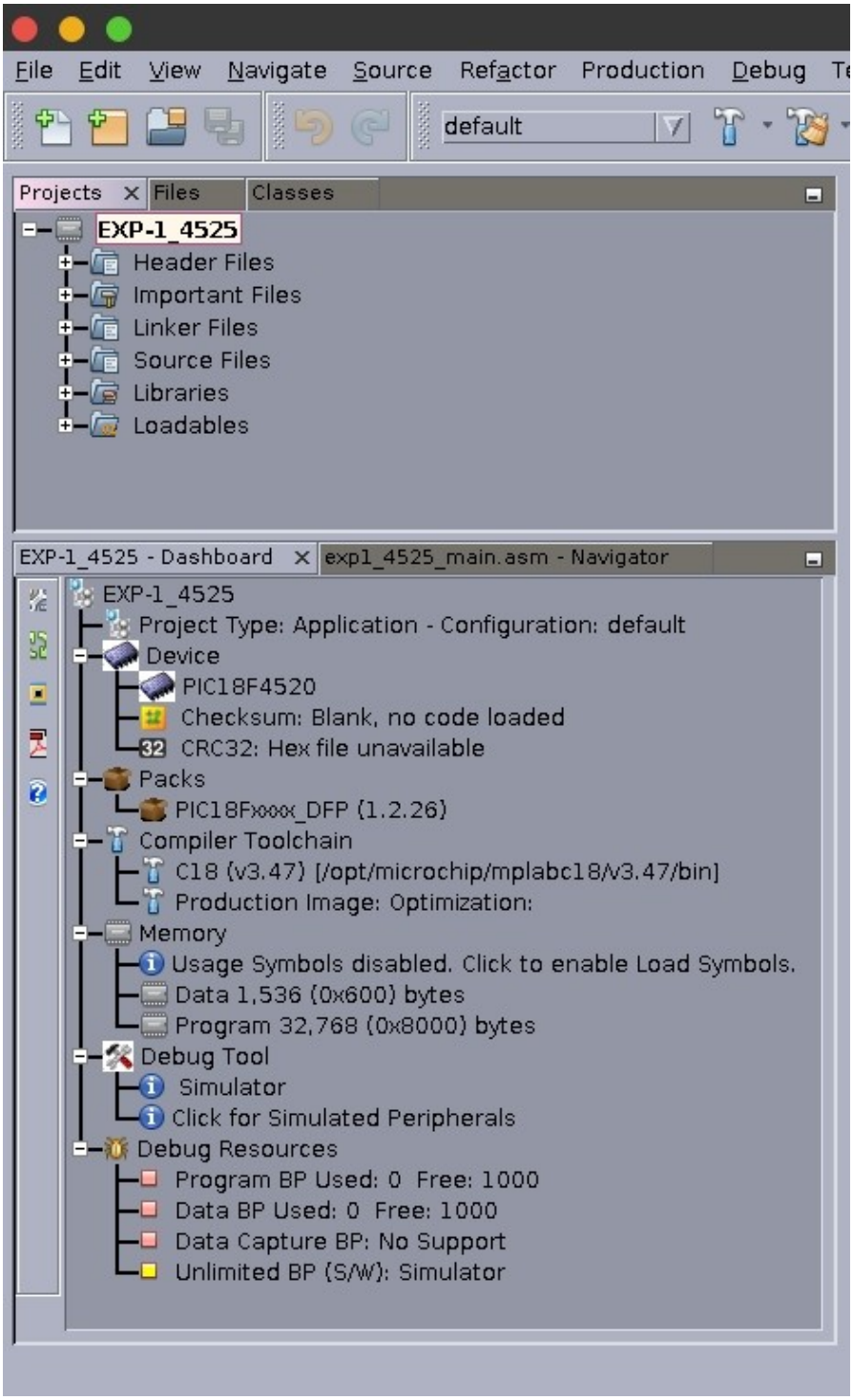
3. Choose Compiler



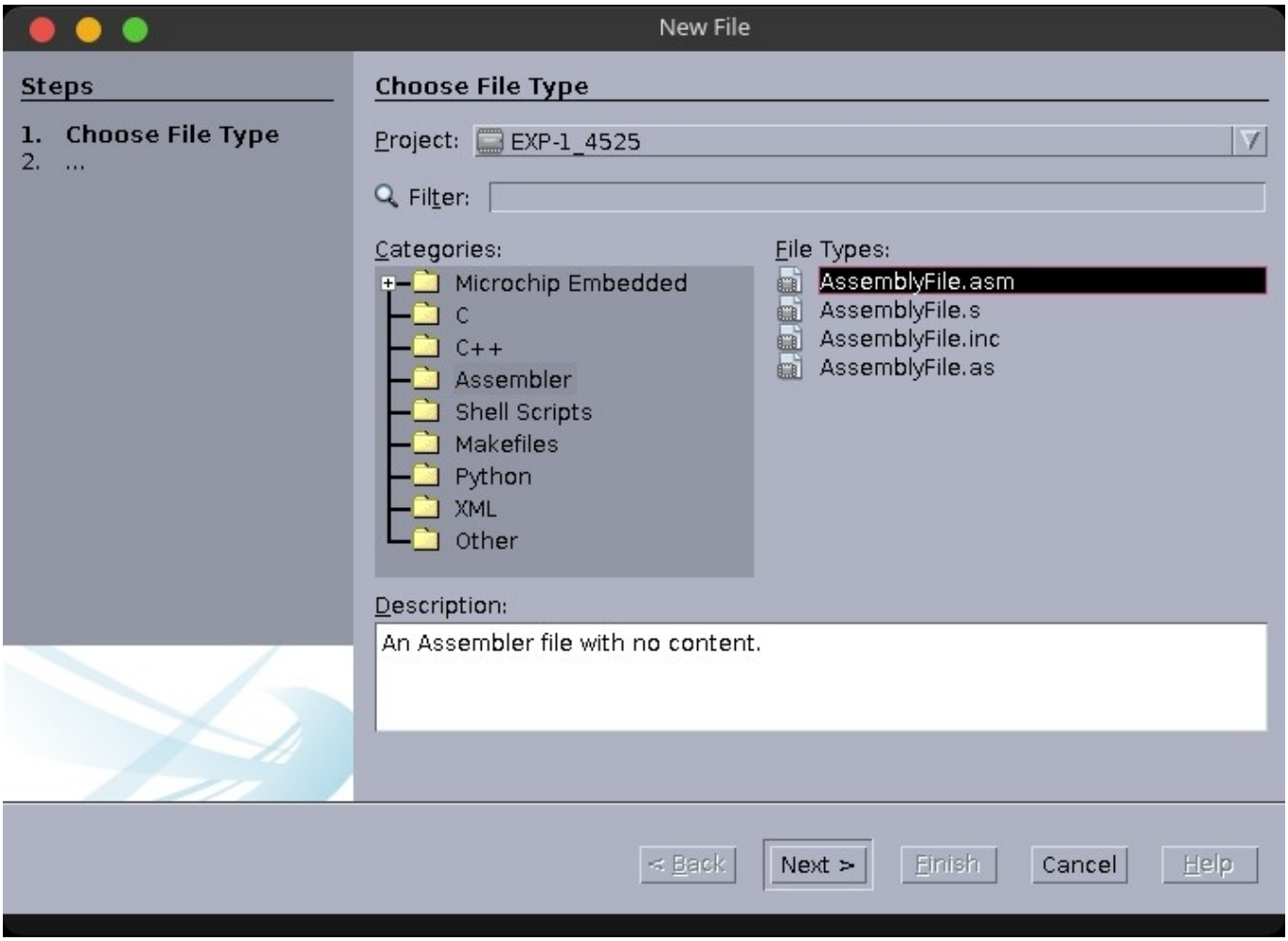
4. Set Project Name



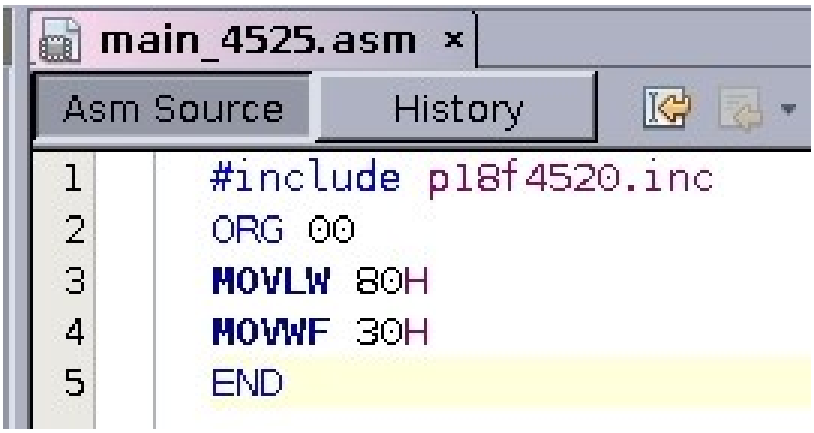
5. Project Created.



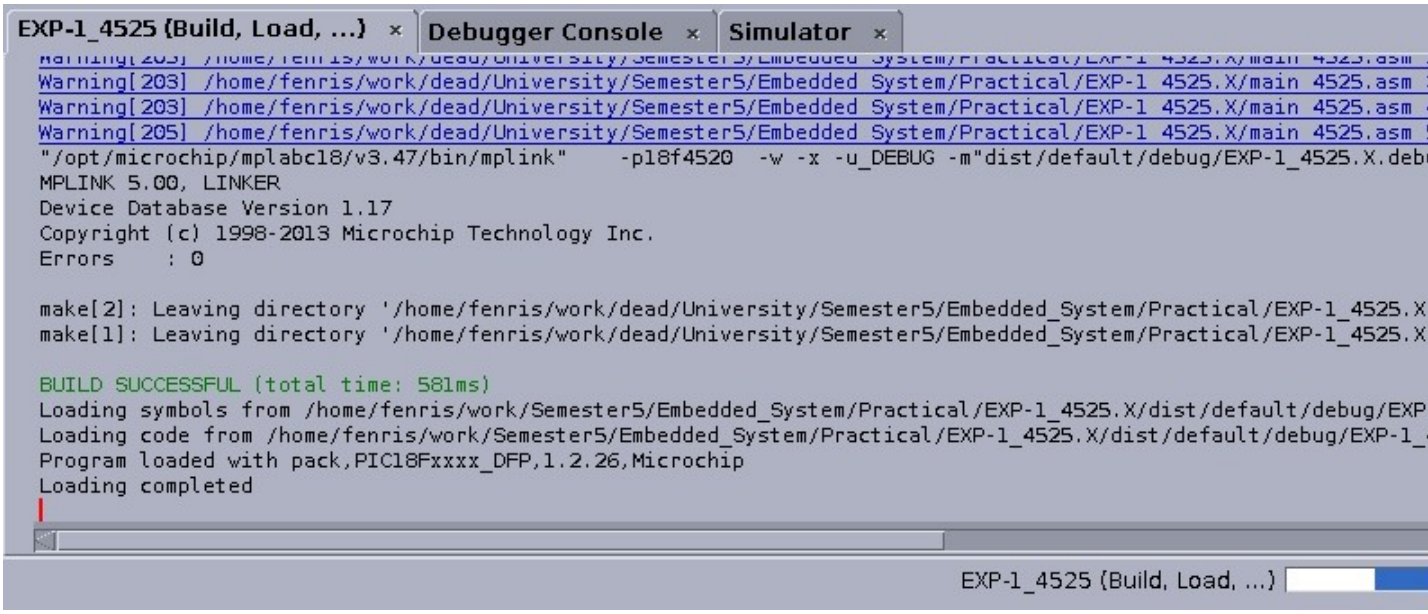
6. Create a new file now.



7. Write CODE.



8. Compile



5. Observations :

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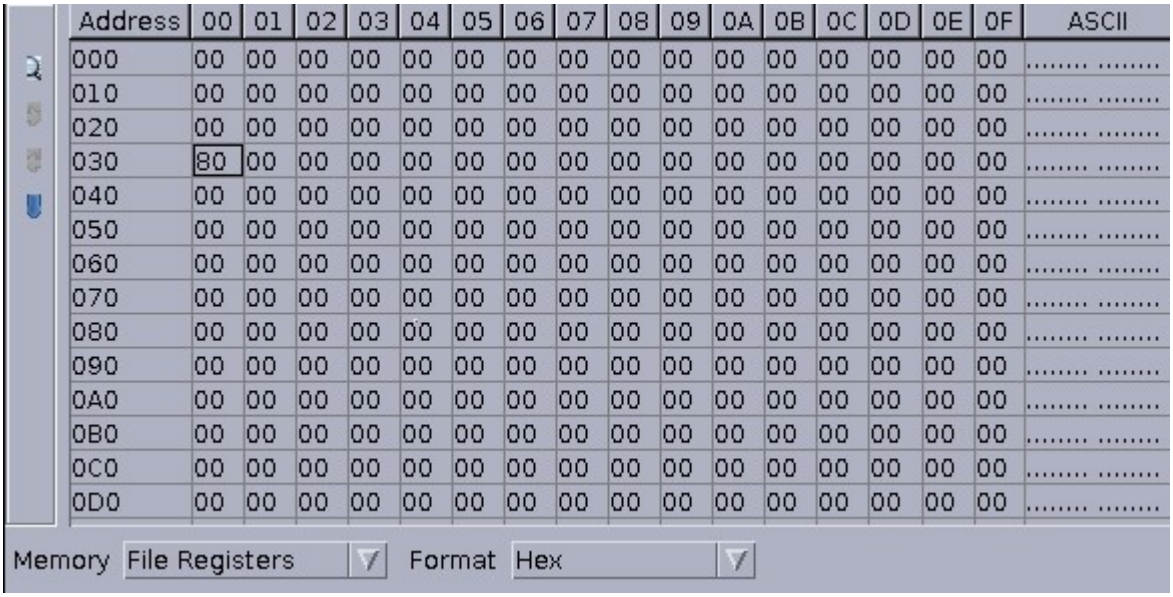
Address	Name	Hex	Decimal	Binary	Char
FE8	WREG	0x80	128	10000000	''
FD1	WDTCON	0x00	0	00000000	'.'
FAC	TXSTA	0x02	2	00000010	'.'
FAD	TXREG	0x00	0	00000000	'.'
F96	TRISE	0x07	7	00000111	'.'
F95	TRISD	0xFF	255	11111111	'ÿ'
F94	TRISC	0xFF	255	11111111	'ÿ'
F93	TRISB	0xFF	255	11111111	'ÿ'
F92	TRISA	0xFF	255	11111111	'ÿ'
FFF	TOSU	0x00	0	00000000	'.'
FFD	TOSL	0x00	0	00000000	'.'
FFE	TOSH	0x00	0	00000000	'.'
FFD	TOS	0x000...	0	00000000 0000000...	'...'
	TMR3_Prescale	0x00	0	00000000	'.'

Memory SFRs Format Individual

- 

Address	00	01	02	03	04	05	06	07	08	09	0A	0B	0C	0D	0E	0F	ASCII
000	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	....
010	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	....
020	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	....
030	80	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	....
040	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	....
050	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	....
060	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	....
070	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	....
080	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	....
090	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	....
0A0	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	....
0B0	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	....
0C0	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	....
0D0	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	....

Memory File Registers Format Hex

Learning Outcomes :

- MPLABX IDE
- WREG

S. No.	Parameters	Marks Obtained	Maximum Marks
1.			
2.			
3.			