

https://github.com/theguydanish/CM4_MATX

Copyright Oliver Møller/©TheGuyDanish 2020, CERN OHL-P v2

Sheet: /ATX_PSU/

File: CM4_ATX_PSU.sch

Title: CM4_MATX: ATX PSU Connection

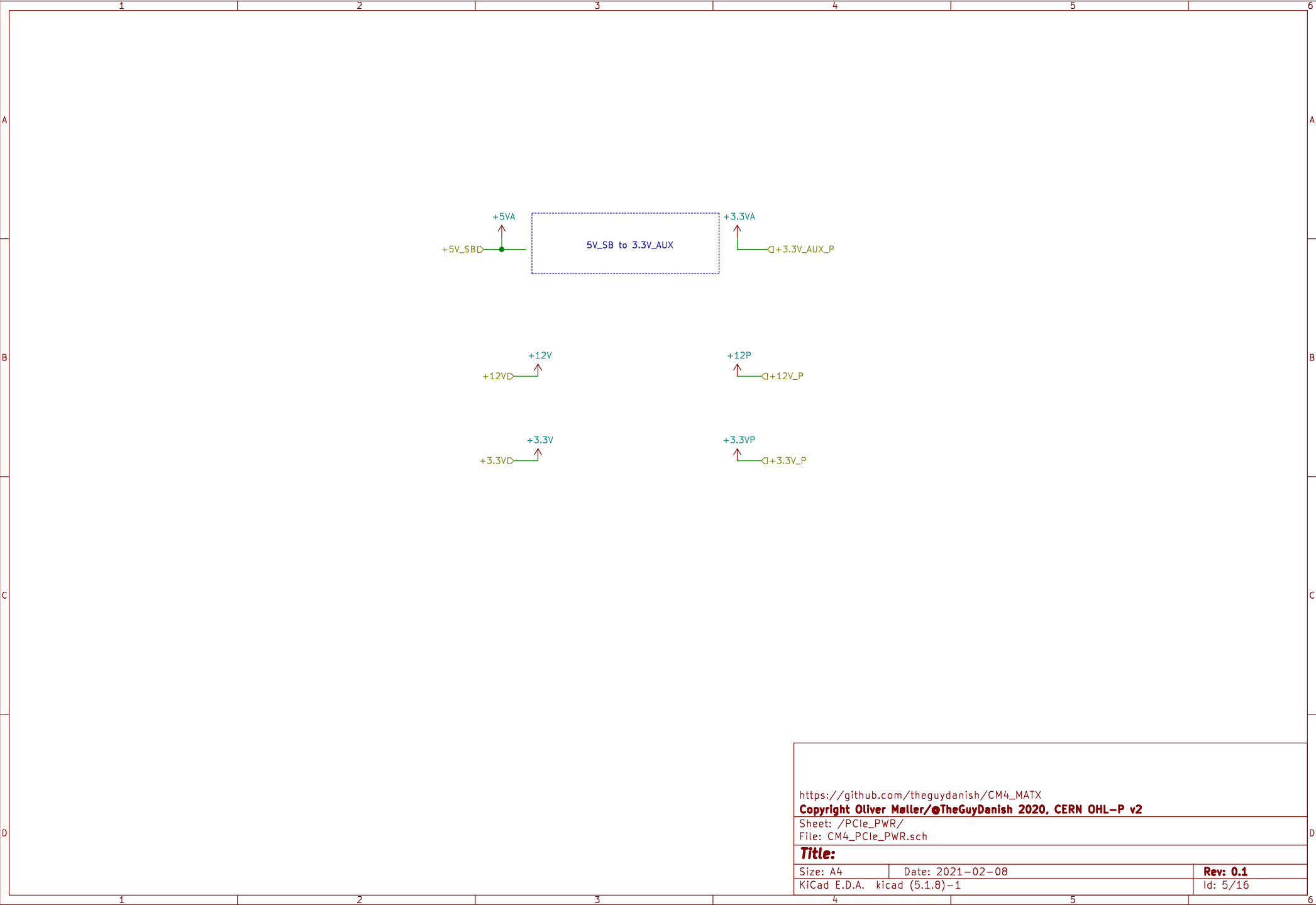
Size: A4

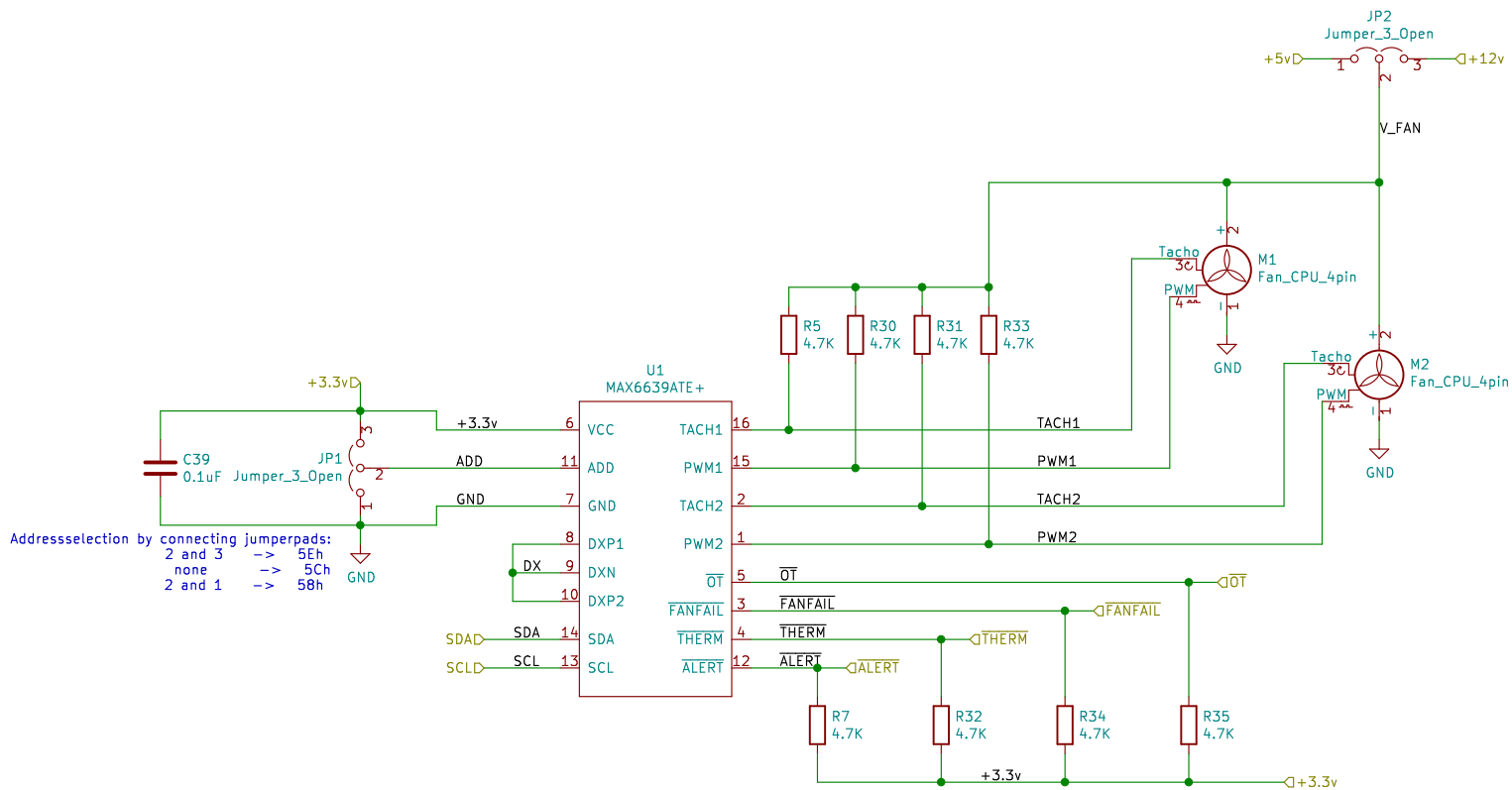
Date: 2021-02-08

Rev: 0.1

KiCad E.D.A. kicad (5.1.8)-1

Id: 3/16





https://github.com/theguydanish/CM4_MATX

Copyright Oliver Møller/©TheGuyDanish 2020, CERN OHL-P v2

Sheet: /FanCTRL_MAX6639/

File: CM4_FanCTRL_MAX6639.sch

Title:

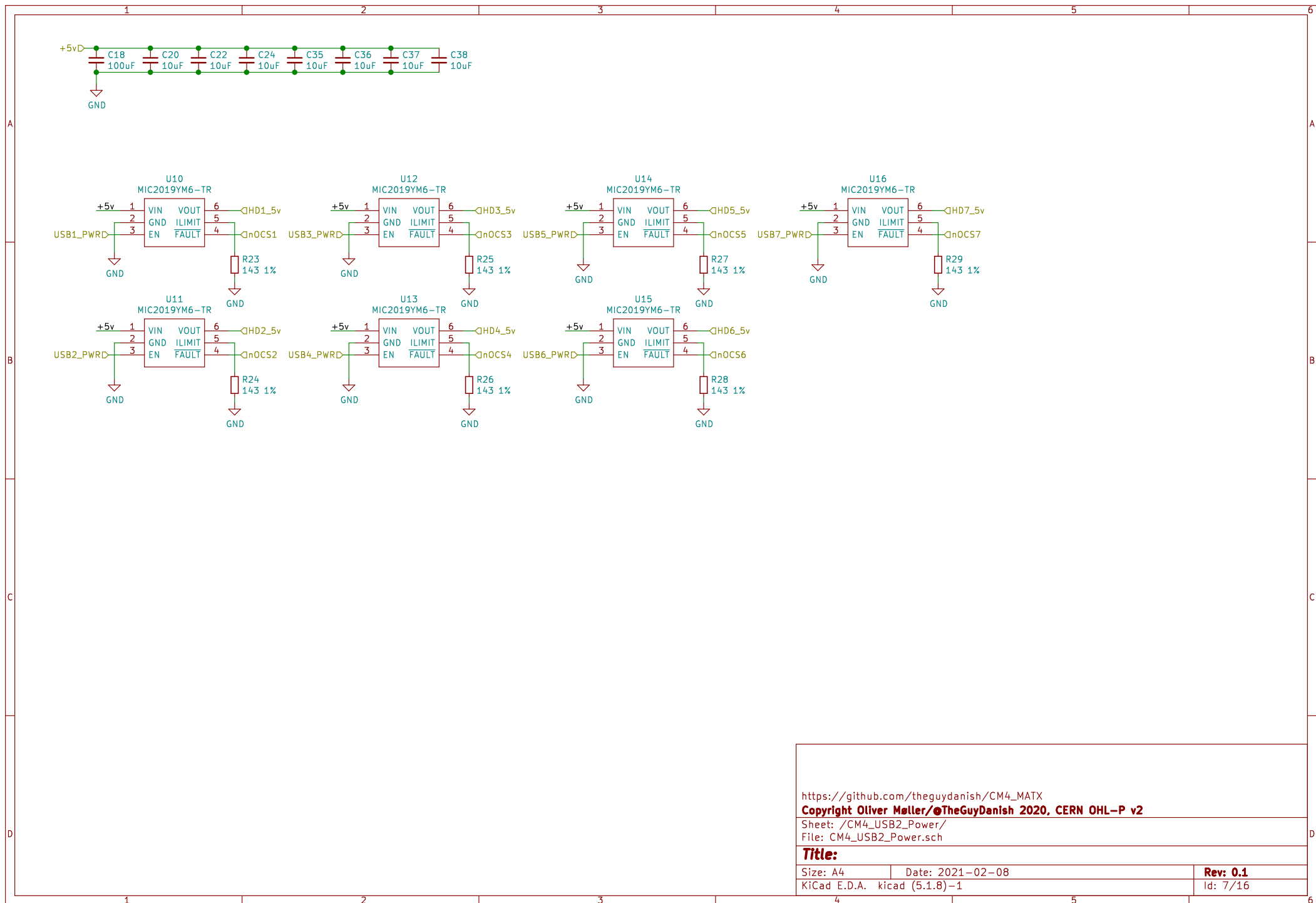
Size: A4

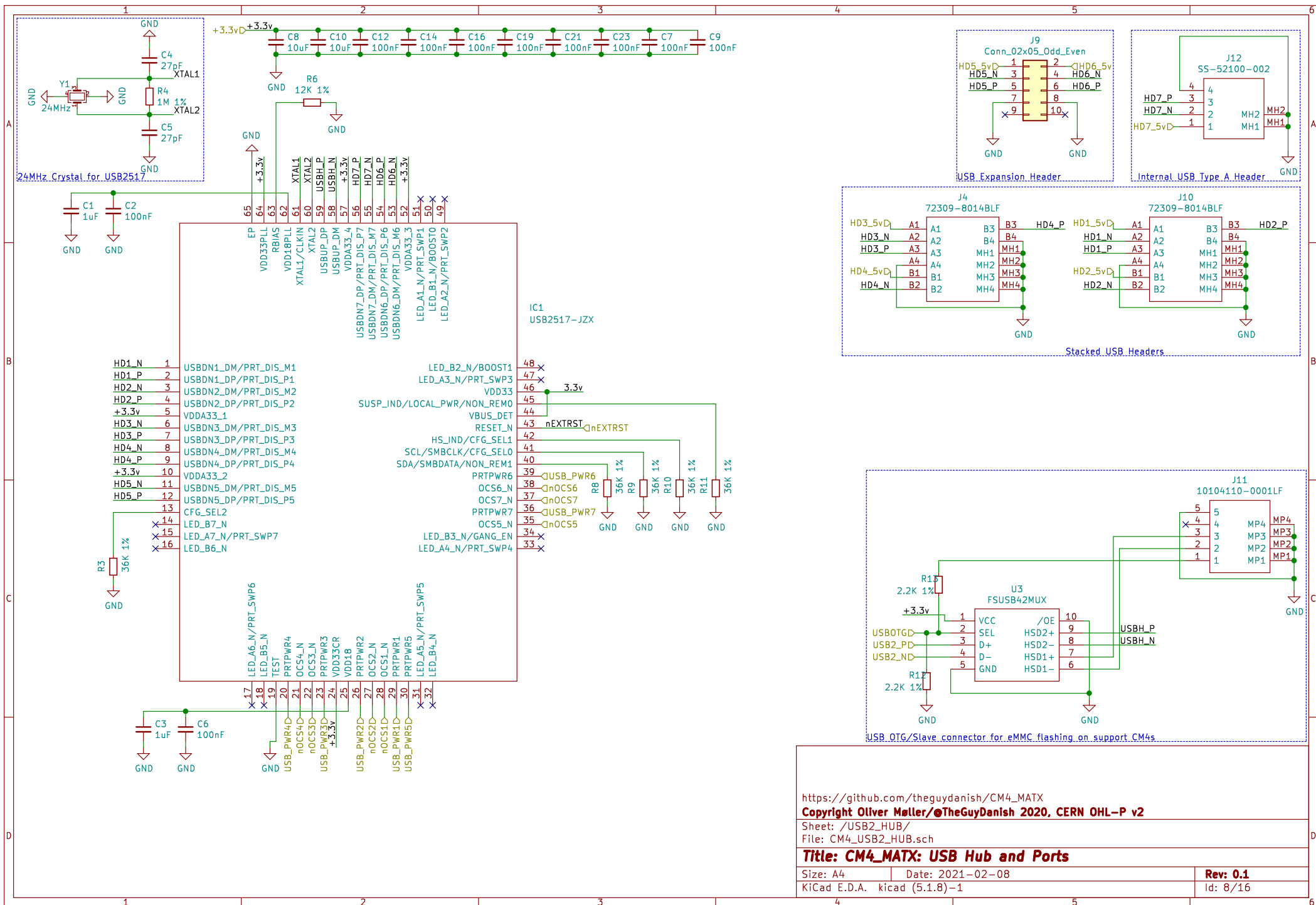
Date: 2021-02-08

Rev: 0.1

KiCad E.D.A. kicad (5.1.8)-1

Id: 6/16





https://github.com/theguydanish/CM4_MATX

Copyright Oliver Møller/©TheGuyDanish 2020, CERN OHL-P v2

Sheet: /USB2_HUB/

File: CM4_USB2_HUB.sch

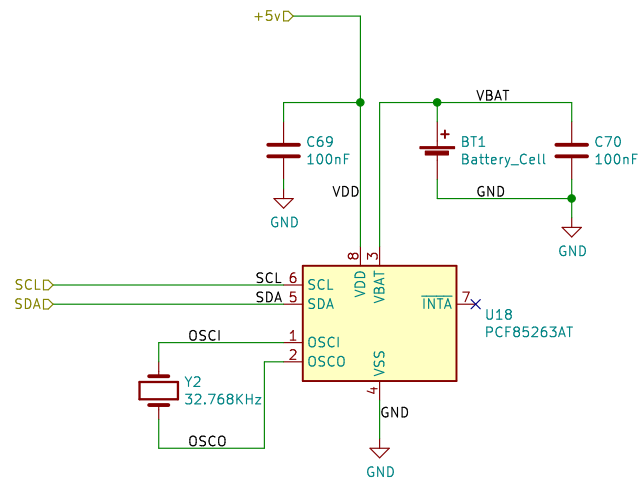
Title: CM4_MATX: USB Hub and Ports

Size: A4 Date: 2021-02-08

KiCad E.D.A. kicad (5.1.8)-1

Rev: 0.1

Id: 8/16



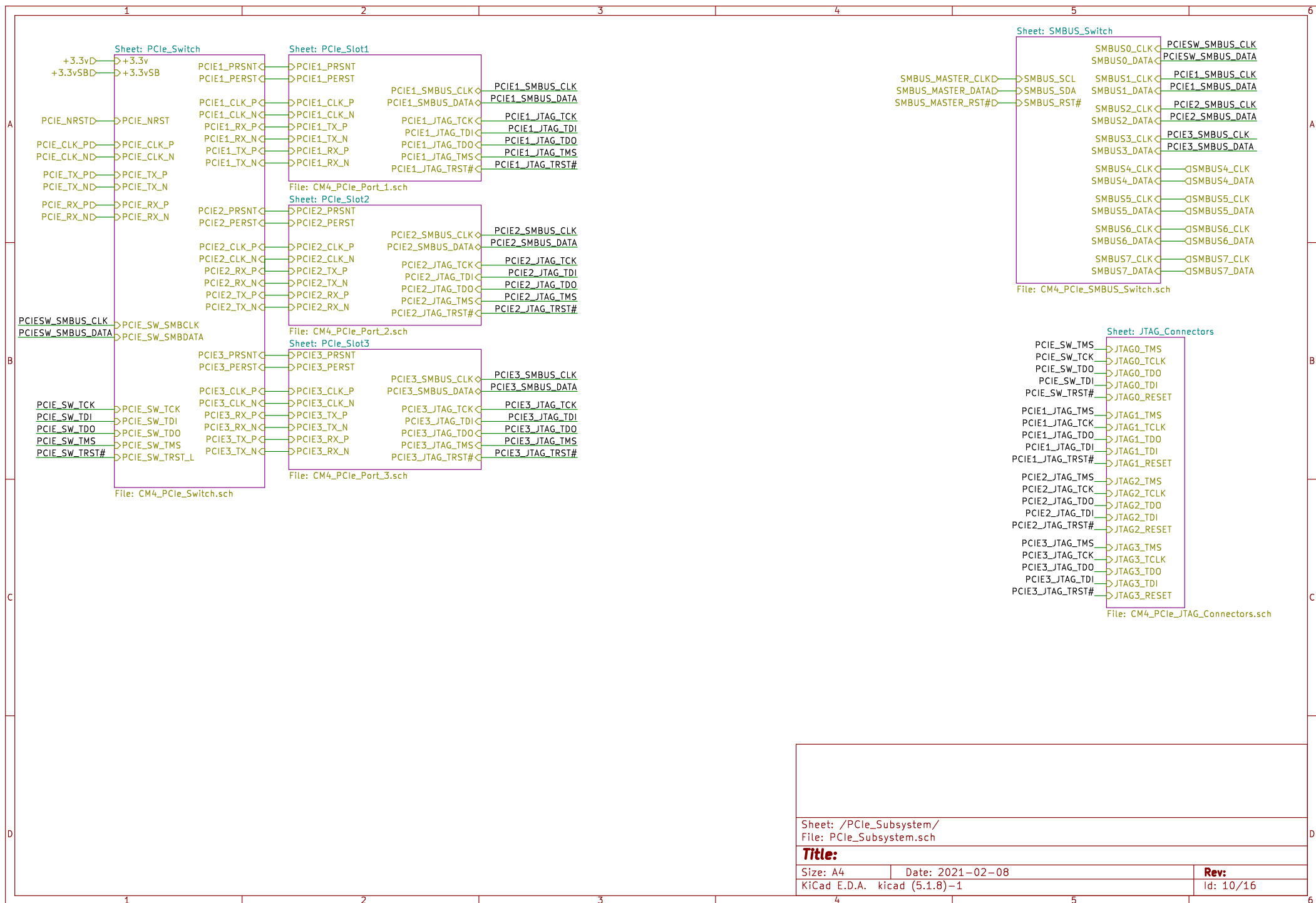
Sheet: /RTC_PCF85263A/
File: CM4_RTC_PCF85263A.sch

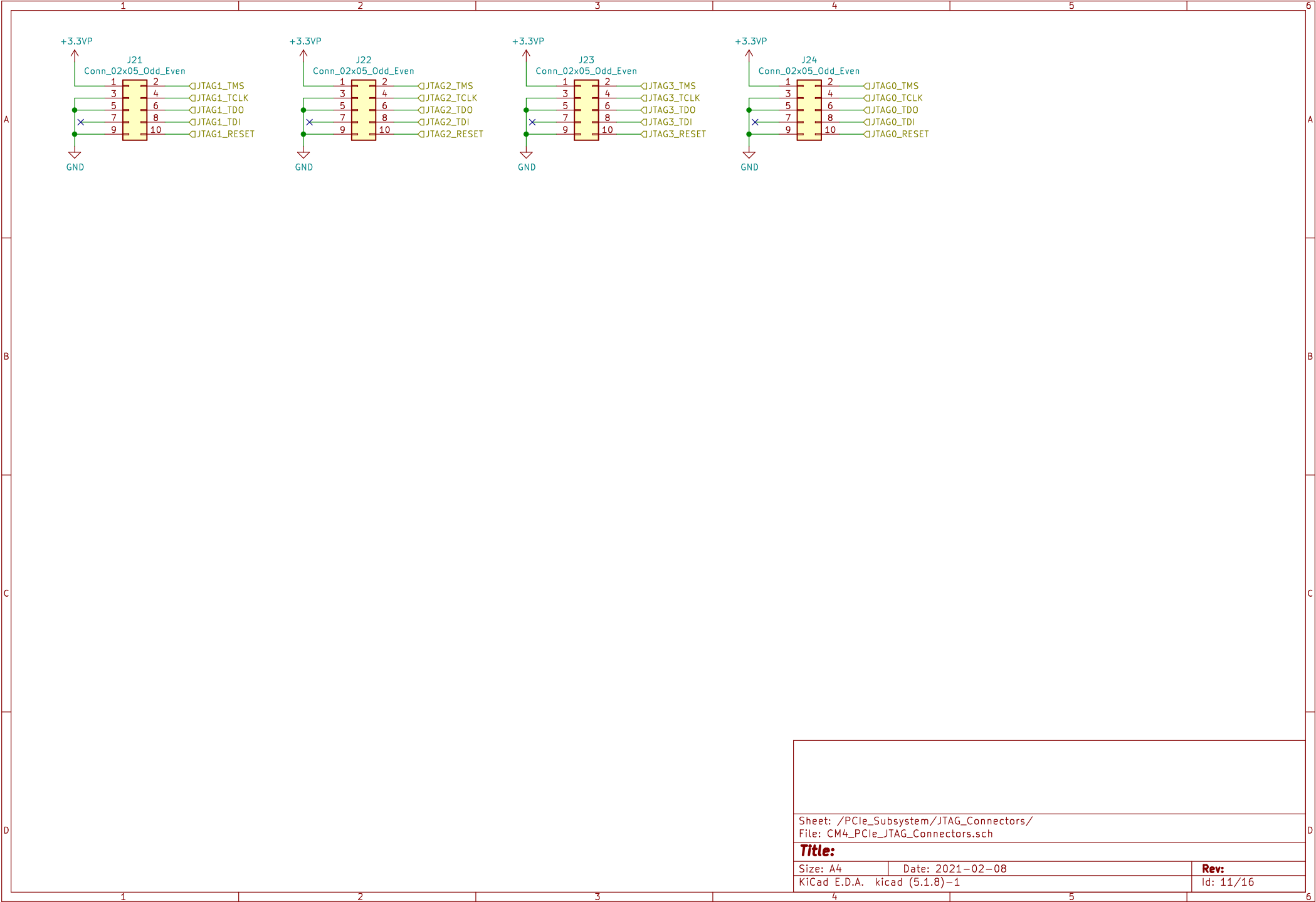
Title:

Size: A4 Date: 2021-02-08

KiCad E.D.A. kicad (5.1.8)-1

Rev:
Id: 9/16





Sheet: /PCIe_Subsystem/JTAG_Connectors/
File: CM4_PCIe_JTAG_Connectors.sch

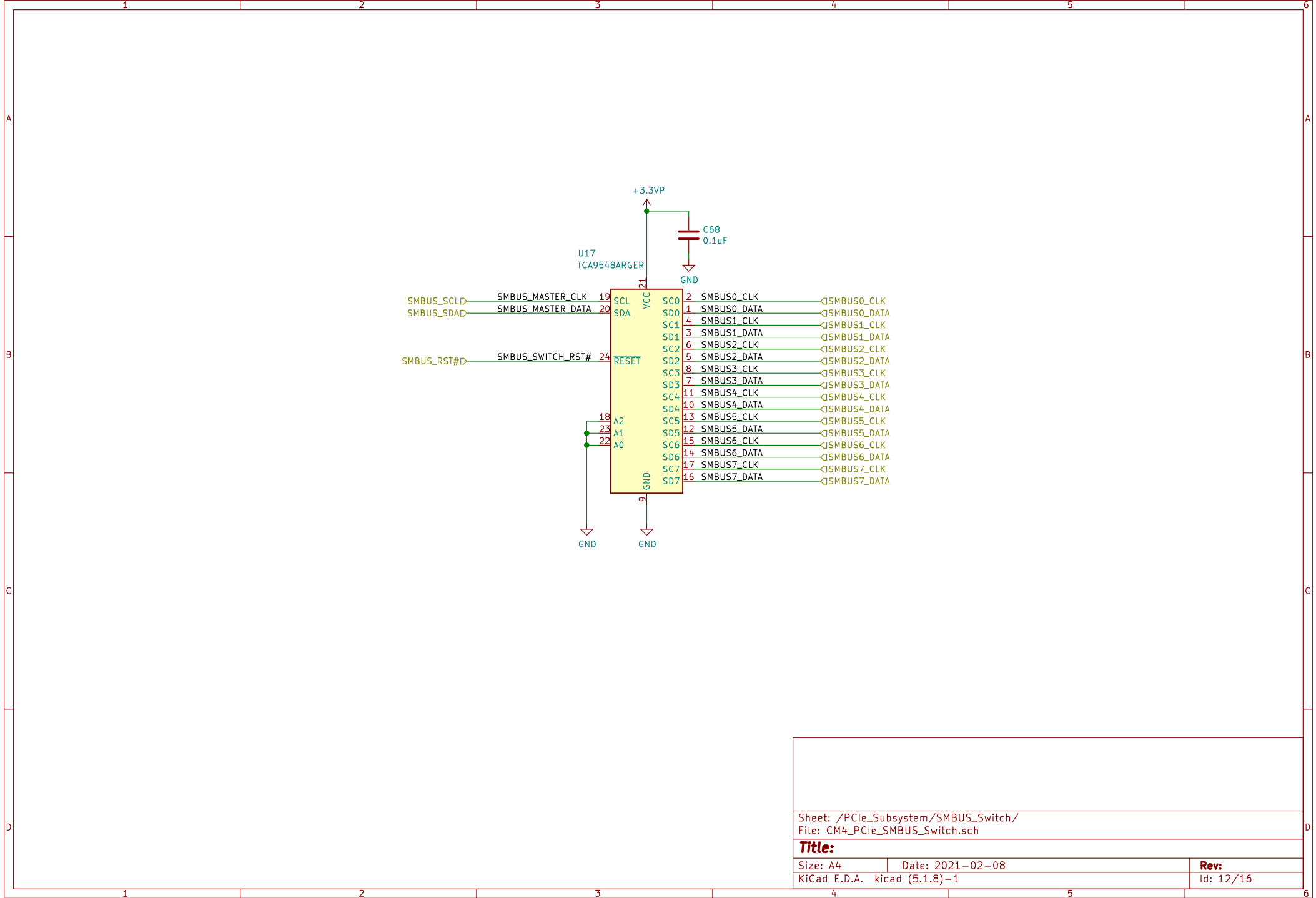
Title:

Size: A4 Date: 2021-02-08

KiCad E.D.A. kicad (5.1.8)-1

Rev:

Id: 11/16



Sheet: /PCIe_Subsystem/SMBUS_Switch/
File: CM4_PCIe_SMBUS_Switch.sch

Title:

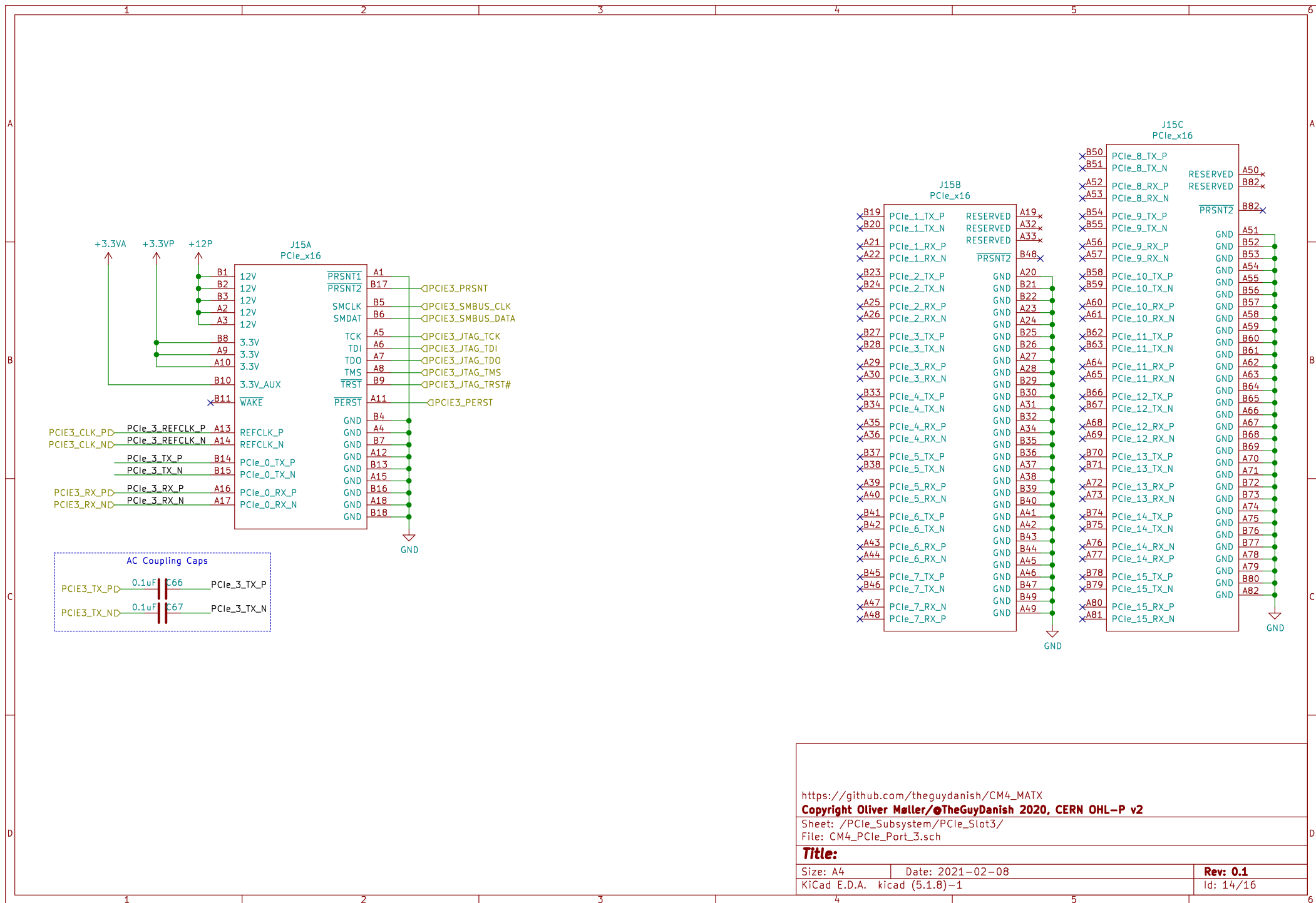
Size: A4 Date: 2021-02-08

KiCad E.D.A. kicad (5.1.8)-1

Rev:

Id: 12/16





https://github.com/theguydanish/CM4_MATX

Copyright Oliver Møller/©TheGuyDanish 2020, CERN OHL-P v2

Sheet: /PCIe_Subsystem/PCIe_Slot3/
File: CM4_PCIe_Port_3.sch

Title:

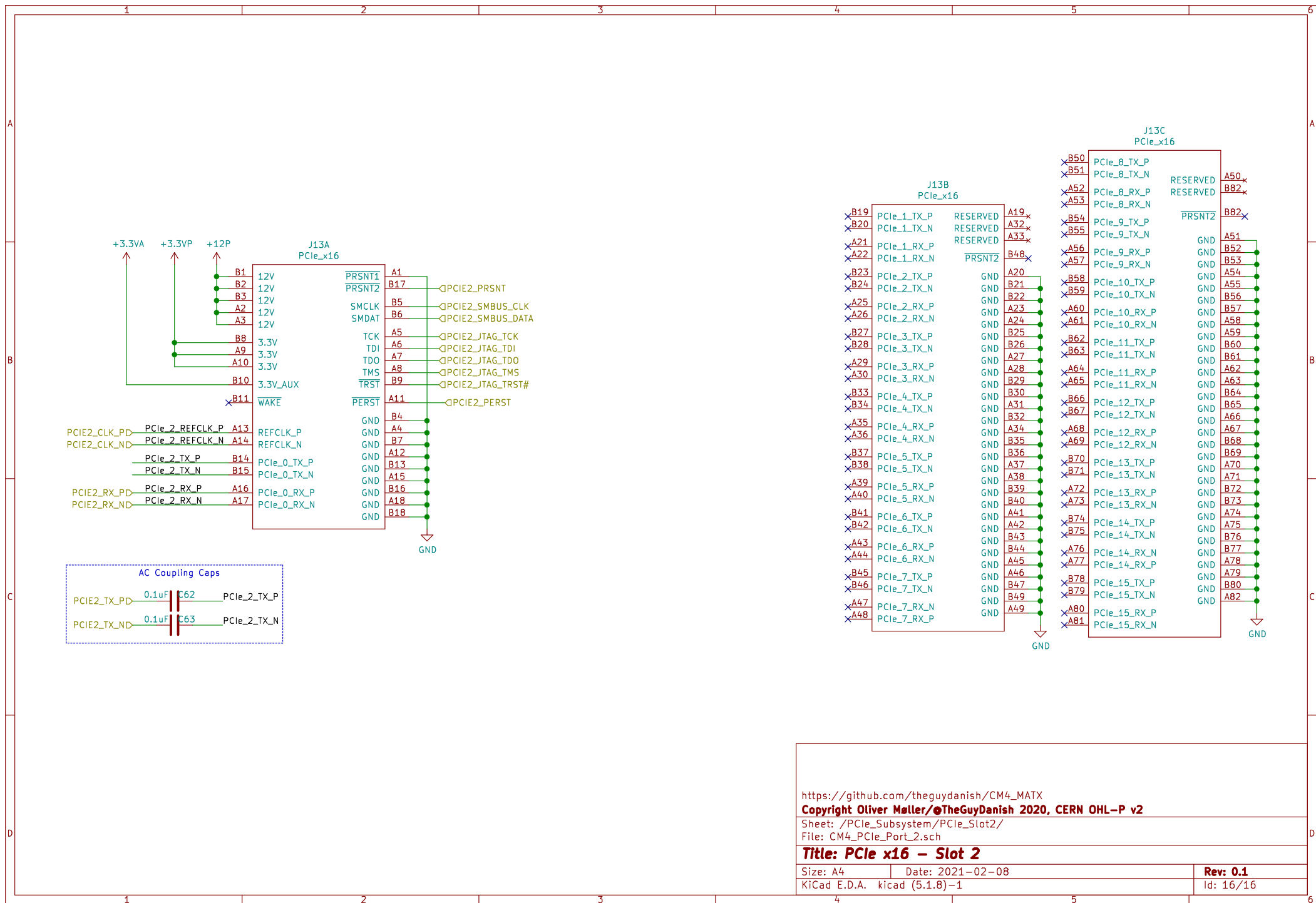
Size: A4 Date: 2021-02-08

KiCad E.D.A. kicad (5.1.8)-1

Rev: 0.1

Id: 14/16





https://github.com/theguydanish/CM4_MATX

Copyright Oliver Møller @TheGuyDanish 2020, CERN OHL-P v2

Sheet: /PCie_Subsystem/PCie_Slot2/

File: CM4_Pcie_Port_2.sch

Title: PCie x16 – Slot 2

Size: A4 Date: 2021-02-08

KiCad E.D.A. kicad (5.1.8)-1

Rev: 0.1

Id: 16/16