

https://github.com/theguydanish/CM4_MATX

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Sheet: /CM4_GPIO/

File: CM4_GPIO.sch

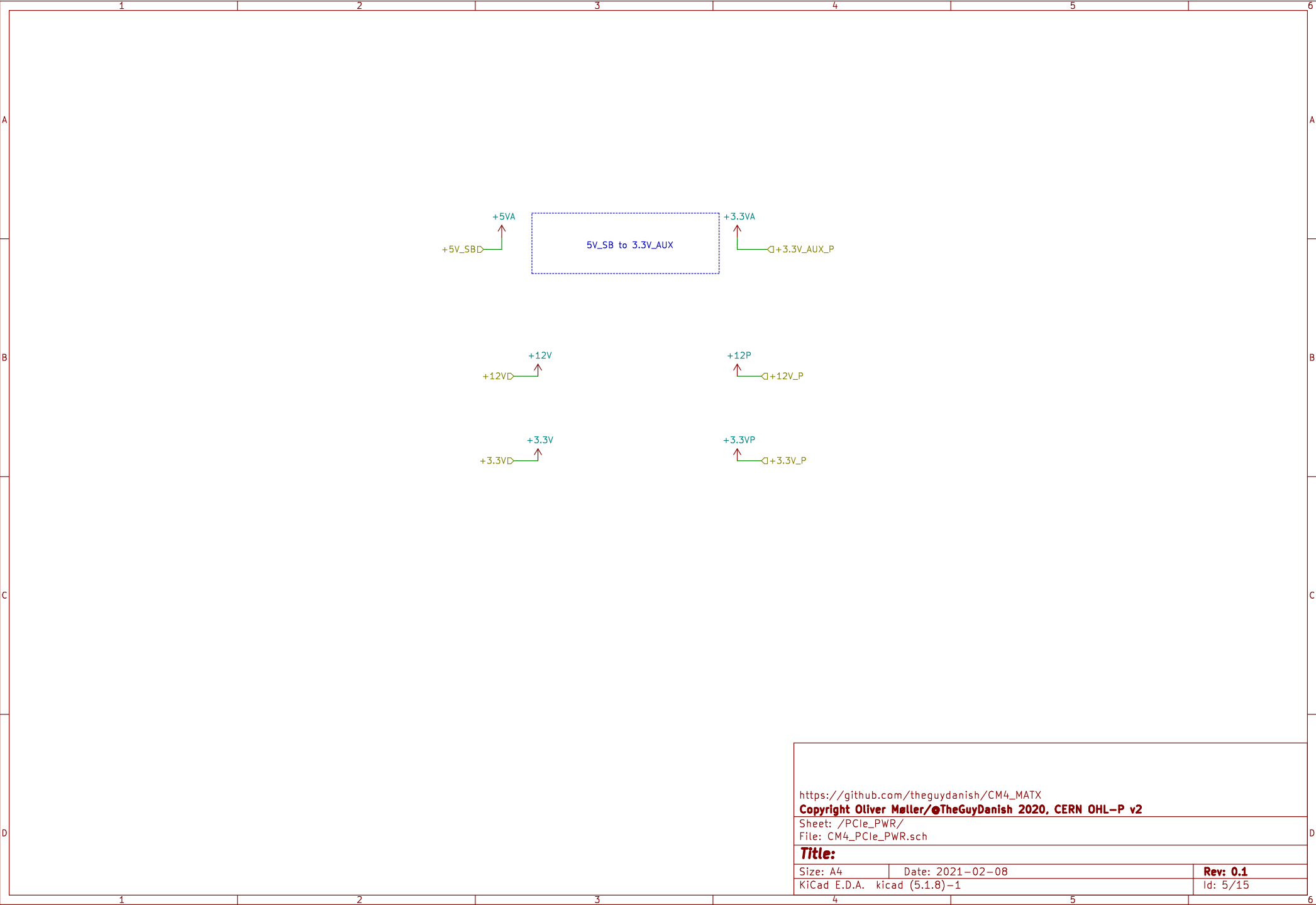
Title: GPIO, SDIO, Pin Headers, Ethernet

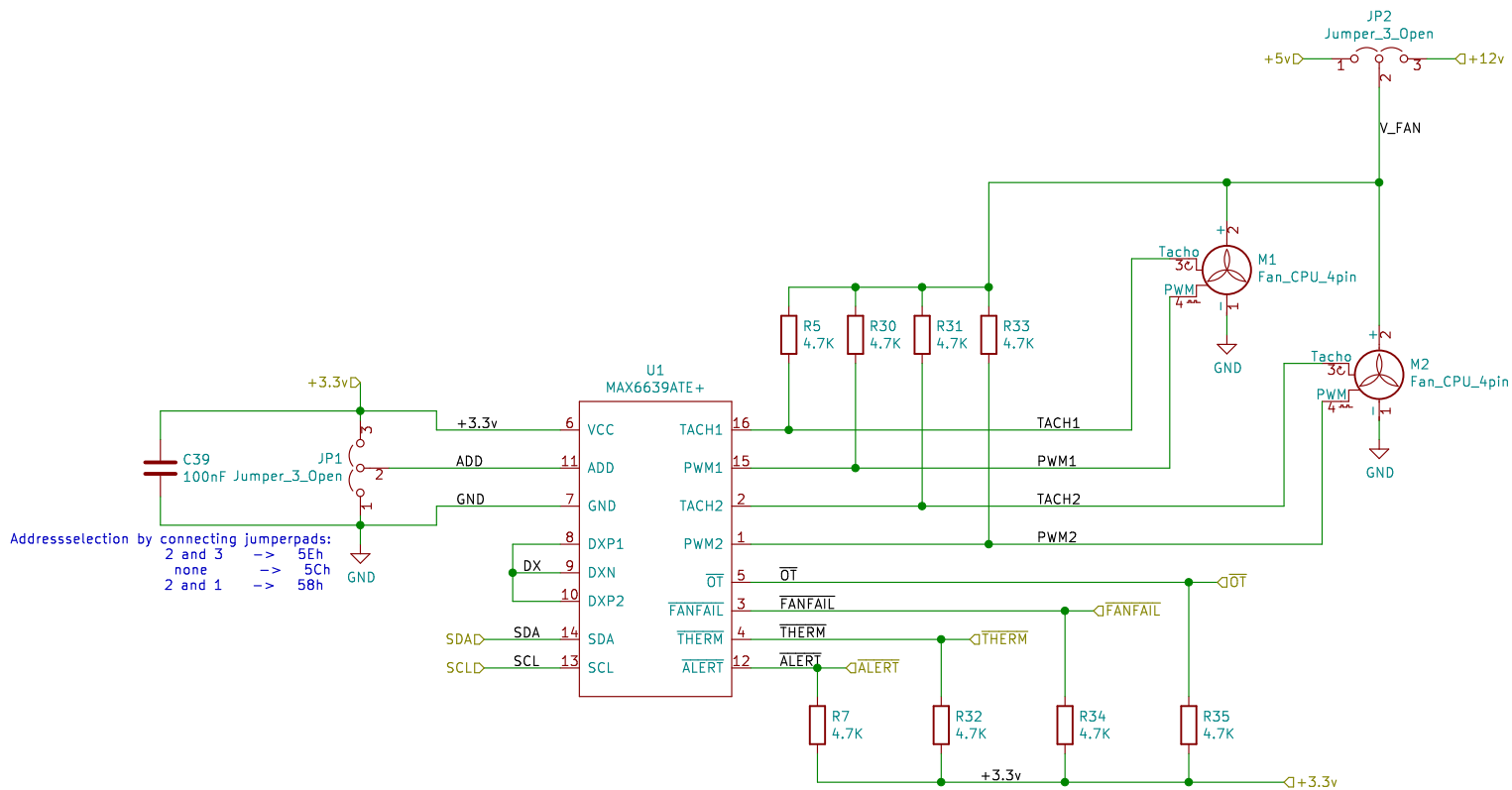
Size: A4 Date: 2021-02-08

KiCad E.D.A. kicad (5.1.8)-1

Rev: 0.1

Id: 4/15





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Sheet: /FanCTRL_MAX6639/

File: CM4_FanCTRL_MAX6639.sch

Title:

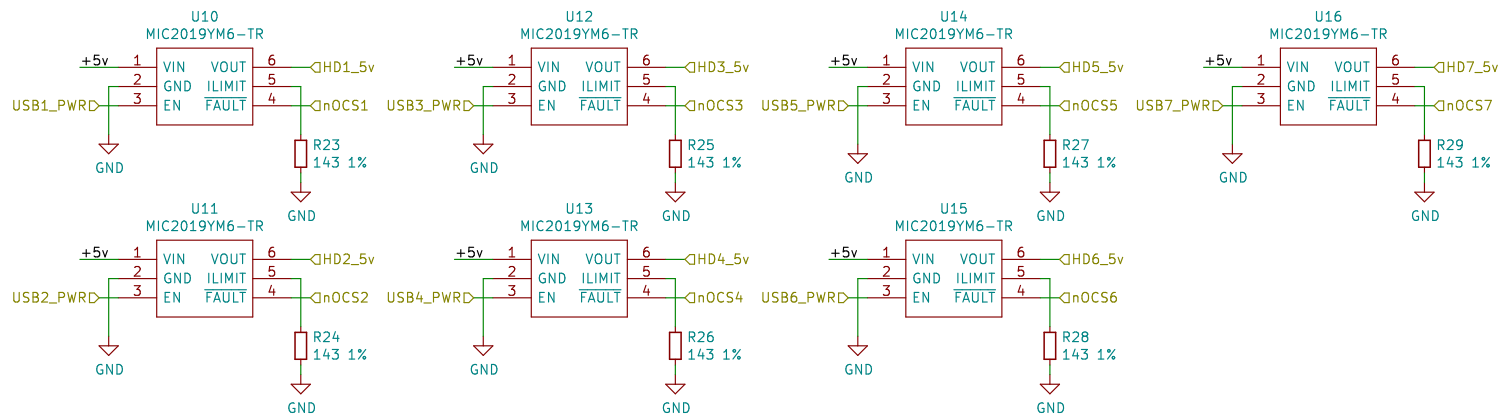
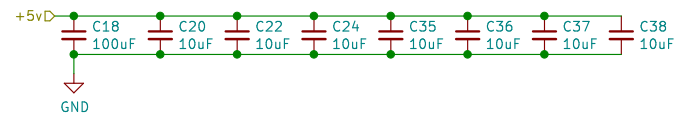
Size: A4

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Rev: 0.1

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Id: 6/15



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Sheet: /CM4_USB2_Power/

File: CM4_USB2_Power.sch

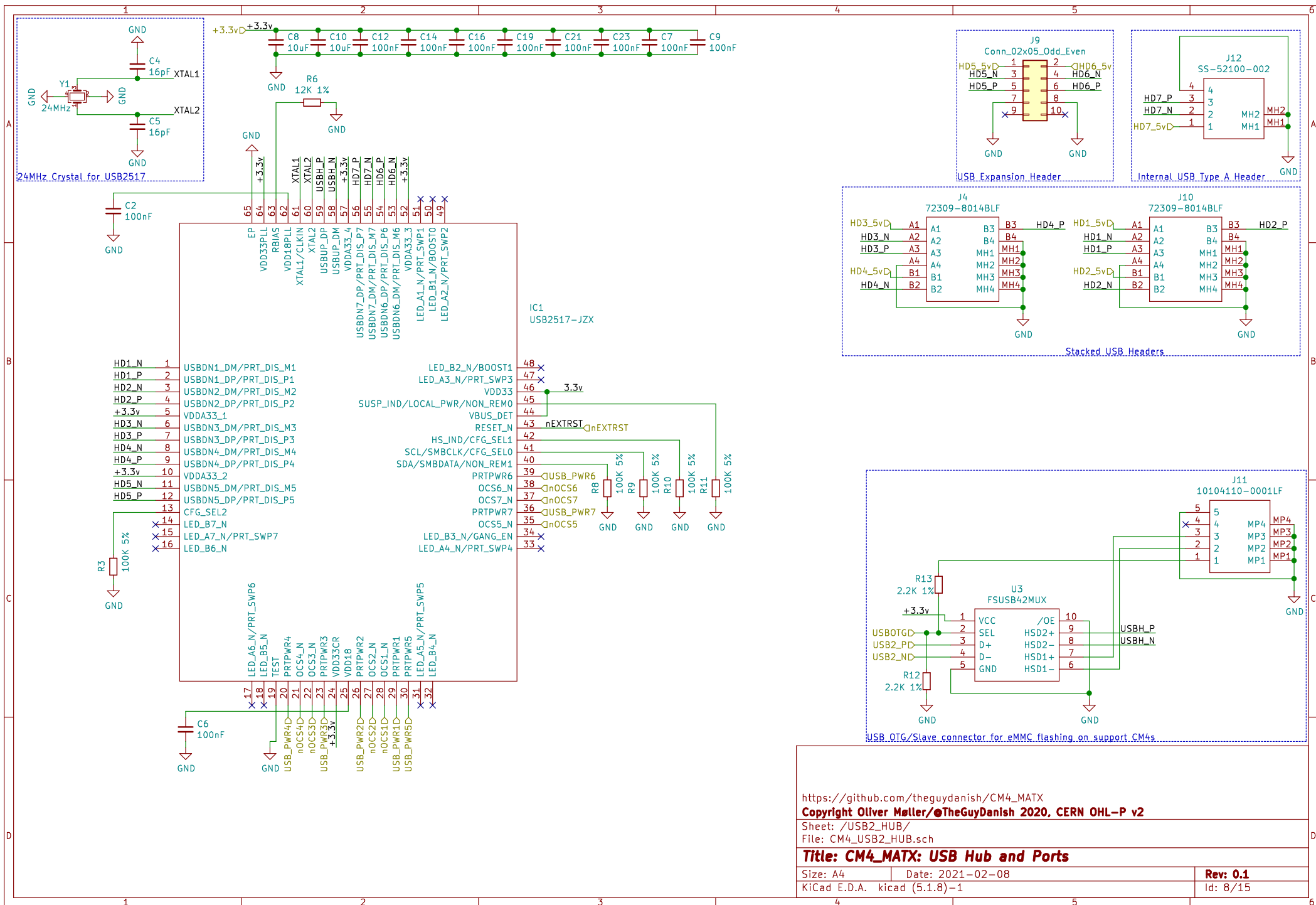
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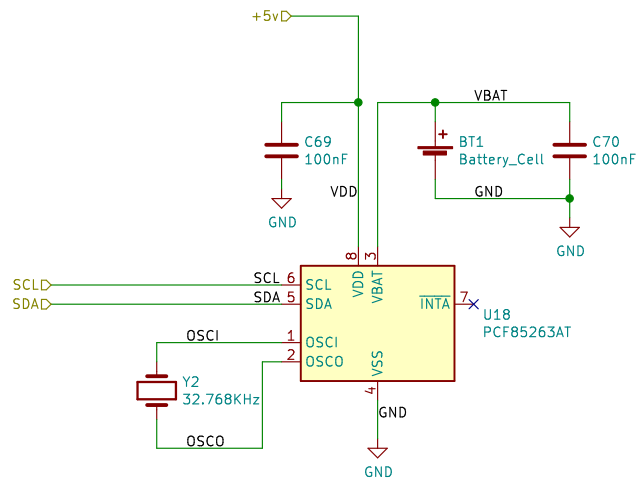
Size: A4 Date: 2021-02-08

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Rev: 0.1

Id: 7/15





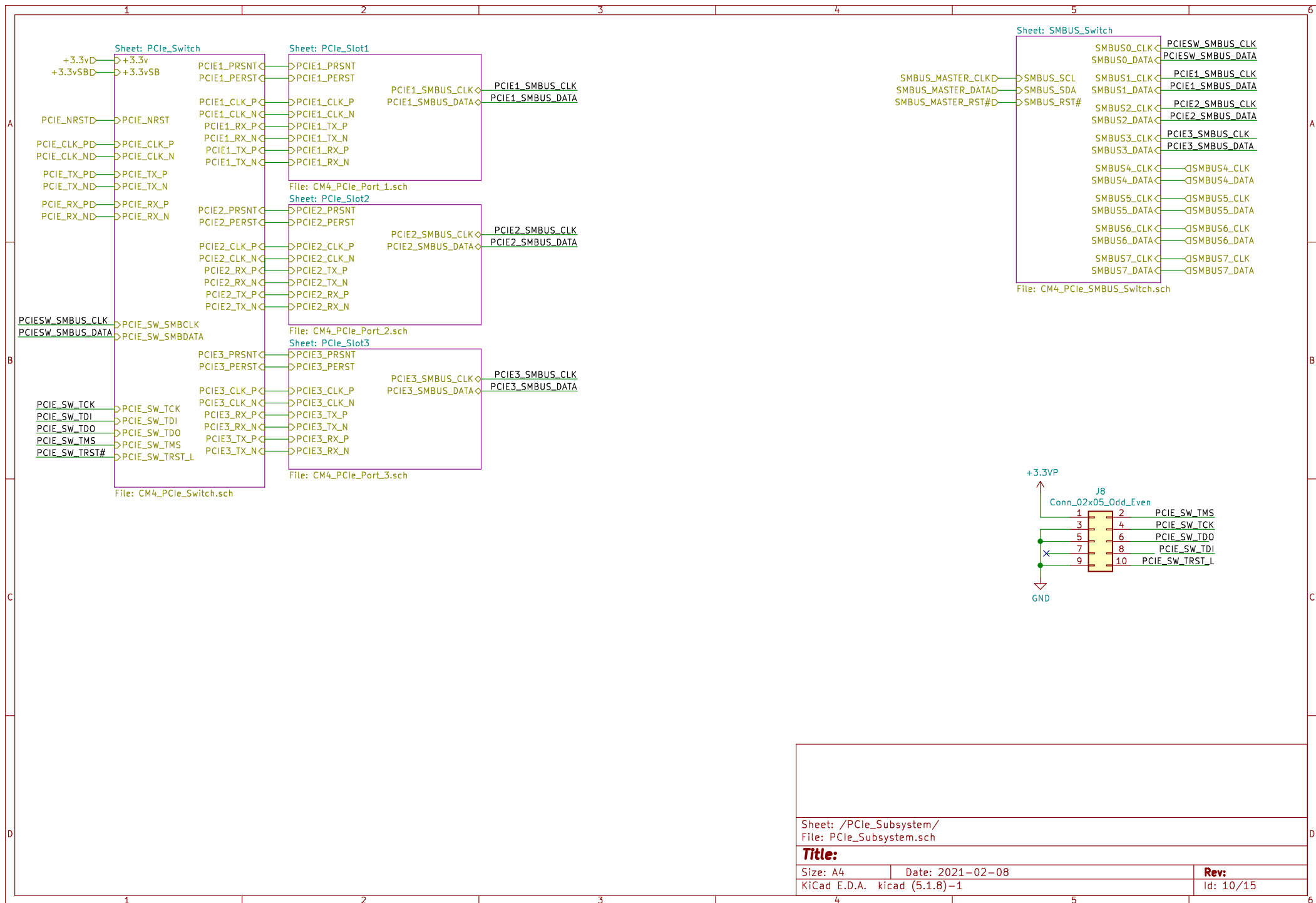
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File: CM4_RTC_PCF85263A.sch

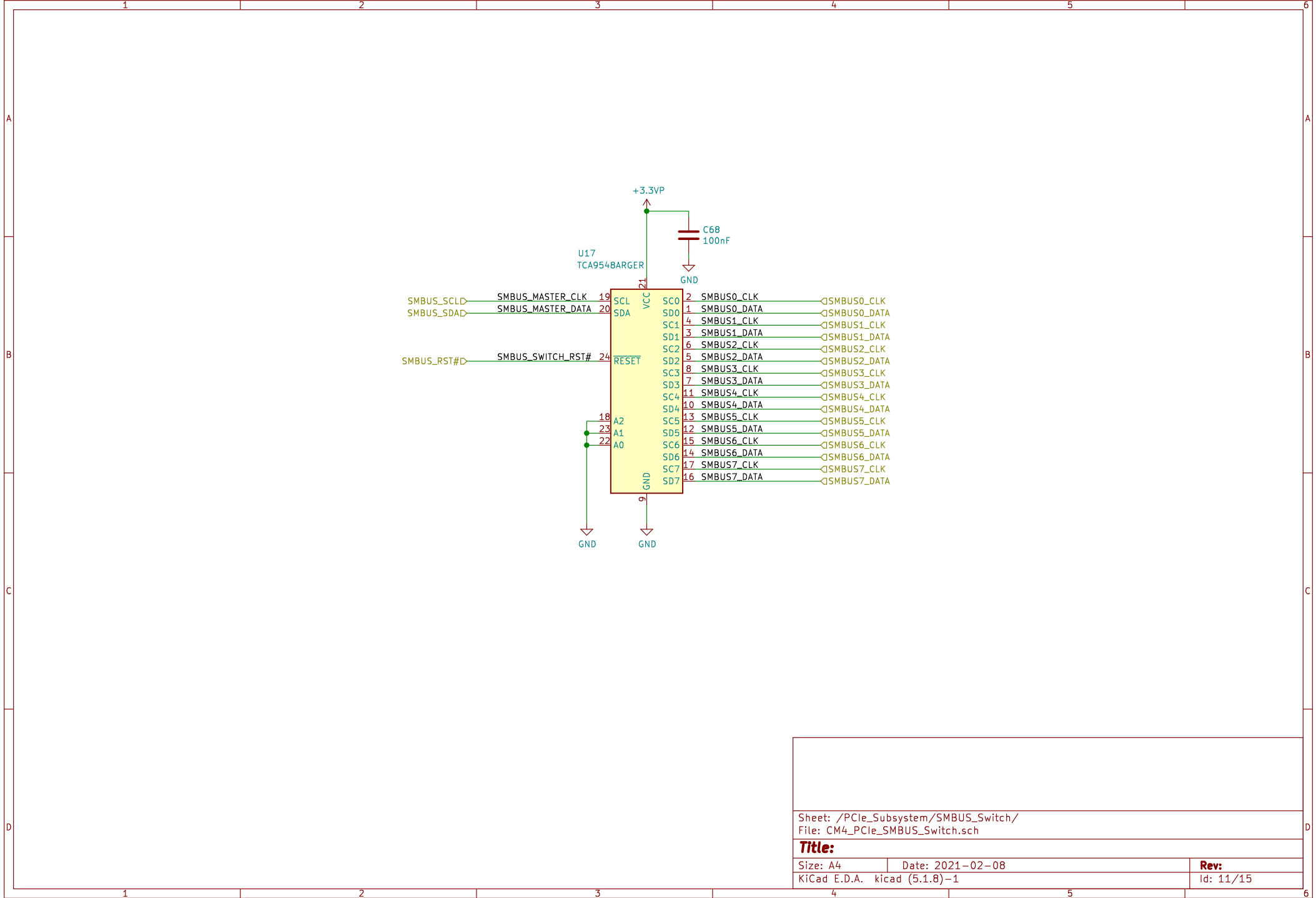
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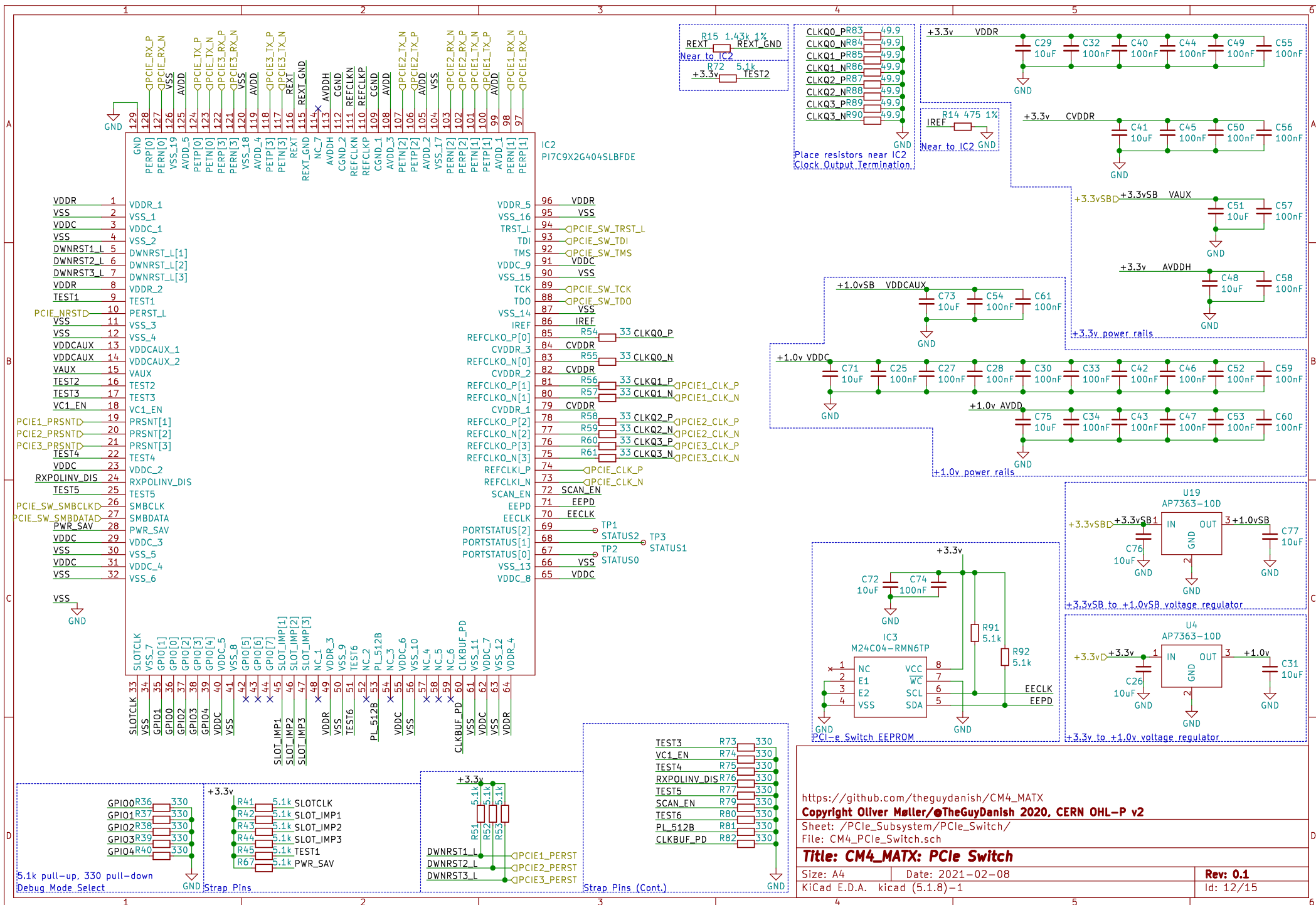
Size: A4 Date: 2021-02-08

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Rev:
Id: 9/15



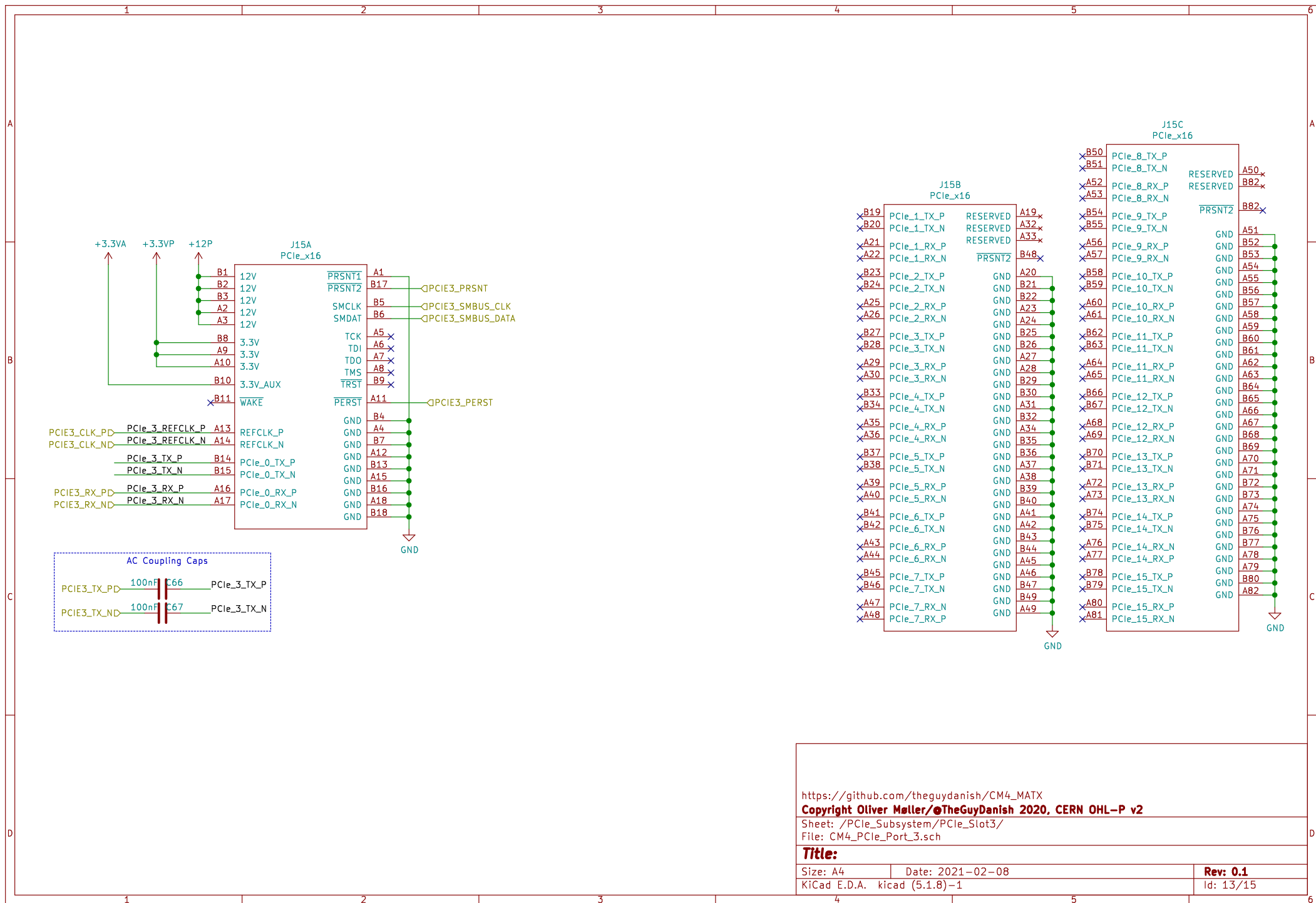




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Sheet: /PCIE_Subsystem/PCIE_Switch/
File: CM4_PCL_01.sch

Title: CM4_PCL_01: PCIE Switch

Size: A4	Date: 2021-02-08	Rev: 0.1
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Sheet: /PCIe_Subsystem/PCIe_Slot3/

File: CM4_PCIe_Port_3.sch

Title:

Size: A4

Date: 2021-02-08

Rev: 0.1

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Id: 13/15



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Sheet: /PCie_Subsystem/PCie_Slot1/

File: CM4_Pcie_Port_1.sch

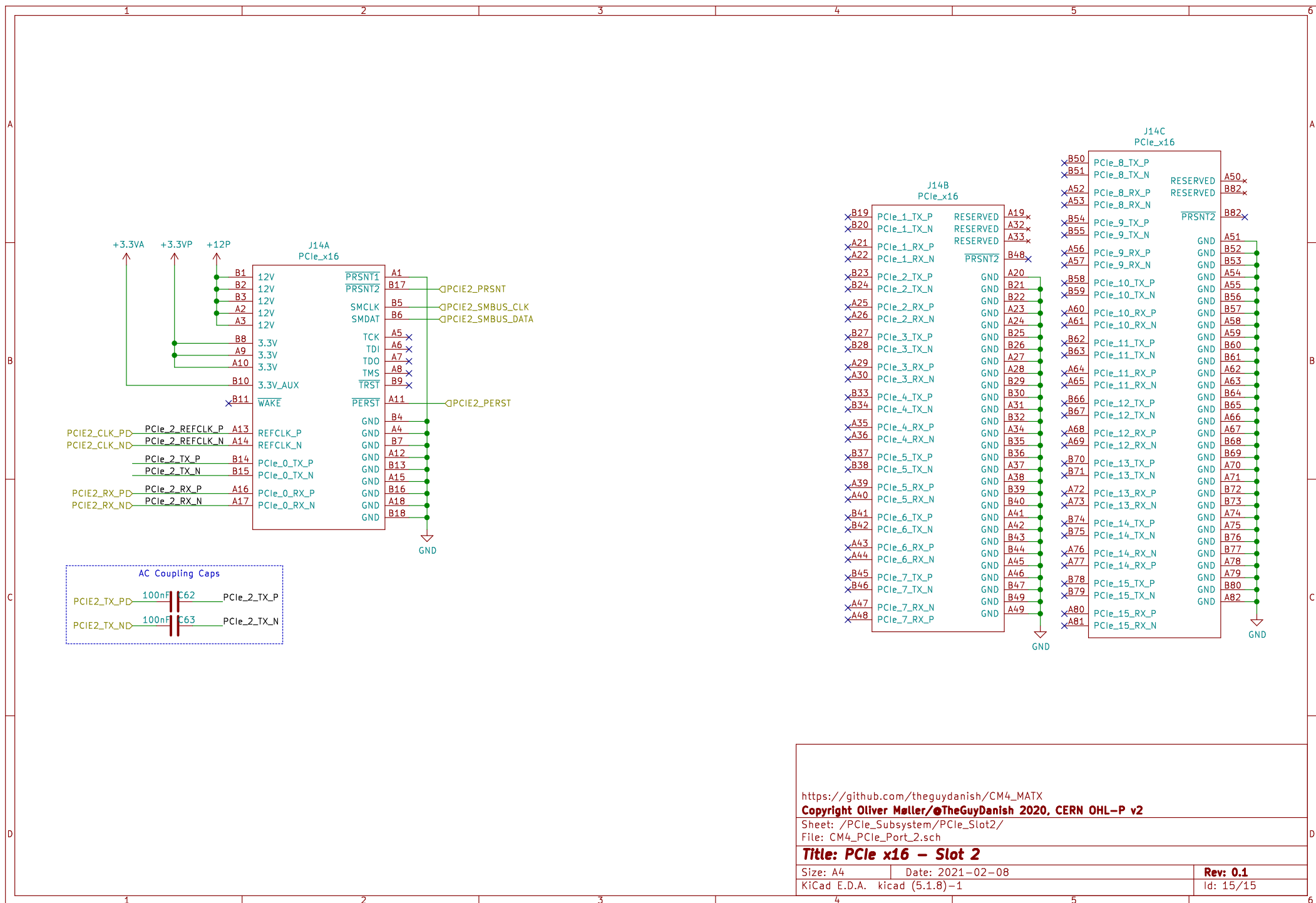
Title: PCie x16 – Slot 1

Size: A4 Date: 2021-02-08

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Rev: 0.1

Id: 14/15



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Sheet: /PCle_Subsystem/PCle_Slot2/

File: CM4_PClle_Port_2.sch

Title: PCIe x16 – Slot 2

Size: A4 Date: 2021-02-08

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Rev: 0.1

Id: 15/15