

Extra unclaimed SMBUS/I2C busses.
Will be routed to header if not used.

- H1 MountingHole
- H2 MountingHole
- H3 MountingHole
- H4 MountingHole
- H6 MountingHole
- H7 MountingHole
- H5 MountingHole
- H8 MountingHole
- H9 MountingHole

https://github.com/theguydanish/CM4_MATX

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Sheet: /
File: CM4_MATX.sch

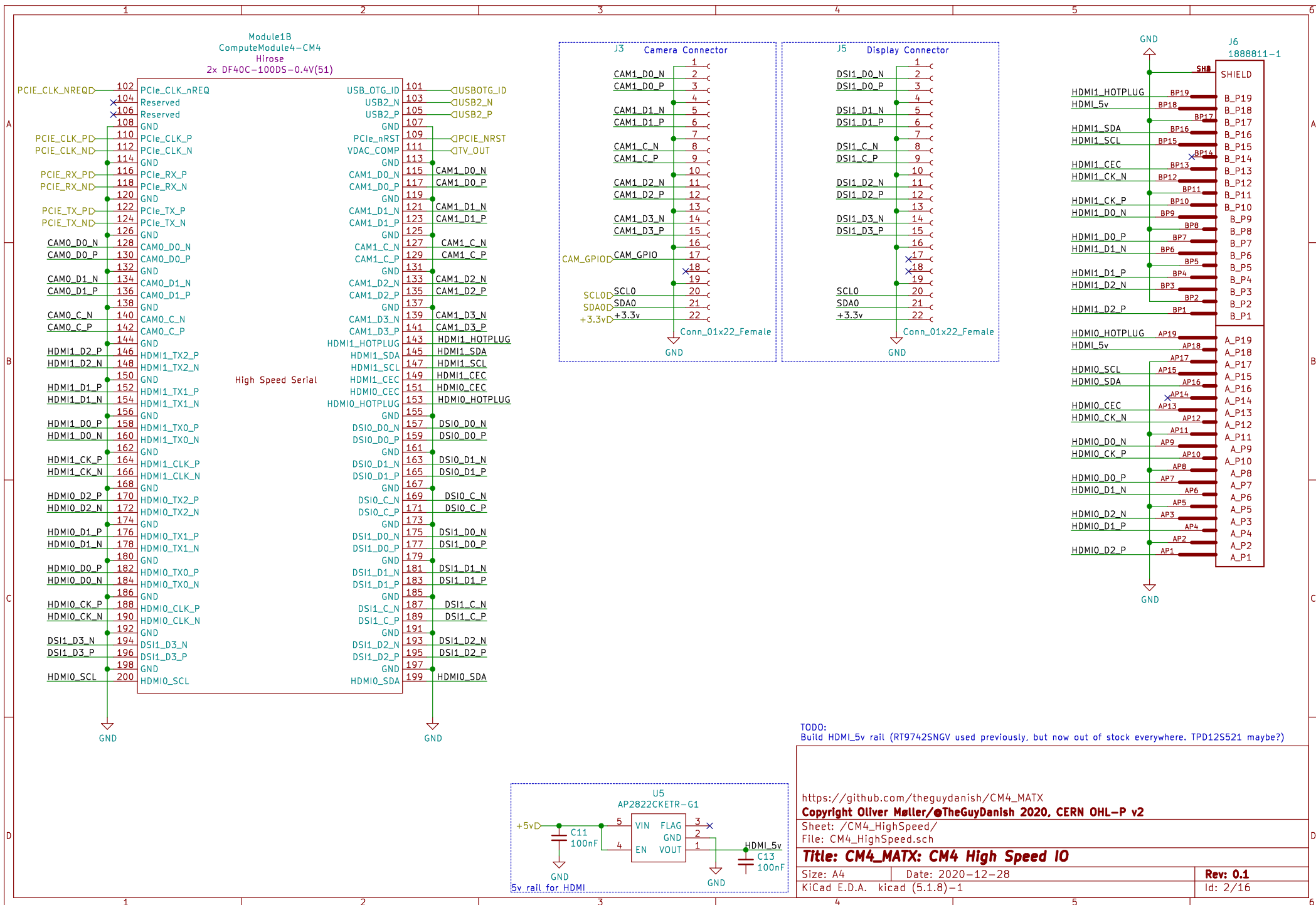
Title: CM4 MATX Root Sheet

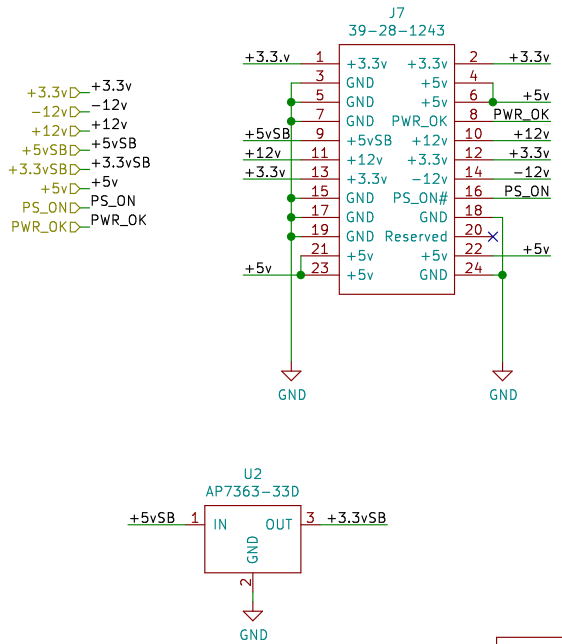
Size: A4 Date: 2020-12-28

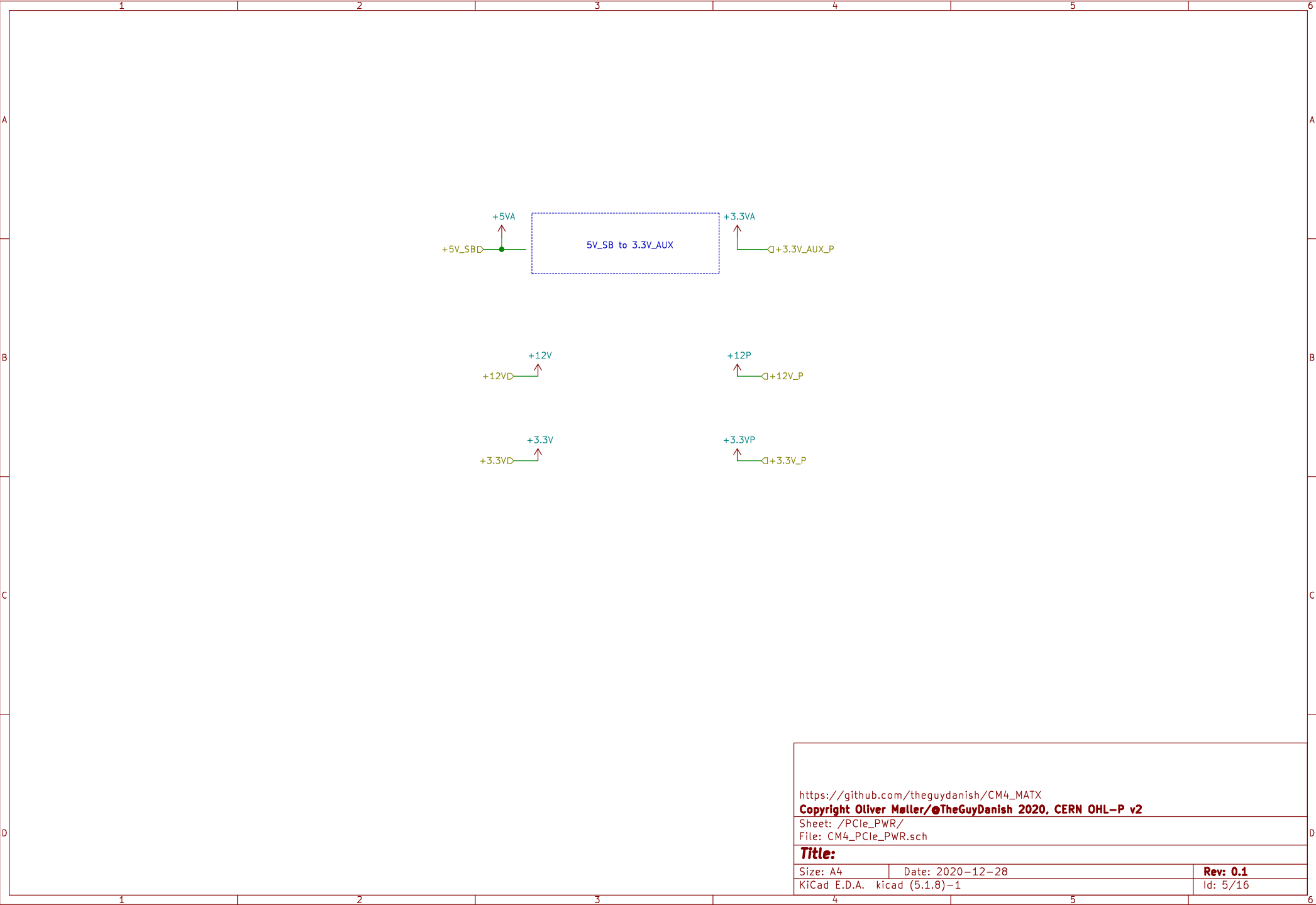
KiCad E.D.A. kicad (5.1.8)-1

Rev: 0.1

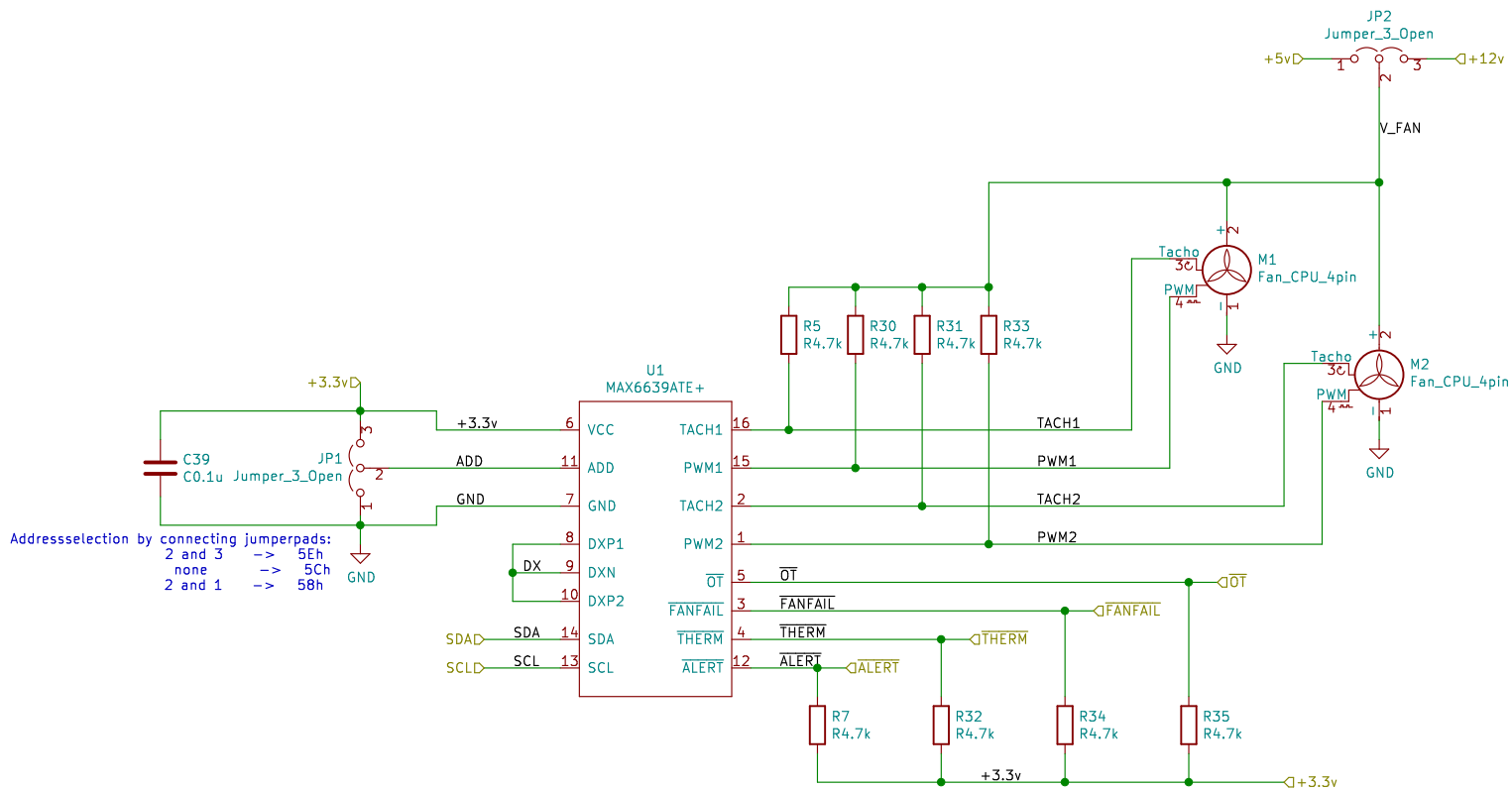
Id: 1/16







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Sheet: /PCIe_PWR/ File: CM4_PCIe_PWR.sch		
Title:		
Size: A4	Date: 2020–12–28	Rev: 0.1
KiCad E.D.A. kicad (5.1.8)–1		Id: 5/16



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Sheet: /FanCTRL_MAX6639/

File: CM4_FanCTRL_MAX6639.sch

Title:

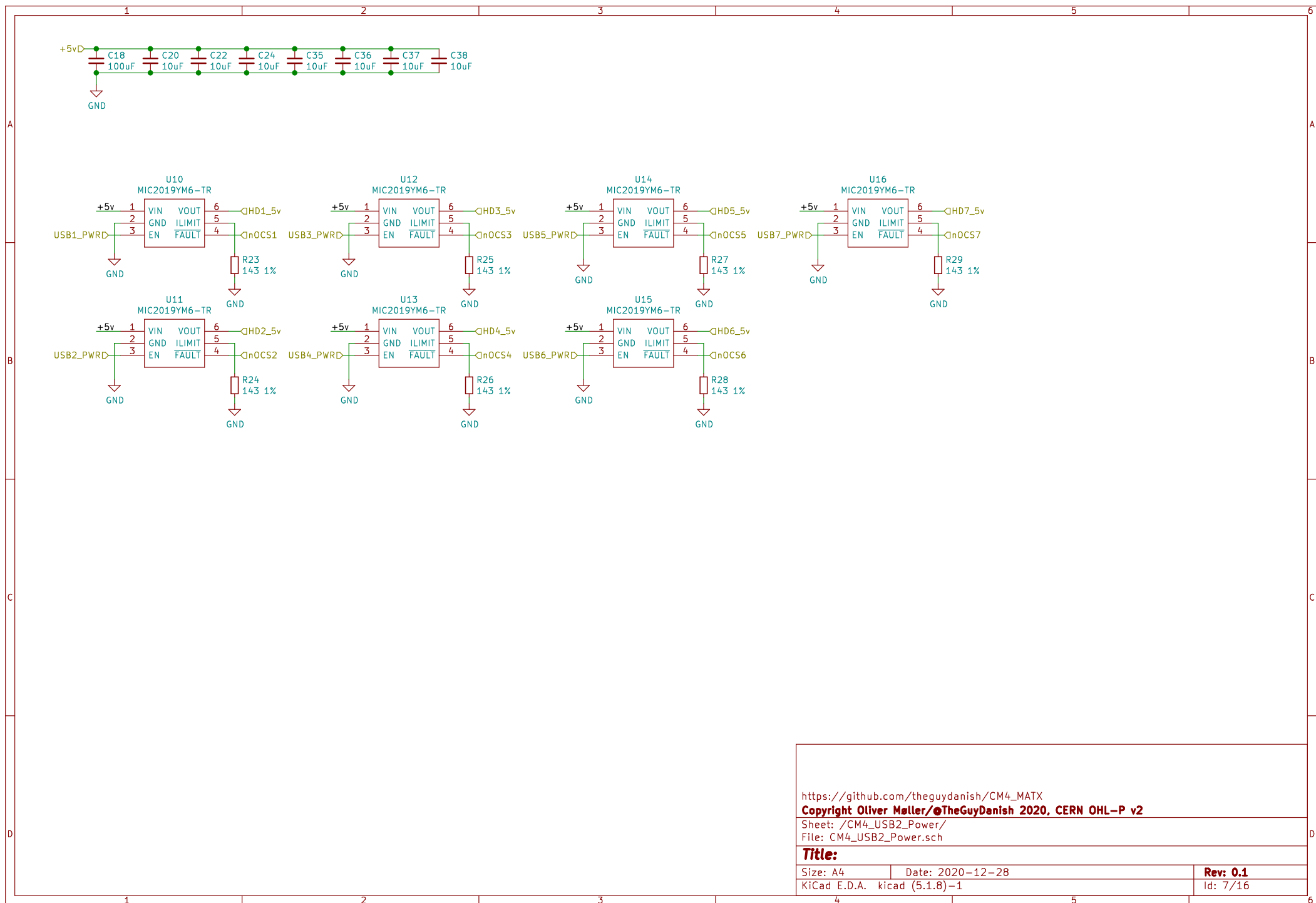
Size: A4

Date: 2020-12-28

Rev: 0.1

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Id: 6/16



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Sheet: /CM4_USB2_Power/

File: CM4_USB2_Power.sch

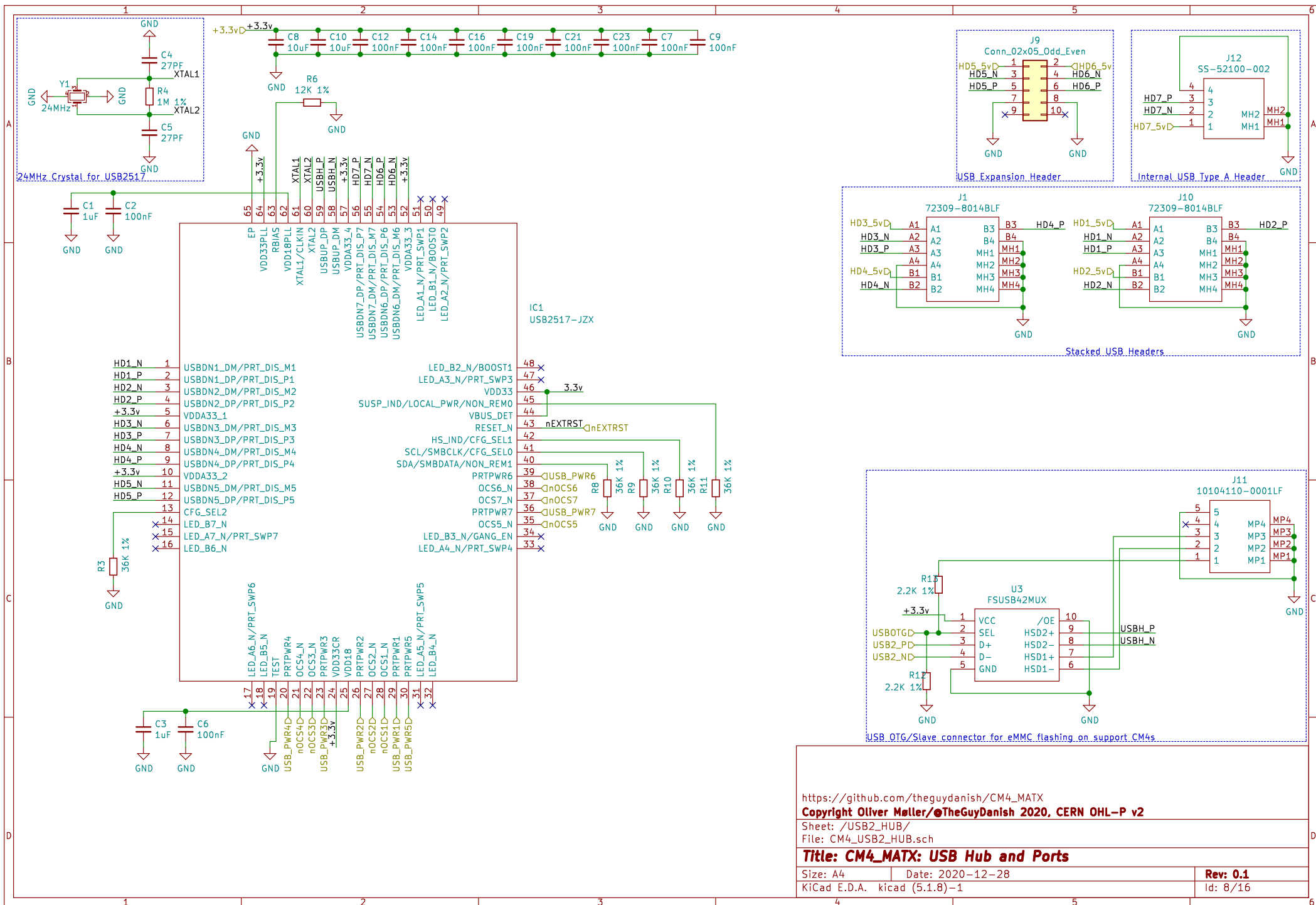
Title:

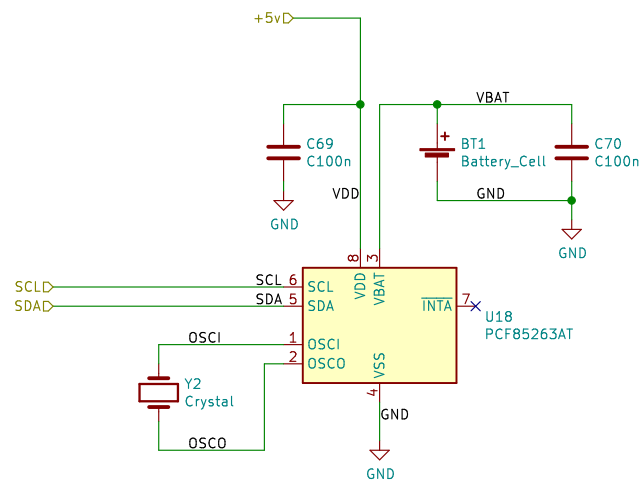
Size: A4 Date: 2020-12-28

KiCad E.D.A. kicad (5.1.8)-1

Rev: 0.1

Id: 7/16





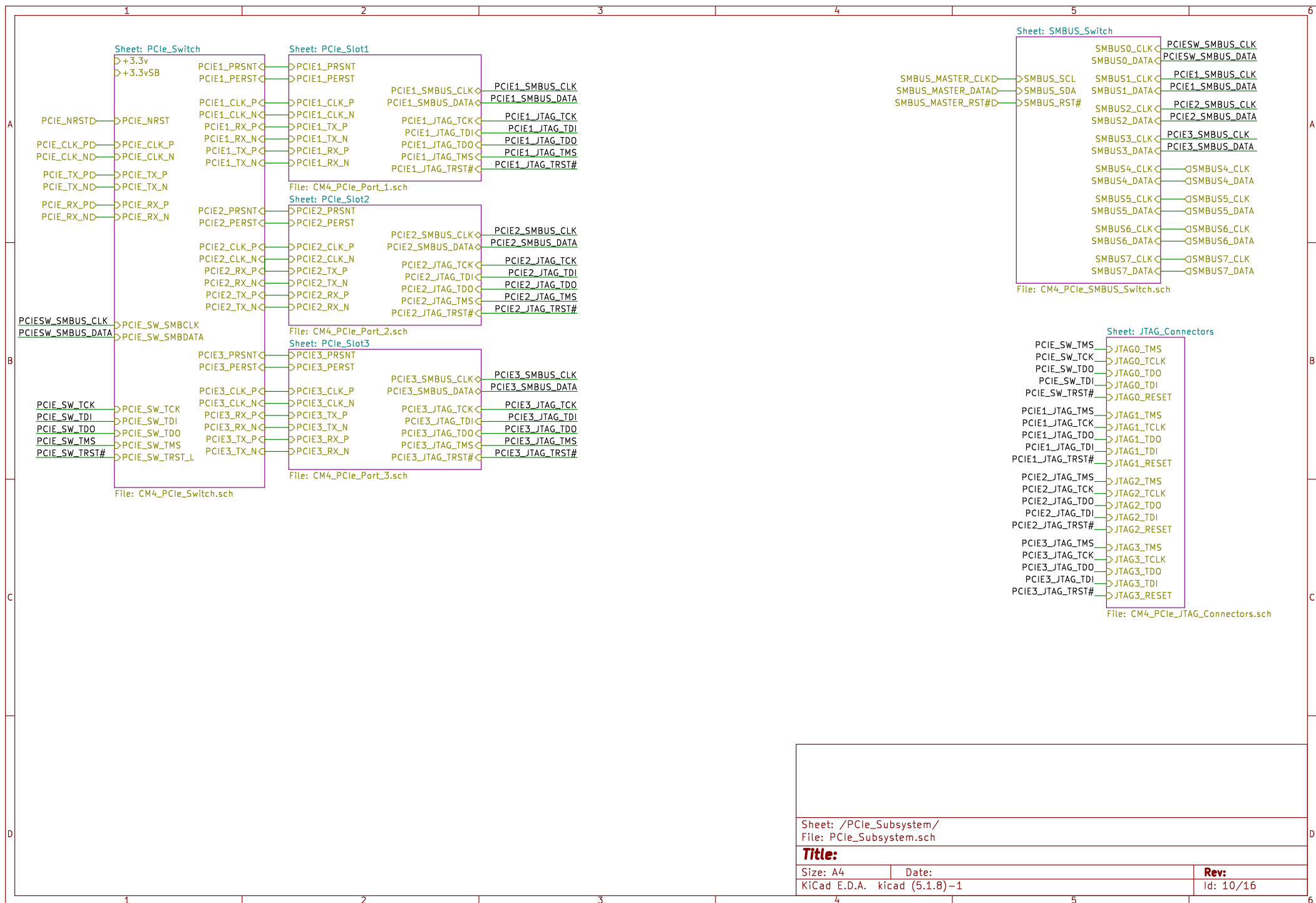
Sheet: /RTC_PCF85263A/
File: CM4_RTC_PCF85263A.sch

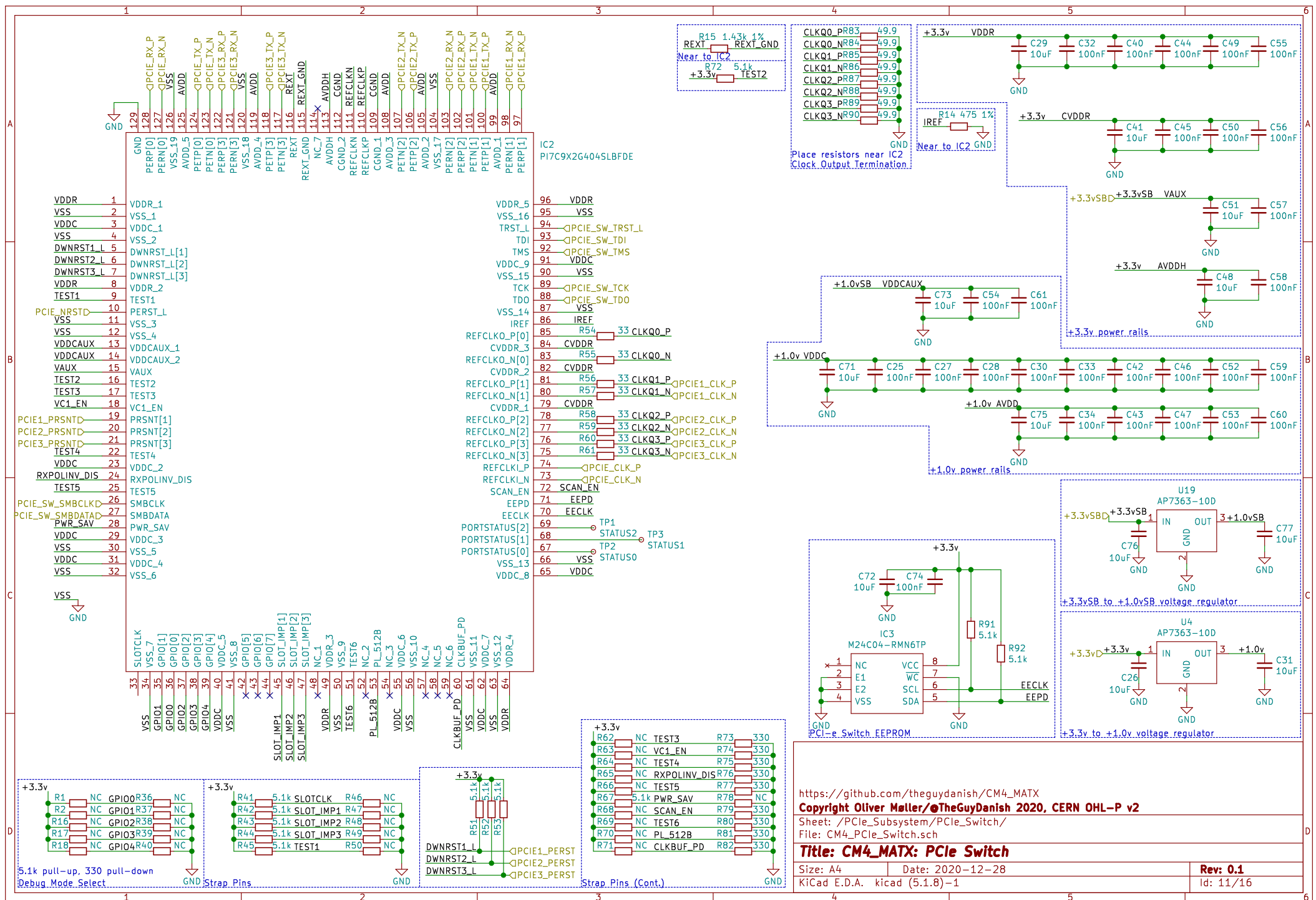
Title:

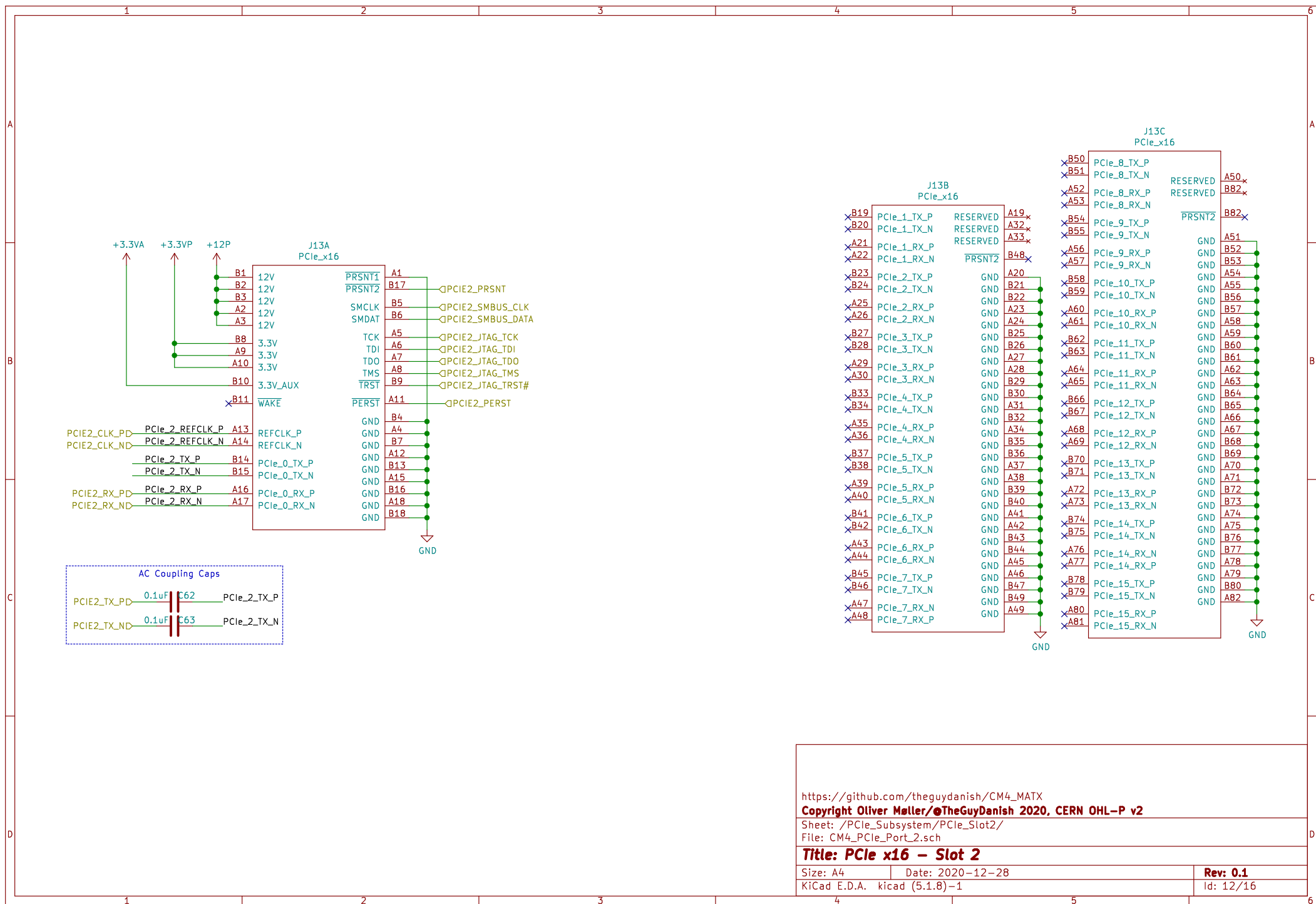
Size: A4
KiCad E.D.A. kicad (5.1.8)-1

Date:

Rev:
Id: 9/16







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Sheet: /PCie_Subsystem/PCie_Slot2/

File: CM4_Pcie_Port_2.sch

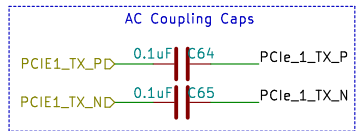
Title: PCie x16 – Slot 2

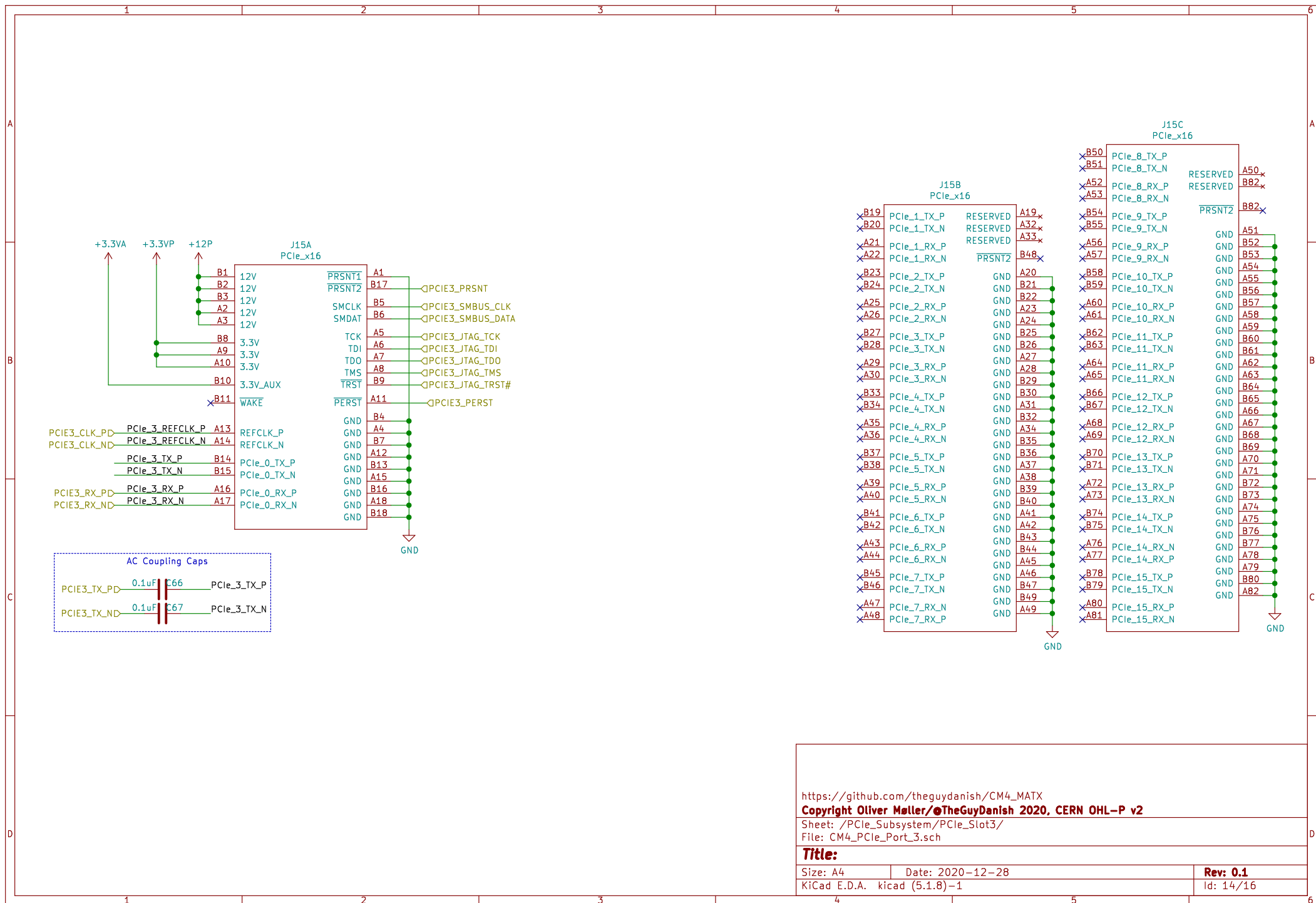
Size: A4 Date: 2020-12-28

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Rev: 0.1

Id: 12/16





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Sheet: /PCle_Subsystem/PCle_Slot3/

File: CM4_PCLE_Port_3.sch

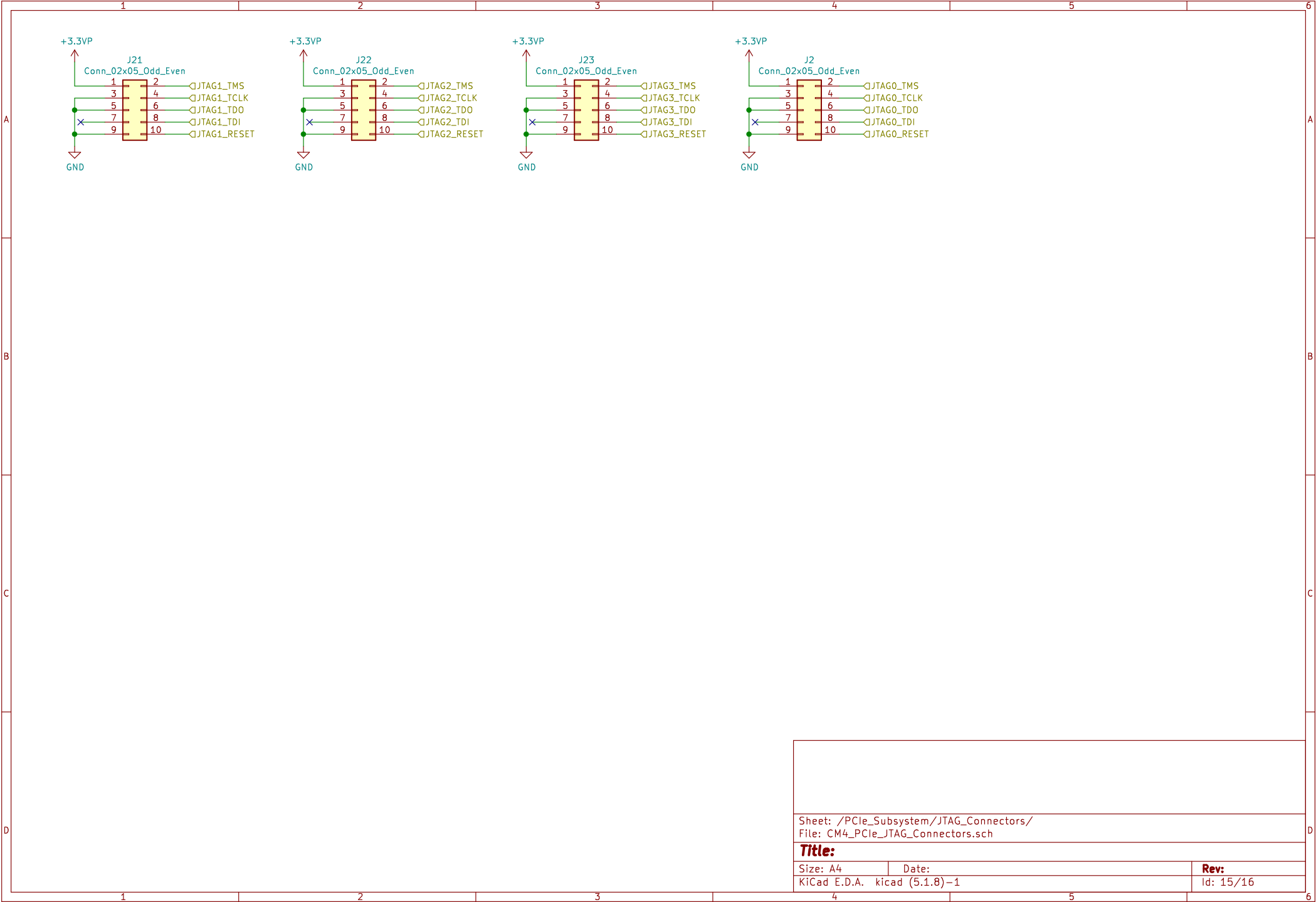
Title:

Size: A4 Date: 2020-12-28

KiCad E.D.A. kicad (5.1.8)-1

Rev: 0.1

Id: 14/16



Sheet: /PCIe_Subsystem/JTAG_Connectors/
File: CM4_PCIe_JTAG_Connectors.sch

Title:

Size: A4

Date:

KiCad E.D.A. kicad (5.1.8)-1

Rev:

Id: 15/16

