

https://github.com/theguydanish/CM4_MATX

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Sheet: /
File: CM4_MATX.sch

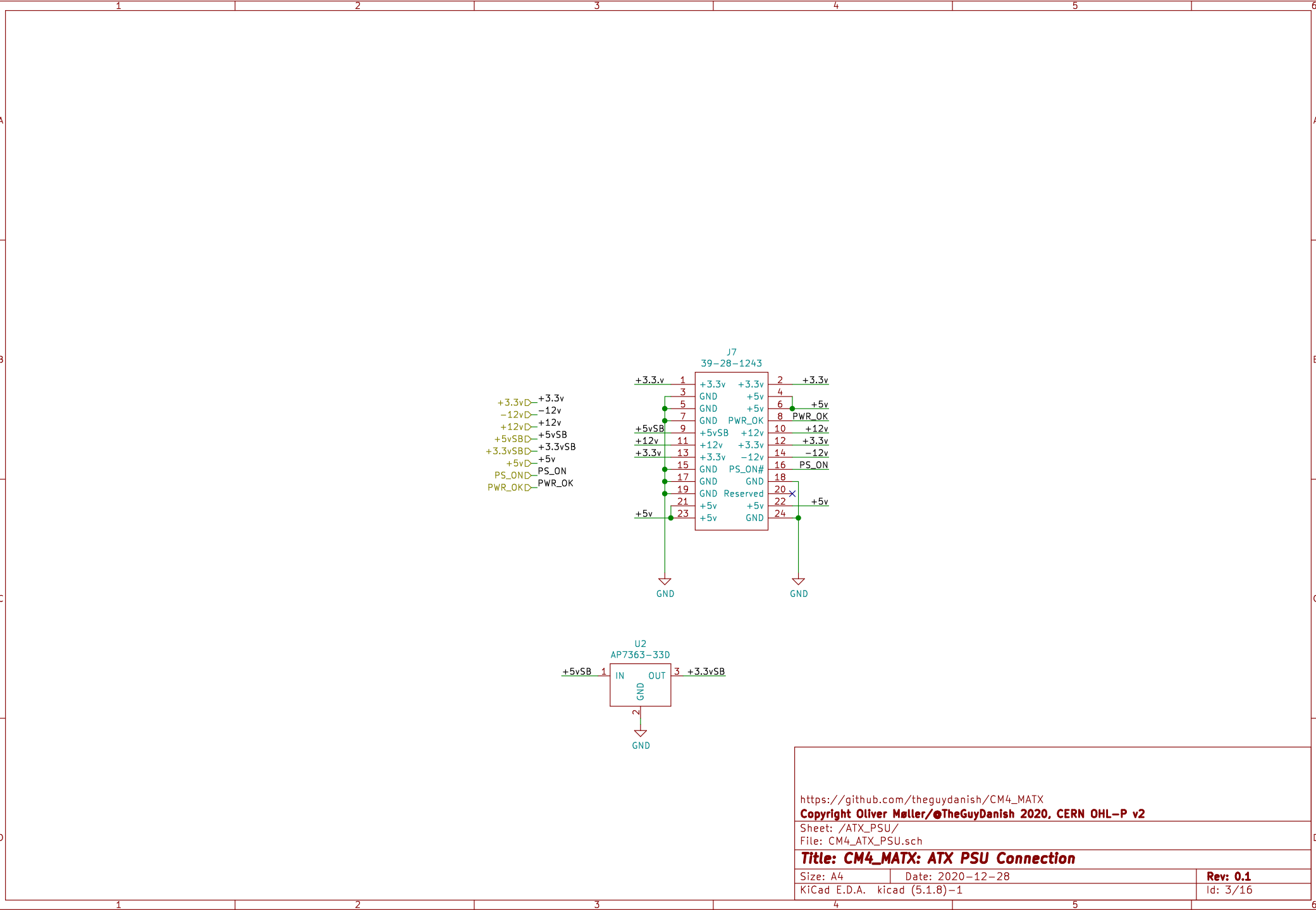
Title: CM4 MATX Root Sheet

Size: A4 Date: 2020-12-28

KiCad E.D.A. kicad (5.1.8)-1

Rev: 0.1

Id: 1/16



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Sheet: /ATX_PSU/

File: CM4_ATX_PSU.sch

Title: CM4_MATX: ATX PSU Connection

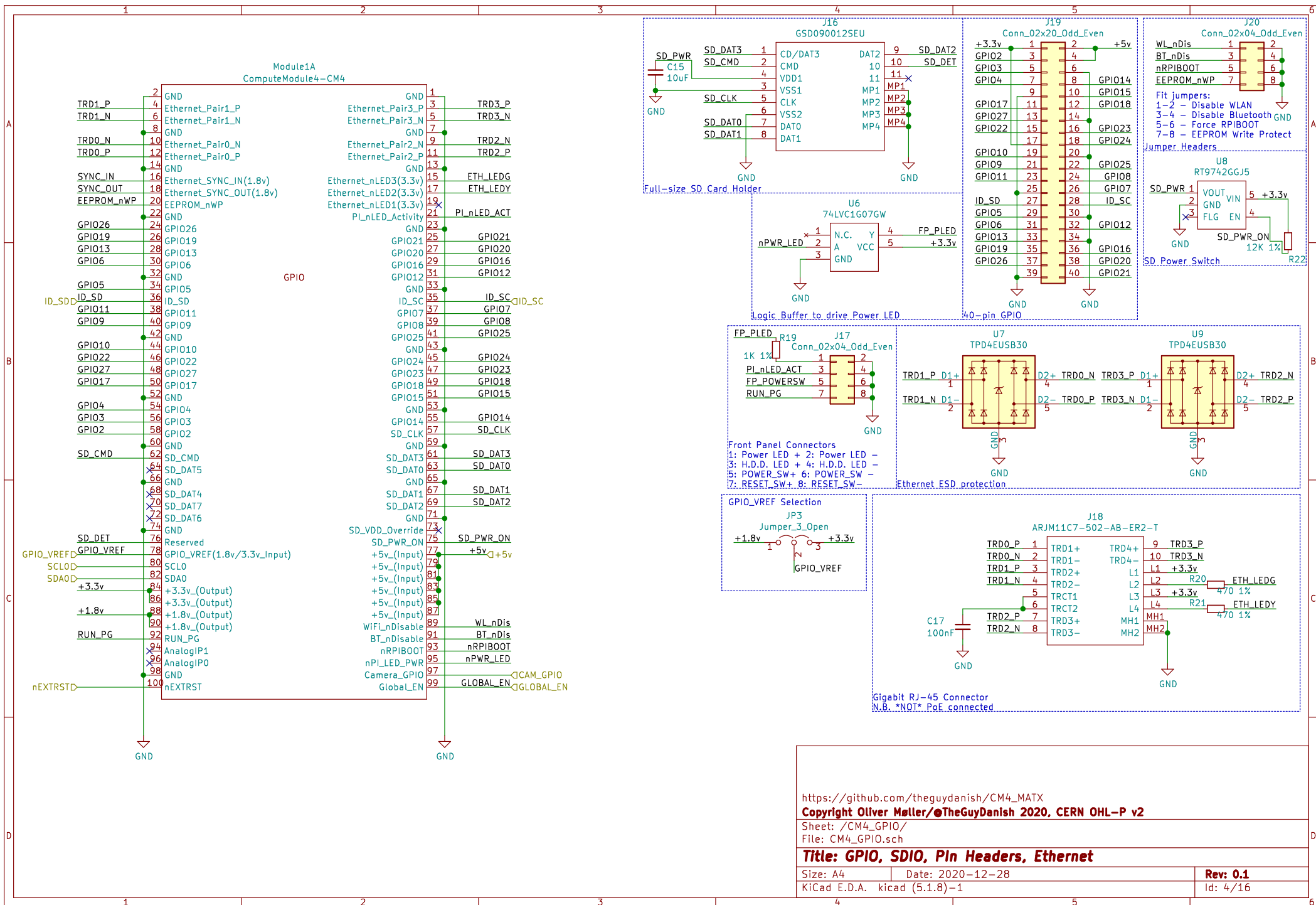
Size: A4

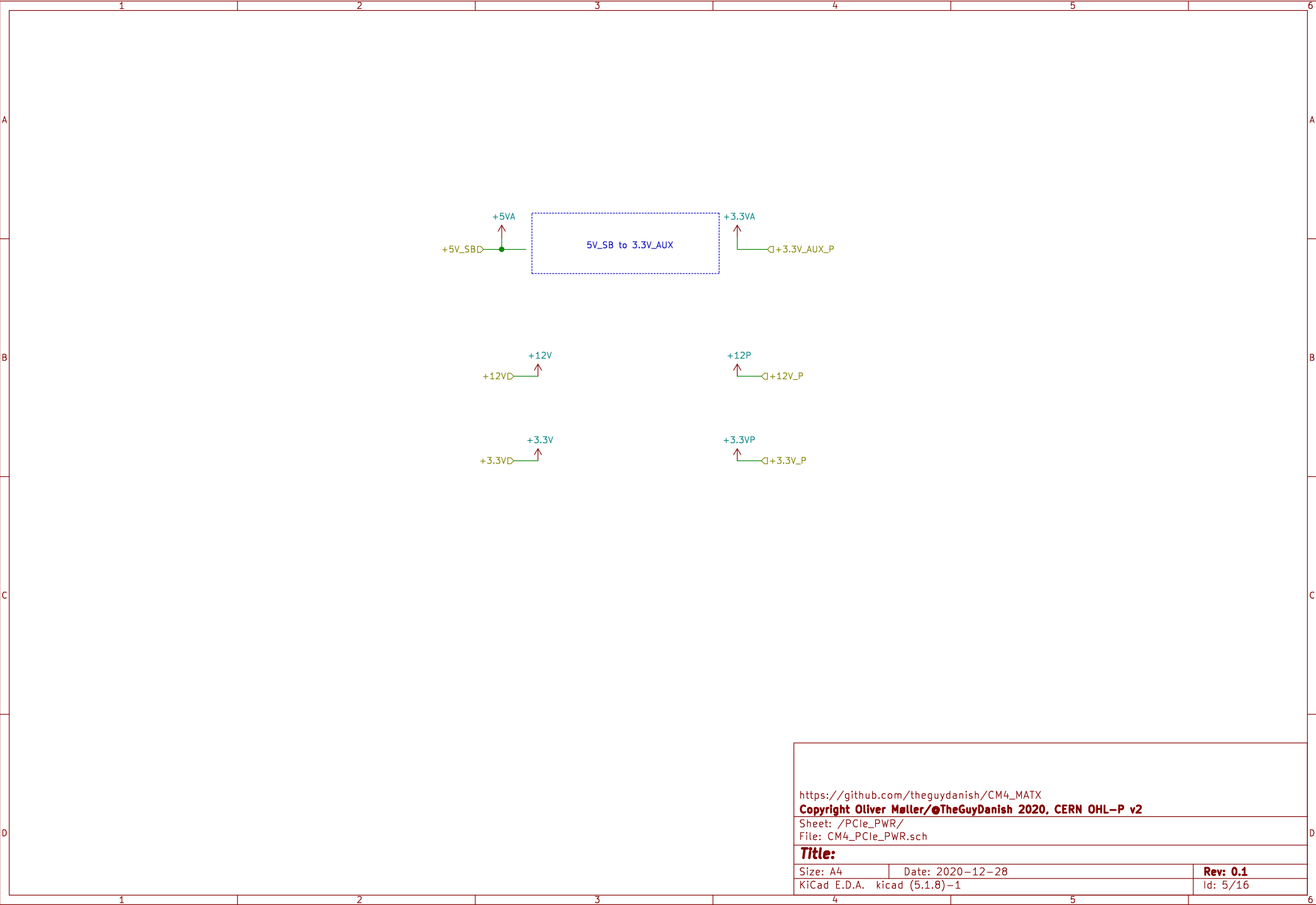
Date: 2020-12-28

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Sheet: /PCIe_PWR/
File: CM4_PCIe_PWR.sch

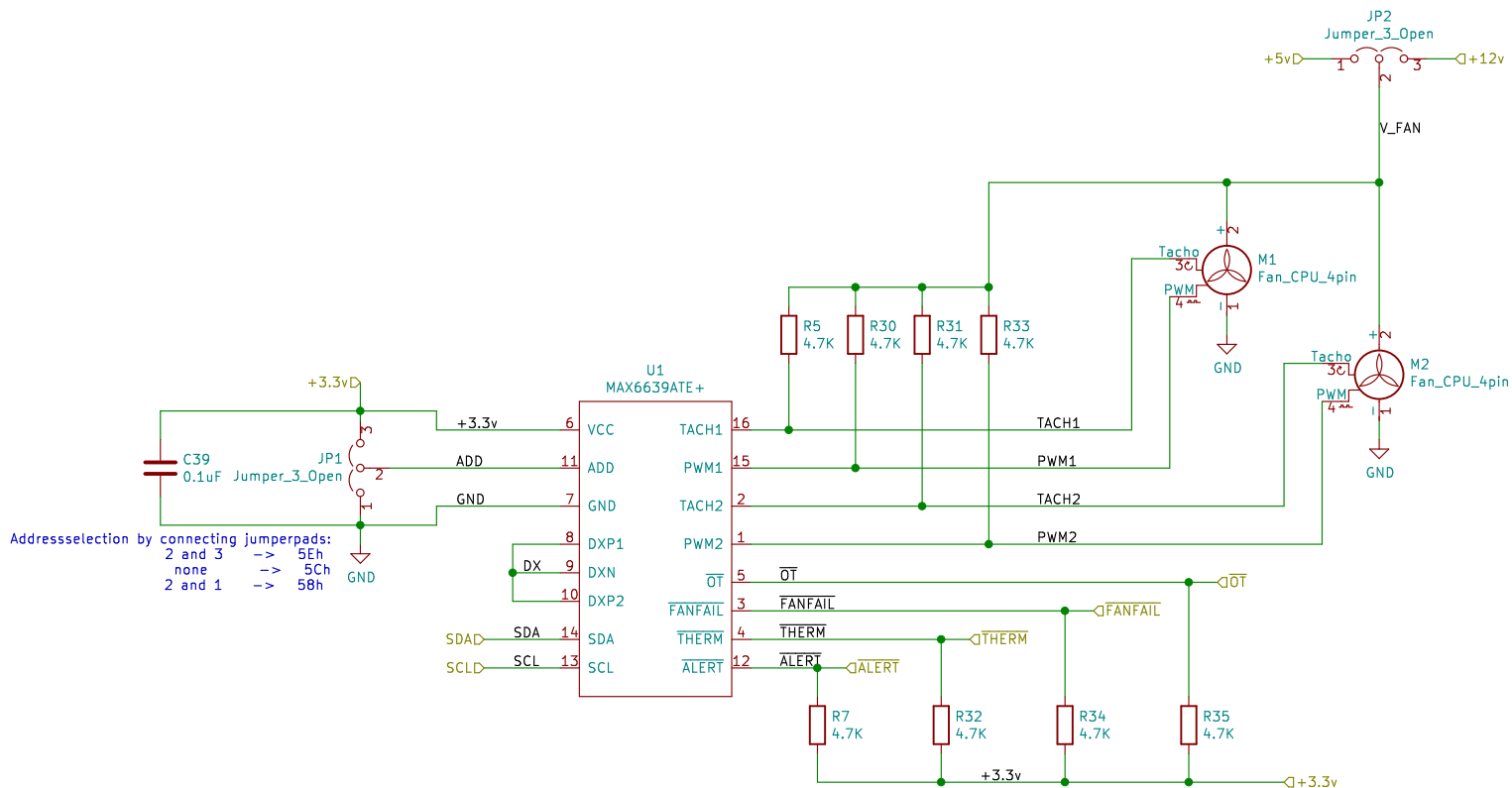
Title:

Size: A4 Date: 2020-12-28

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Sheet: /FanCTRL_MAX6639/

File: CM4_FanCTRL_MAX6639.sch

Title:

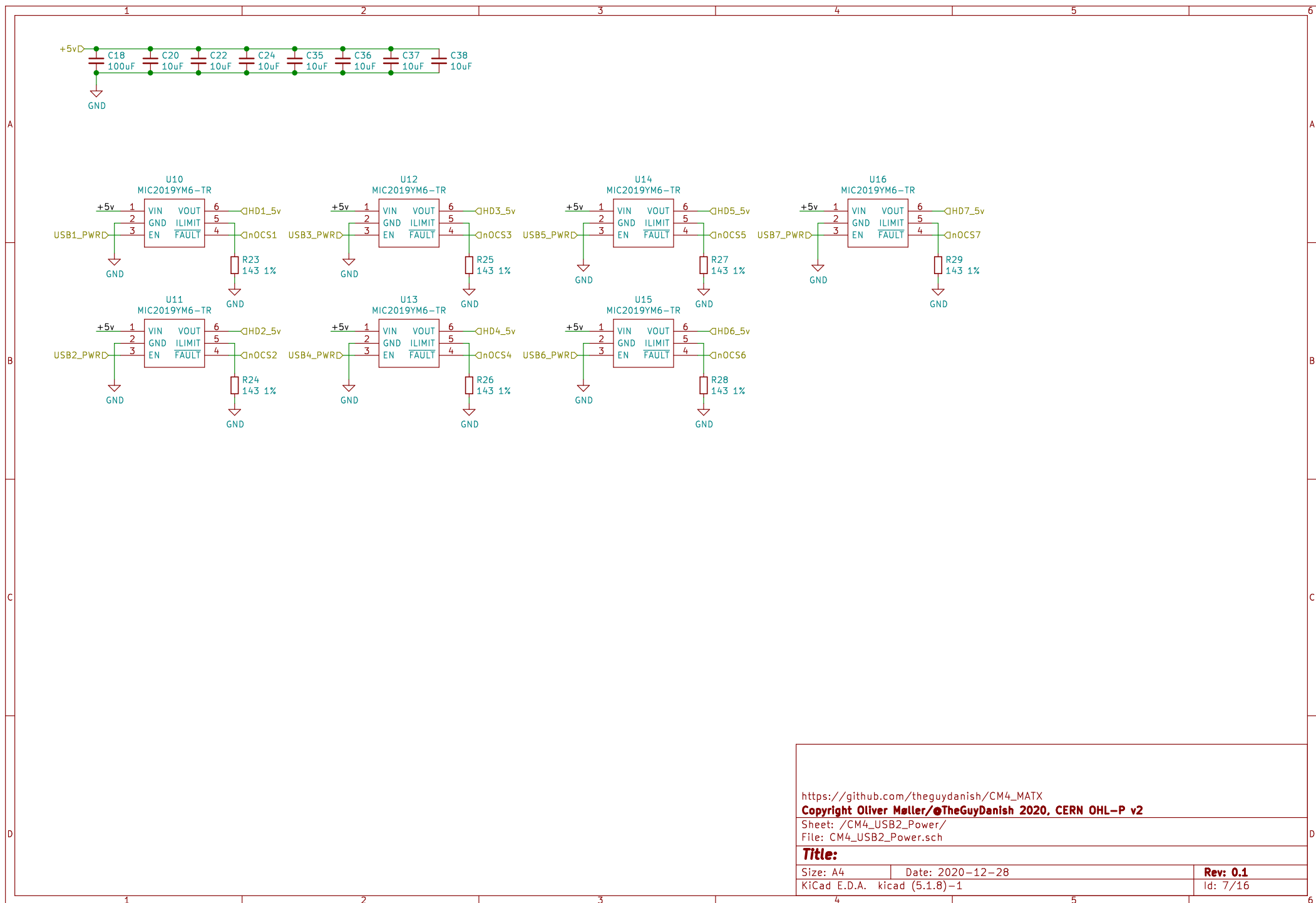
Size: A4

Date: 2020-12-28

Rev: 0.1

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Sheet: /CM4_USB2_Power/

File: CM4_USB2_Power.sch

Title:

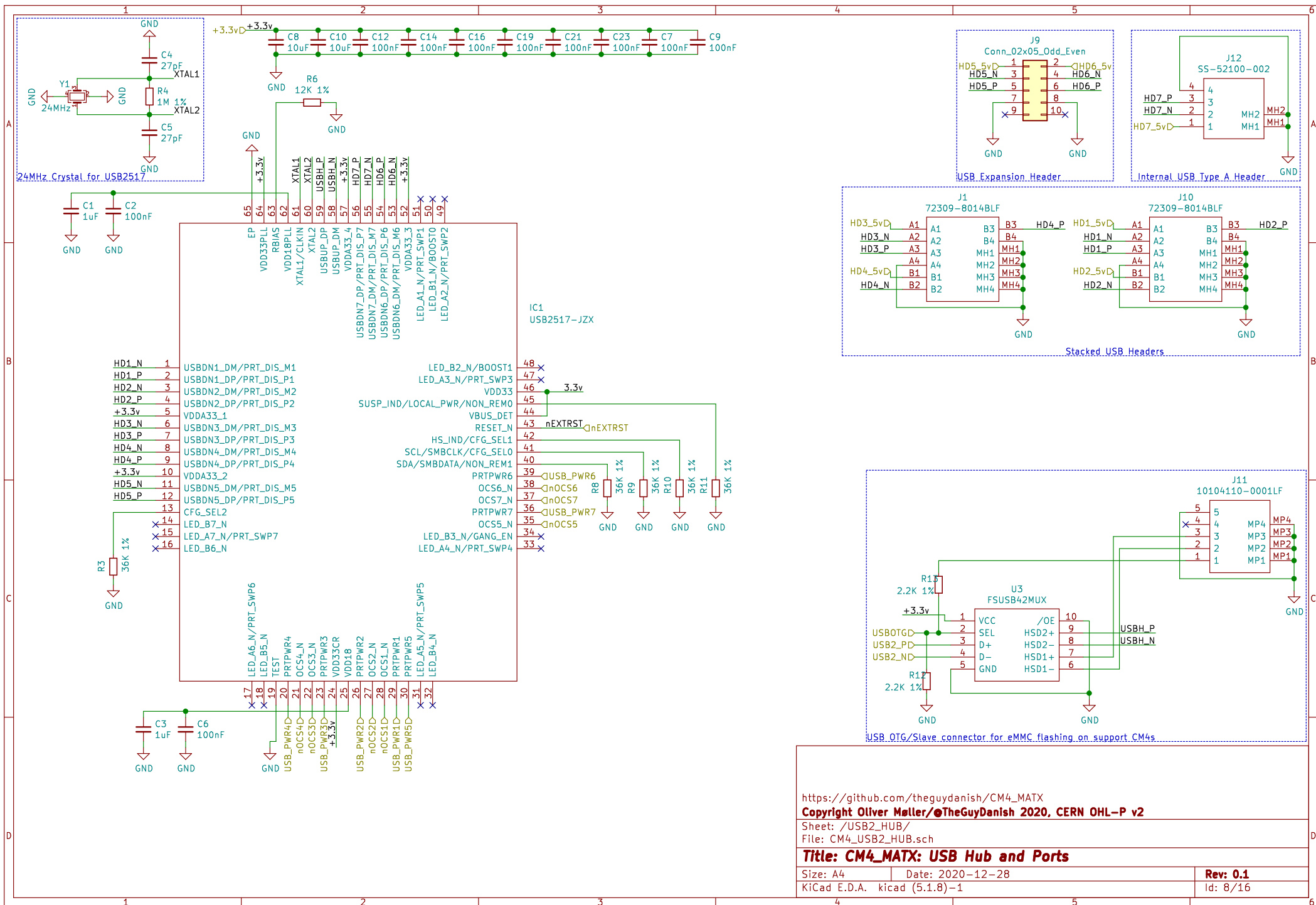
Size: A4

Date: 2020-12-28

Rev: 0.1

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Sheet: /USB2_HUB/

File: CM4_USB2_HUB.sch

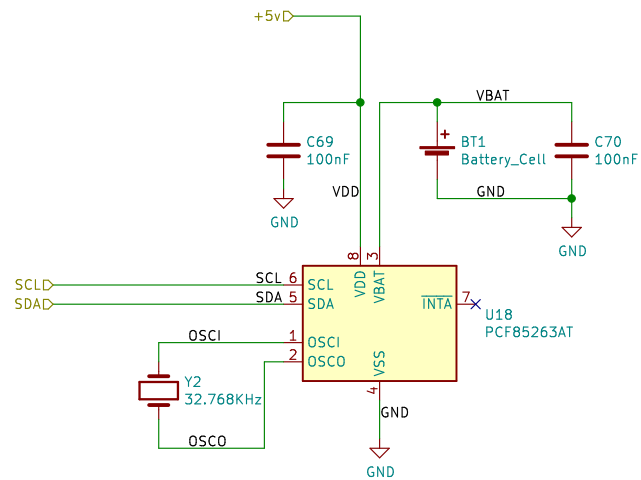
Title: CM4_MATX: USB Hub and Ports

Size: A4 Date: 2020-12-28

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Rev: 0.1

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Sheet: /RTC_PCF85263A/
File: CM4_RTC_PCF85263A.sch

Title:

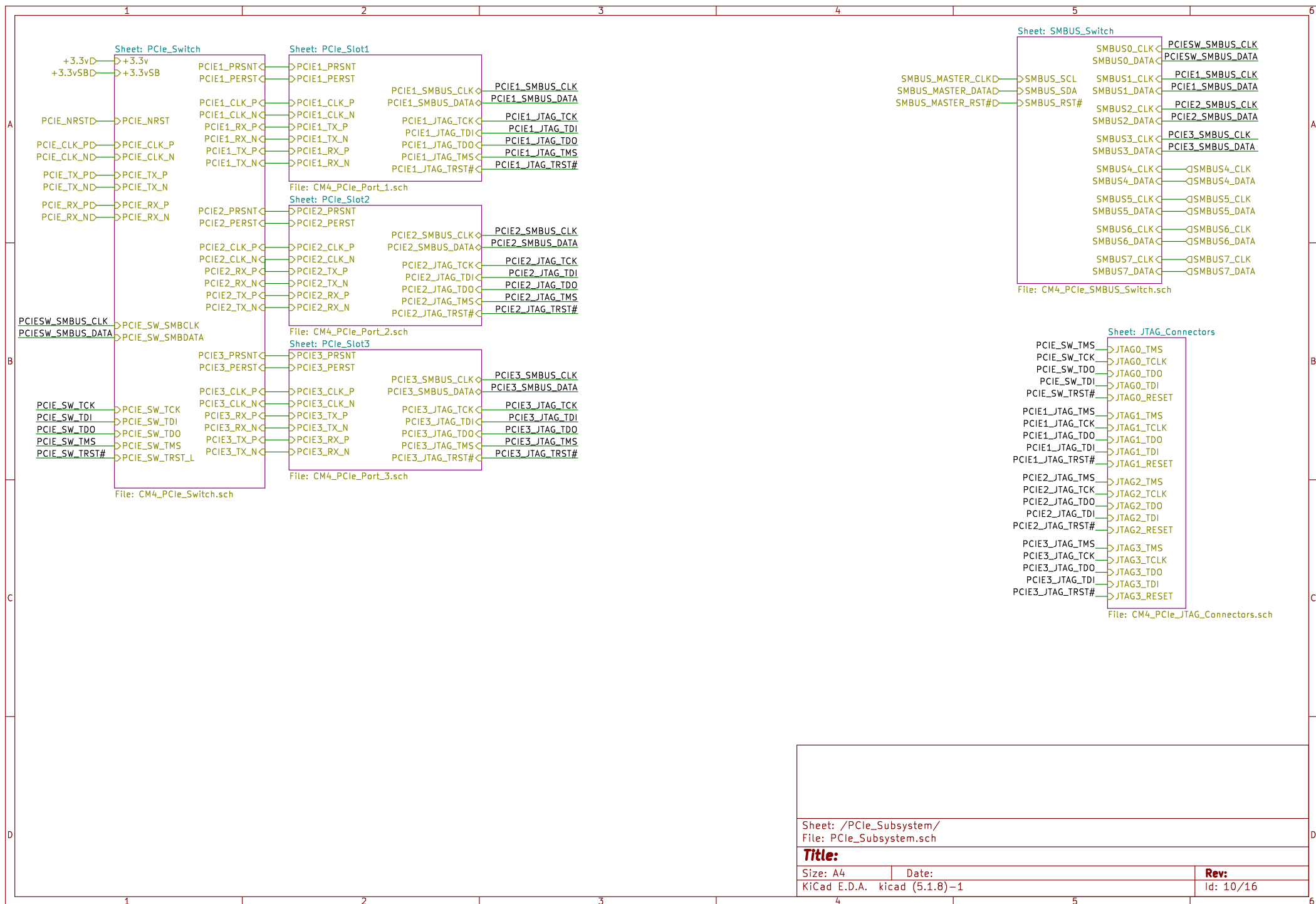
Size: A4

Date:

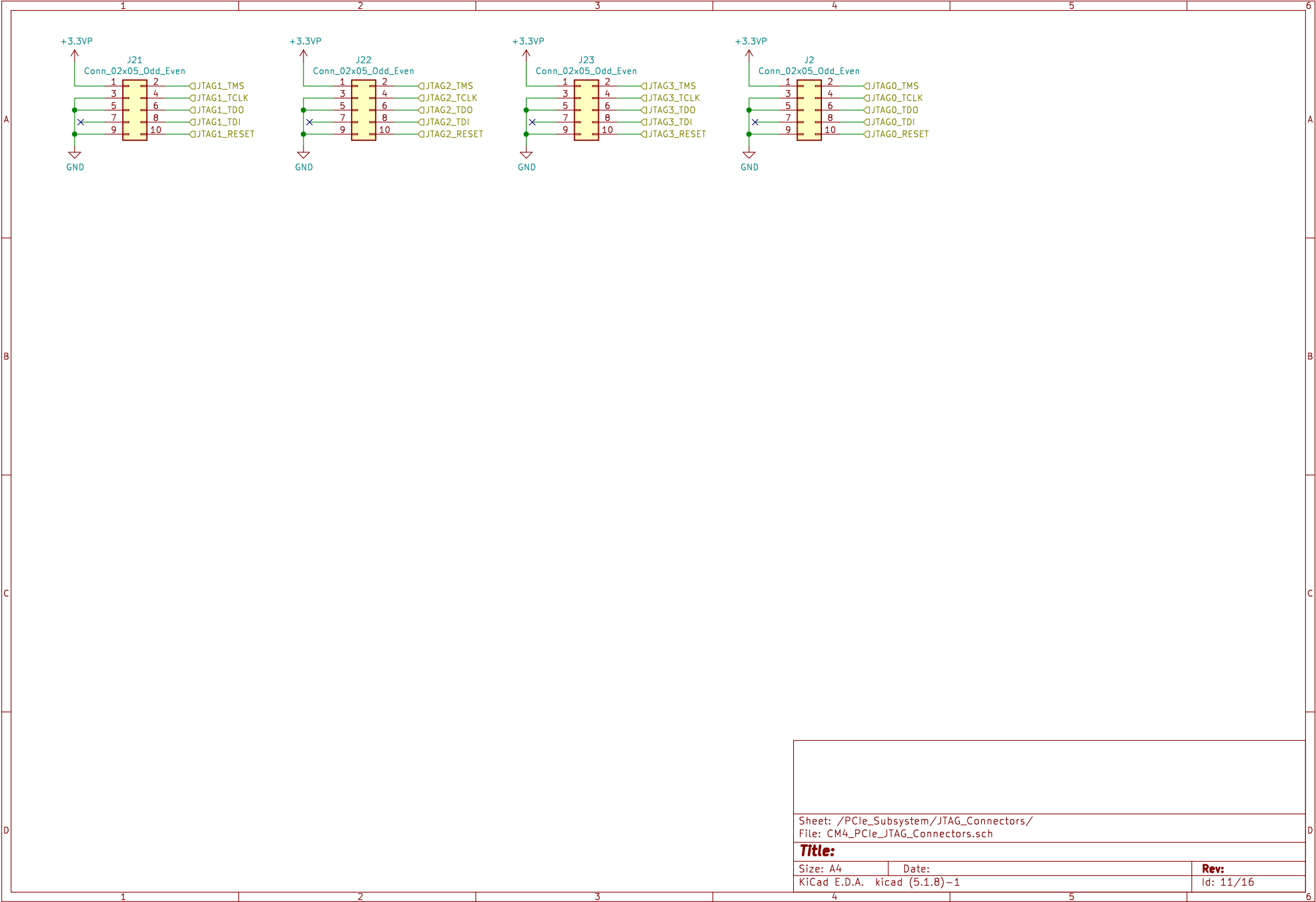
KiCad E.D.A. kicad (5.1.8)-1

Rev:

Id: 9/16



Sheet: /PCle_Subsystem/ File: PCle_Subsystem.sch		
Title:		
Size: A4	Date:	Rev:
KiCad E.D.A. kicad (5.1.8)-1		Id: 10/16



Sheet: /PCIe_Subsystem/JTAG_Connectors/
File: CM4_PCIe_JTAG_Connectors.sch

Title:

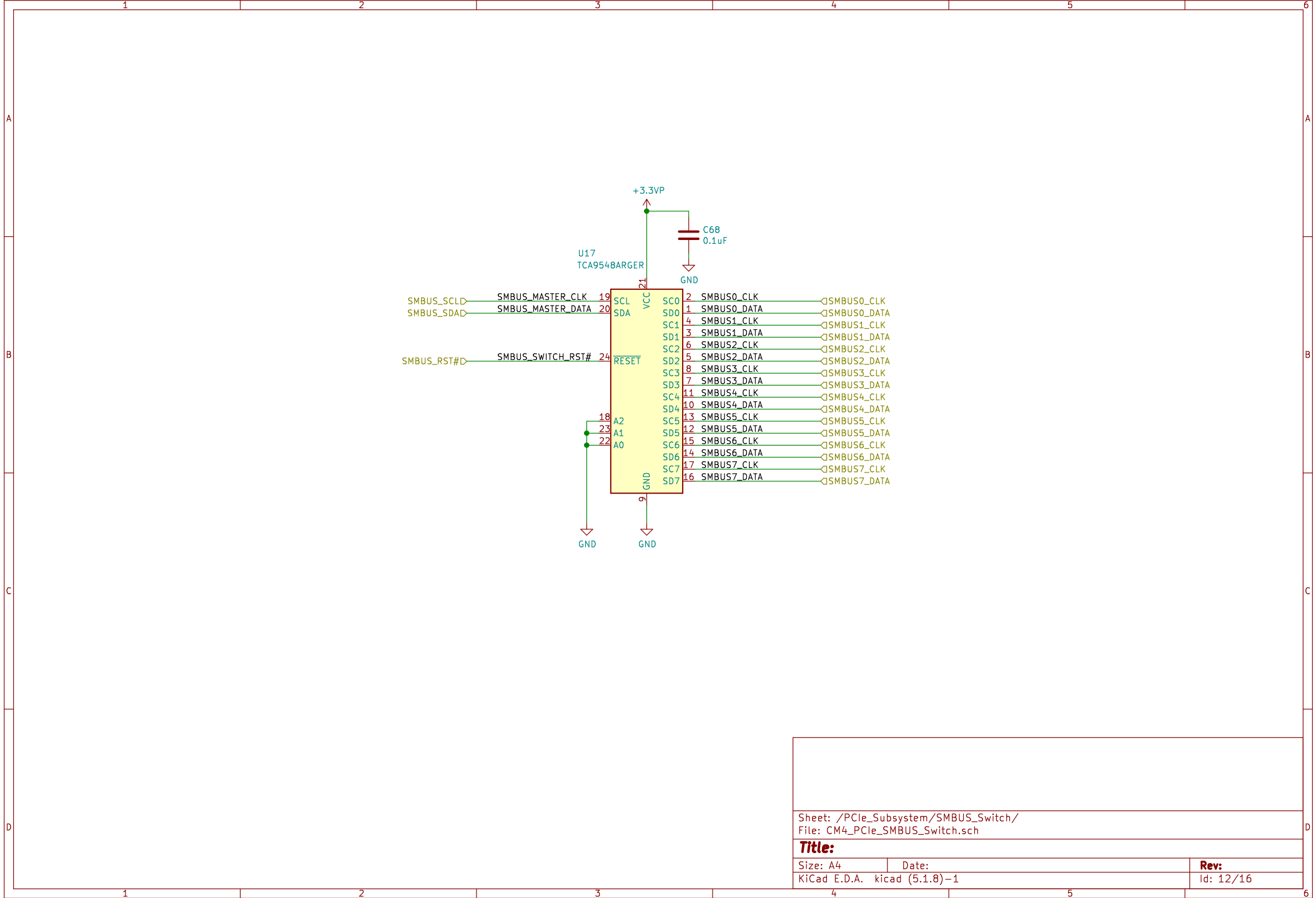
Size: A4

Date:

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Rev:

Id: 11/16



Sheet: /PCIe_Subsystem/SMBUS_Switch/
File: CM4_PCIe_SMBUS_Switch.sch

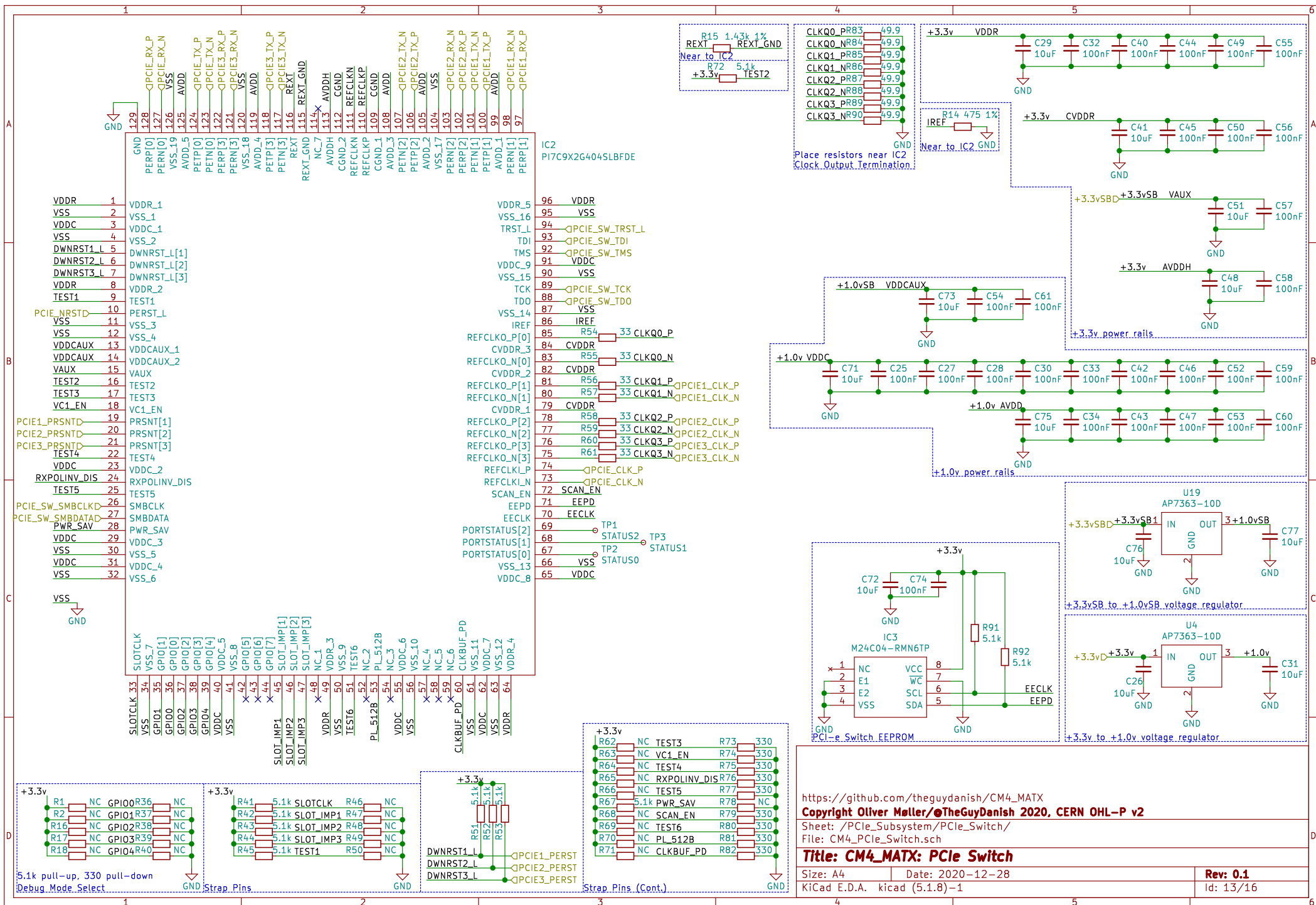
Title:

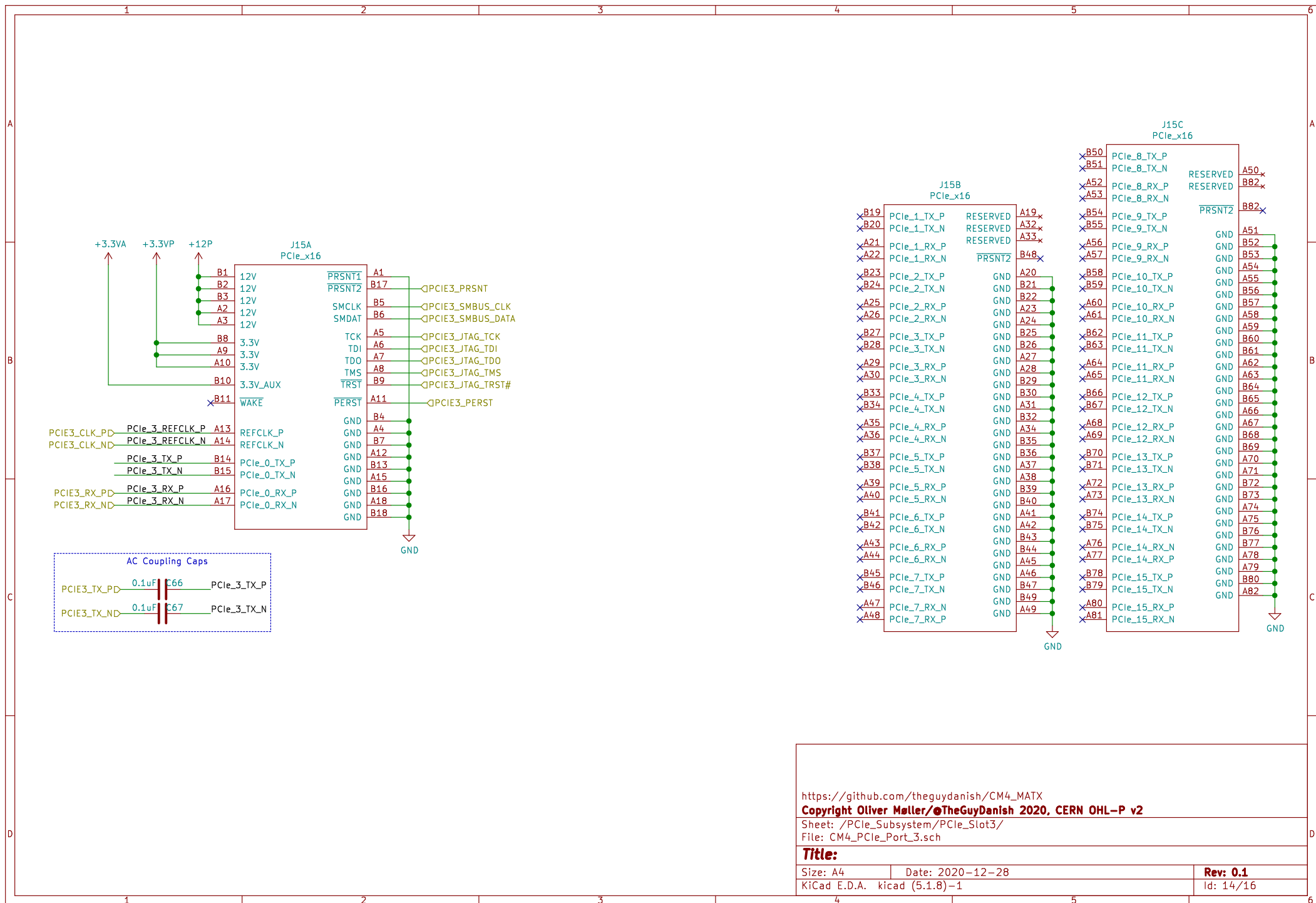
Size: A4
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Date:

Rev:

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Sheet: /PCle_Subsystem/PCle_Slot3/

File: CM4_PCLE_Port_3.sch

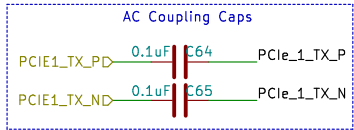
Title:

Size: A4 Date: 2020-12-28

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Sheet: /PCle_Subsystem/PCle_Slot1/

File: CM4_PCl_e_Port_1.sch

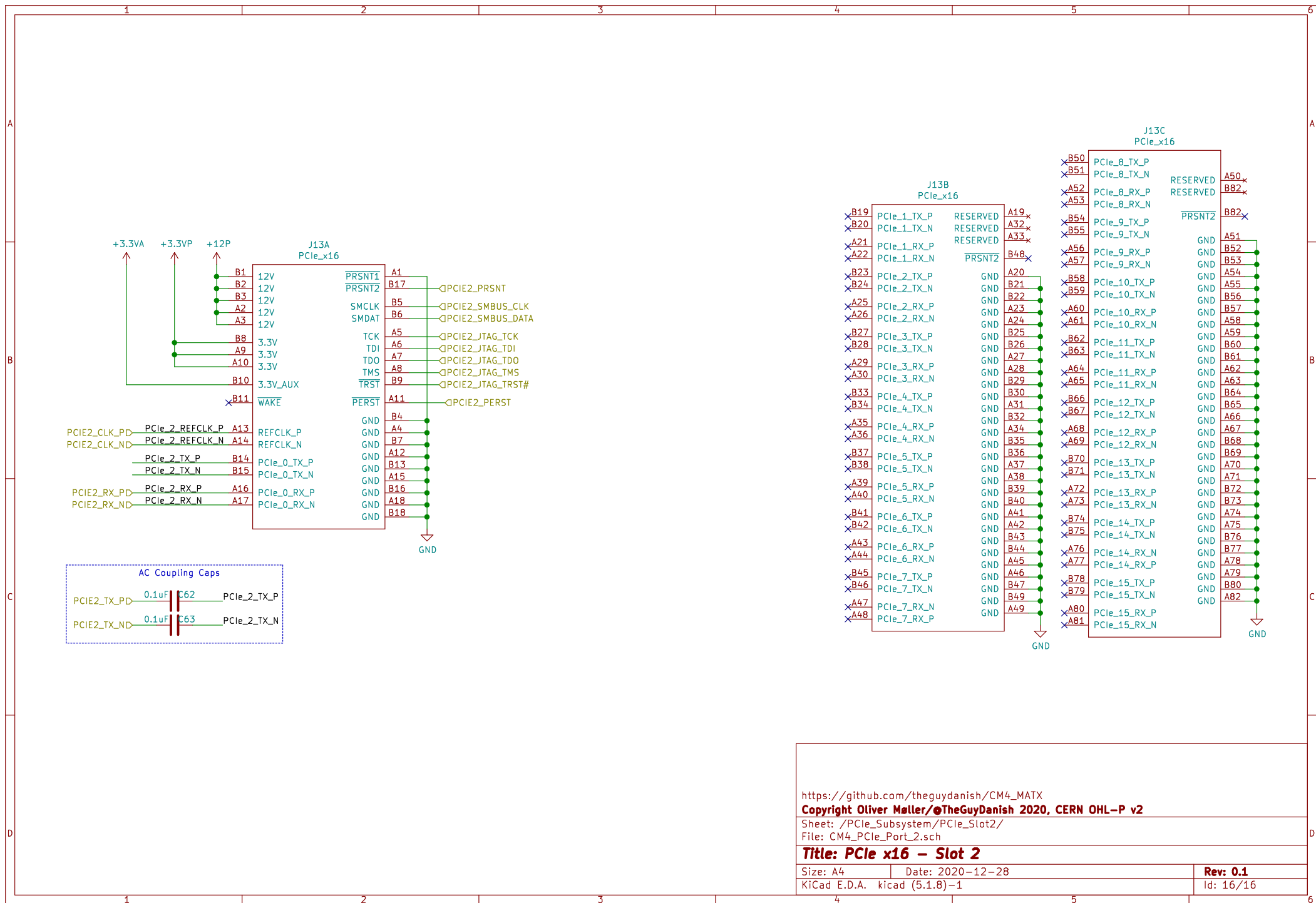
Title: PCle x16 – Slot 1

Size: A4 Date: 2020-12-28

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Rev: 0.1

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Sheet: /PCie_Subsystem/PCie_Slot2/

File: CM4_PClie_Port_2.sch

Title: PCIe x16 – Slot 2

Size: A4 Date: 2020-12-28

KiCad E.D.A. kicad (5.1.8)-1

Rev: 0.1

Id: 16/16