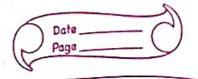
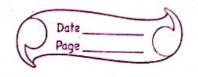
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	Himanshu Dixit
	B11 . Date
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	FS-II (Assignment-2)
	Assignment 2)
0-1-	P = 1 - 1 -= 1
¥ .	9714n 1-6x10-19 x1017 x 800
	= 0.078 = 0.08_acm.
0.2.	Clipper Circuits are the Circuits that clip off or
	removes a portion of an input signal, without
-	causing my distribution distortion to the remaining
	part of the wave form.
pi v —	Clippers are hasically wave-shaping Circuits that
N 25	Control the Shape of an output waveform.
	dippers circuit are basically teamed as protection devices
120	
	Classification of Clipper Circuit
} →	Positive Aipper arait
	Negative Clipper Circuit
	The clippex circuit that is inteded to attenuate positive
	partions of the input signal can be termed as a positive
	clipper.
	We have following types:
•	Positive Sevies Velipper :-
	positive duies clipper with positive Vr (Reference Voltage)
	positive series clippes with negetive Vy
	Positive shunt dipper :-
:-	positive shunt clipper conth positive Vr
	positive shunt clipper with negative Vx
	The dipper circuit that is intended to attenuate negative
	postions of the input signal can be toomed as
	negative clipper.
	0



	Negative Series Clipper:
	No since copper
_	Megative series clippes with positive VY
	Negative desires no negative Vr
	Negative shunt clipper:
	Negative shunt clipper with positive Vr
	on my with negative VY
	U U
	Clamper circuit:
	A Clamper circuit can be defined as the Circuit that consist
	of a diade a resistor & a apacitor that shifts the
	Waveforms to a desired DC level without changing the
	actual appearance of the applied signal
	In a clamper circuit, a vertical suft of upward or
	downward takes place in the output waveform.
	Takes place in the source
	There are less than of Clampon Christ
	There are few types of Clamper Circuit.
	positive clamper with positive Vs
_	n n negative Vy
•	Negative clamper
	Negative clamper with positive Vy
_	
	n n hegasius Vy
0.2	The Configuration in which the all a
9-57	The Configuration in which the emitter is Connected
	between the Collector & base is known as a Common
	emitter configuration (CE)
→	Input characteristic: The variation of emitter current (ID)
	Was keeping collector Emitter
_	Value (Vac) constant
	$Rin = \Delta V_{8c}$
	DIB Vee = constant
ll ll	



-	output characteristics. The variation of collector Circuit (Ic)
	with Collector- Emittex Voltage (VCG) Keeping the base Corrent
	(Ig) Constant
	Rout = DVCE
	AIc Ip = contont
	In CB Configuration, the Base terminal of the transistor will
-	be connected common between the output and the input
	terminals (CB)
	Input characteristics: The Variation of emitter (unent (Ic)
	with Base - Smitter Voltage (Vpc), keeping collector base
-	voltage (VB) Constant
	$R_{in} = \Delta V_{AE}$ ΔI_{E} $V_{CR} = Constant$
-	VCR = CONSTANT Of Collector (T)
	Output characteristics: The variation of Collector Current (Ic)
	with collector - Base voltage (VCB). Keeping the emitter (worent
	(TE) Constant.
	Rout = AVCB
	ΔI_B $I_C = constant$
	a la la control
	In CE configuration, the Collector terminal of the transistor
	will be connected common between the output & the input
	terminal.
<u>→</u>	Input characteristics. The Variation of emitter Current (IB)
9	with Collector - Buse Voltage (VCB), Keeping Collector Base
	voltage (Ver) constant
->	Desput characteristics: The Variation of emitter Current (Ie)
	with collector - Emitter voltage (VCE), Keeping the base
,	Current (In) constant.
	CA NOW CHANGE
-	

