	start = 1	start = 0	rst = 1	rst = 0
RESET	START	START	RESET	-
START	READ1	START	RESET	-
READ1	READ0	-	RESET	-
READ0	READADDR	MEMWORK	RESET	-
READADDR	READADDR	MEMWORK	RESET	-
MEMREAD	MEMWRITE	MEMWRITE	RESET	-
MEMWRITE	DONE	DONE	RESET	-
DONE	START	START	RESET	-